

SIC XE 2-pass C++ Assembler

◆ Team members:

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◆ Requirements specifications

- implementing a 2-pass assembler for SIC XE Assembly, written in C/C++, producing code for the absolute loader used in the SIC programming assignments.
 - **Specifications**
 - supports about **59** SIC XE architecture **instructions**.
 - supports about **14** SIC XE architecture **pseudo-instructions** including [*EQU*, *ORG*, *LTORG*]
 - supports about **10** registers.
 - is executed through *assemble.cpp*.
 - is to execute by entering “*assemble <source-file-name>*” in OS shell.
 - supports *simple expression evaluation* including operands [*+*, *-*, ***, */*]
 - supports **Literals** (Including *LTORG*) in the forms
[*=C'<ASCII-TEXT>'*, *=X'HEX-TEXT'*] [**Bonus Feature**]
 - supports *Control sections* [**Bonus Feature**]
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- **Design**
- **9 C/C++ code files**
 - “*assemble.cpp*” [contains **main function** only]
 - “*SICXEPass1.cpp*” with header file “*SICXEPass1.h*” [performs **Pass1 algorithm**]
 - Input: *input.txt*
 - Output: *Intermediate_File.txt*
 - *Listing File Array* will be implemented as following:
 - listingFile[0]* -> **Line Number**
 - listingFile[1]* -> **Location Counter**
 - listingFile[2]* -> **Label _If found_**
 - listingFile[3]* -> **Operation Code**
 - listingFile[4]* -> **Flags**
 - listingFile[5]* -> **Operands**
 - listingFile[6]* -> **Comments _If found_**
 - listingFile[7]* -> **Object Code for pass2**
 - listingFile[8]* -> **Modification Boolean for pass2**
 - vector<string> *starts*; [address of **start of each control section**]
 - vector<string> *ends*; [address of **end of each control section**]
 - bool *pass1()*; [performs **pass1 algorithm** stated by Leland L. Beck]
 - void *generateIntermediateFile()*; [write to **O/P file**]
 - string *evaluateExpression*(string expression);
 - bool *isComment*(string line);
 - int *verifyLine*(string line); [checks **validity of code** statements]
 - vector<string> *split*(string s); [**parses** code statements **recursively**]
 - bool *isLiteralFoundListingFile*(int lineNum, string literal);
 - vector<string> *splitOperators*(string expression);
 - “*SICXEPass2.cpp*” with its header file “*SICXEPass2.h*” [**pass2 implementation**]
 - Input: **ListingFile** array generated by *SICXEPass1.cpp*
 - Output: **Intermediate_File.txt, Object.o**
 - void *getObjectCode*(int line); [**generates Object Code** of each statement]
 - void *setFlags*(string operand,int line); [sets **n,i, x, b, p, e** instruction bits]
 - bool *pass2()*; [performs **pass2 algorithm** stated by Leland L. Beck]
 - void *flushRecord*(); [writes to **O/P file**]
 - bool *headerRecord*(string programName, string startAdd, string endAdd); [generates **H records**]
 - bool *flushTextRecord*(); [writes **T record** to **O/P file**]
 - bool *appendTextRecord*(string locationCounter, string objectCode, bool executable); [generates **T records**]

- `bool endRecord();` [generates **E records**]
 - `bool modificationRecord(string address, string length, string details);`
[generates **M records**]
 - `bool addExternalDefinition(string name, string address);`
[generates **D records**]
 - `bool addExternalReference(string name);` [generates **R record**]
 - “*Tables.cpp*” associated with “*Tables.h*” [contains **main data structures**]
 - “*Conversions.cpp*” associated with “*Conversions.h*”
[contains *ASCII-number-system* conversions]
 - supports *ASCII*, *Binary*, *Decimal* and *Hexadecimal* 2-way conversions.
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- **Main data structures**
 - Hash Table
 - ◆ **SYMTAB:** `map<string, vector<string> > symTab;`
 - ◆ **Registers:** `map<string, int> registersTable;`
 - ◆ **OPTAB:** `map<string, string> opTab;`
 - ◆ Set
 - ◆ **pseudo-instructions:** `set<string> directivesSet;`
 - ◆ 2-dimensional array
 - ◆ **LITTAB :** `string LITTAB[1000][4];`
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◆ Algorithms description

[O(N), N is the count of ASCII characters in I/O file]

Pass1

```
begin
  read first input line
  if OPCODE = 'START' then
    begin
      save #[OPERAND] as starting address
      initialize LOCCTR to starting address
      write line to intermediate file
      read next input line
    end (if START)
  else
    initialize LOCCTR to 0
    while OPCODE ≠ 'END' do
      begin
        if this is not a comment line then
          begin
            if there is a symbol in the LABEL field then
              begin
                search SYMTAB for LABEL
                if found then
                  set error flag (duplicate symbol)
                else
                  insert (LABEL,LOCCTR) into SYMTAB
                end (if symbol)
              end
            search OPTAB for OPCODE
            if found then
              add 3 (instruction length) to LOCCTR
            else if OPCODE = 'WORD' then
              add 3 to LOCCTR
            else if OPCODE = 'RESW' then
              add 3 * #[OPERAND] to LOCCTR
            else if OPCODE = 'RESD' then
              add #[OPERAND] to LOCCTR
            else if OPCODE = 'BYTE' then
              begin
                find length of constant in bytes
                add length to LOCCTR
              end (if BYTE)
            else
              set error flag (invalid operation code)
            end (if not a comment)
          end
          write line to intermediate file
          read next input line
        end (while not END)
      end
      write last line to intermediate file
      save (LOCCTR - starting address) as program length
    end (Pass 1)
```

pass2

```
begin
  read first input line (from intermediate file)
  if OPCODE = 'START' then
    begin
      write listing line
      read next input line
    end (if START)
  write Header record to object program
  initialize first Text record
  while OPCODE ≠ 'END' do
    begin
      if this is not a comment line then
        begin
          search OPTAB for OPCODE
          if found then
            begin
              if there is a symbol in OPERAND field then
                begin
                  search SYMTAB for OPERAND
                  if found then
                    store symbol value as operand address
                  else
                    begin
                      store 0 as operand address
                      set error flag (undefined symbol)
                    end
                  end (if symbol)
                else
                  store 0 as operand address
                  assemble the object code instruction
                end (if opcode found)
              else if OPCODE = 'BYTE' or 'WORD' then
                convert constant to object code
              if object code will not fit into the current Text record then
                begin
                  write Text record to object program
                  initialize new Text record
                end
              add object code to Text record
            end (if not comment)
          write listing line
          read next input line
        end (while not END)
      write last Text record to object program
      write End record to object program
      write last listing line
    end (Pass 2)
```

◆ **Assumptions**

- Free-formatted I/O.
 - Memory of target machine does not exceed **1 MB**.
 - Hexadecimal literals must fill completely one or multiple byte-blocks.
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◆ **TeamWord distribution**

- **Ramy Wagdy Sobhy**
 - Code statements structure verification [**pass1**]
 - generating object code for each code statement [**pass2**]
- **Rofael Emil Fayez Behnam**
 - Free-format recursive parsing [**pass1**]
 - generating Object File Records [**pass2**]
- **Remon Hanna Wadie Youssef**
 - generating SYMTAB [**pass1**]
 - parsing EXTREF and EXTDEF operands [**pass2**]
 - QT-creator GUI with syntax highlighting feature
- **Mohamed Ahmed Taher Mohamed Ahmed Elkholy**
 - pass2 main algorithm [**pass2**]
 - QT-creator GUI with auto-compeletion feature
- **Mohamed Abd ElRahman ElFeki**
 - pass1 main algorithm [pass1]
 - generating LITAB
 - construction OPTAB [pre-design]

◆ Sample Runs

File: /home/emil/Desktop/SICXEAssembler/input.txt

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```
1  .dadssa
2  COPY      START      0
3              EXTDEF    BUFFER,      BUFEND,LENGTH
4              EXTREF    RDREC,        WRREC
5  FIRST     STL        RETADR
6  CLOOP     +JSUB       RDREC
7              LDA        LENGTH,X      .hello World
8              COMP      #0
9              JEQ        ENDFIL
10             +JSUB      WRREC
11             J          CLOOP
12  ENDFIL    LDA        = C'E0          F'
13             STA        BUFFER
14             LDA        #      3
15             STA        LENGTH
16             +          JSUB      WRREC!
17             J          @      RETADR
18  RETADR    RESW        1
19  LENGTH    RESW        1
20             LTORG
21  BUFFER    RESB        4096
22  BUFEND    EQU         *
23  MAXLEN    EQU         BUFEND      -      BUFFER
24  RDREC     CSECT
25             EXTREF    BUFFER,LENGTH,BUFEND
26             CLEAR     X
27             CLEAR     A
28             CLEAR     S
29             +LDT       #MAXLEN2
30  RLOOP     TD          INPUT
31             JEQ        RLOOP
32             RD         INPUT
33             COMPR      A,S
34             JEQ        EXIT
35             +STCH      BUFFER,X
36             TIXR       T
37             JLT        RLOOP
38  EXIT      +STX        LENGTH
39             RSUB
40  INPUT     BYTE        X'F1'
41  MAXLEN2   WORD        BUFEND-BUFFER
42  WRREC     CSECT
43             EXTREF    LENGTH,BUFFER
44             CLEAR     X
45             +LDT       LENGTH
46  WLOOP     TD          =X'05'
47             JEQ        WLOOP
48             +LDCH      BUFFER,X
49             WD         =X'05'
50             TIXR       T
51             JLT        WLOOP
52             RSUB
53             END        FIRST
```

```

1  0      .dadssa
2  1      0000      COPY      START      0
3  2      0000      EXTDEF    BUFFER,BUFEND,LENGTH
4  3      EXTREF    RDREC,WRREC
5  4      0000      FIRST     STL      RETADR
6  5      0003      CLOOP     +JSUB    RDREC
7  6      0007      LDA      LENGTH,X      .hello World
8  7      000A      COMP     #0
9  8      000D      JEQ      ENDFIL
10 9      0010      +JSUB    WRREC
11 10     0014      J      CLOOP
12 11     0017      ENDFIL    LDA      =C'EO      F'
13 12     001A      STA      BUFFER
14 13     001D      LDA      #3
15 14     0020      STA      LENGTH
16 15     0023      +      JSUB    WRREC!
17 16     **** Error: Invalid Operand
18 17     0023      J      @RETADR
19 18     0026      RETADR    RESW      1
20 19     0029      LENGTH   RESW      1
21 20     LTORG
22 21     002C      *      =C'EO      F'
23 22     003A      BUFFER    RESB      4096
24 23     0103      BUFEND    EQU      *
25 24     0100      MAXLEN    EQU      BUFEND-BUFFER
26 25     0000      RDREC     CSECT
27 26     EXTREF    BUFFER,LENGTH,BUFEND
28 27     0000      CLEAR     X
29 28     0002      CLEAR     A
30 29     0004      CLEAR     S
31 30     0006      +LDT      #MAXLEN2
32 31     000A      RLOOP     TD      INPUT
33 32     000D      JEQ      RLOOP
34 33     0010      RD      INPUT
35 34     0013      COMPR     A,S
36 35     0015      JEQ      EXIT
37 36     0018      +STCH     BUFFER,X
38 37     001C      TIXR      T
39 38     001E      JLT      RLOOP
40 39     0021      EXIT      +STX      LENGTH
41 40     0025      RSUB
42 41     0028      INPUT     BYTE      X'F1'
43 42     0029      MAXLEN2   WORD      BUFEND-BUFFER
44 43     0000      WRREC     CSECT
45 44     EXTREF    LENGTH,BUFFER
46 45     0000      CLEAR     X
47 46     0002      +LDT      LENGTH
48 47     0006      WLOOP     TD      =X'05'
49 48     0009      JEQ      WLOOP
50 49     000C      +LDCH     BUFFER,X
51 50     0010      WD      =X'05'
52 51     0013      TIXR      T
53 52     0015      JLT      WLOOP
54 53     0018      RSUB
55 54     001B      END      FIRST
56 55     001B      *      =X'05'
57
58
59      Literals TABLE
60      *****
61      *      Name      Value      Length      Address*
62      =C'EO      F'      454F000000000000000000000000000046      14      002C
63      =X'05'      05      2      001B
64      *****
65      *****
66
67

```



```

51 50      0010      WD      =X'05'
52 51      0013      TIXR      T
53 52      0015      JLT      WLOOP
54 53      0018      RSUB
55 54      001B      END      FIRST
56 55      001B      *      =X'05'
57
58
59          Literals TABLE
60 *****
61 *      Name      Value      Length      Address*
62      =C'E0      F'      454F00000000000000000000000046      14      002C
63      =X'05'      05      2      001B
64 *****
65 *****
66
67          SYMBOL TABLE
68 *****
69 *****
70 *      SYMBOL      *      ADDRESS      *Section Number
71 *****
72 *BUFEND      |      103A      | 0      *
73 *BUFFER      |      003A      | 0      *
74 *CLOOP      |      0003      | 0      *
75 *COPY      |      0000      | 0      *
76 *ENDFIL      |      0017      | 0      *
77 *EXIT      |      0021      | 1      *
78 *FIRST      |      0000      | 0      *
79 *INPUT      |      0028      | 1      *
80 *LENGTH      |      0029      | 0      *
81 *MAXLEN      |      103A      | 0      *
82 *MAXLEN2      |      0029      | 1      *
83 *RDREC      |      103A      | 1      *
84 *RETADR      |      0026      | 0      *
85 *RLOOP      |      000A      | 1      *
86 *WLOOP      |      0006      | 2      *
87 *WRREC      |      002C      | 2      *
88 *****
89 #End of pass 1 for SIC/XE Assembler#
90 Line      LocCtr      Labels      OpCode      Flags      Operands      Comments      ObjectCode      ModBoolean
91 0
92 1      0000      COPY      START      0
93 2      0000      EXTDEF      BUFFER,BUFEND,LENGTH      -----
94 3      EXTREF      RDREC,WRREC
95 4      0000      FIRST      STL      110010      RETADR      172023
96 5      0003      CLOOP      +JSUB      110001      RDREC      4B10103A
97 6      0007      LDA      111010      LENGTH,X      .hello World      03A01F
98 7      000A      COMP      011000      #0      298000
99 8      000D      JEQ      110010      ENDFIL      332007
100 9      0010      +JSUB      110001      WRREC      4B10002C

101 10      0014      J      110010      CLOOP      3F2FEC
102 11      0017      ENDFIL      LDA      110010      =C'E0      F'      032012
103 12      001A      STA      110010      BUFFER      0F201D
104 13      001D      LDA      010000      #3      010003
105 14      0020      STA      110010      LENGTH      0F2006
106 15      0023      +      JSUB      WRREC!
107 16      **** Error: Invalid Operand
108 17      0023      J      100010      @RETADR      3E2000
109 18      0026      RETADR      RESW      1      -----
110 19      0029      LENGTH      RESW      1      -----
111 20      LTORG      110000      ---
112 21      002C      *      =C'E0      F'
113 22      003A      BUFFER      RESB      4096      -----
114 23      0103      BUFEND      EQU      *      -----
115 24      0100      MAXLEN      EQU      BUFEND-BUFFER      -----
116 25      0000      RDREC      CSECT      110000      ---
117 26      EXTREF      BUFFER,LENGTH,BUFEND      -----
118 27      0000      CLEAR      X      B410
119 28      0002      CLEAR      A      B400
120 29      0004      CLEAR      S      B440
121 30      0006      +LDT      010001      #MAXLEN2      75100029
122 31      000A      RLOOP      TD      110010      INPUT      E3201B
123 32      000D      JEQ      110010      RLOOP      332FFA
124 33      0010      RD      110010      INPUT      DB2015
125 34      0013      COMPR      A,S      A004
126 35      0015      JEQ      110010      EXIT      332009
127 36      0018      +STCH      111001      BUFFER,X      5790003A
128 37      001C      TIXR      T      B850
129 38      001E      JLT      110010      RLOOP      3B2FE9
130 39      0021      EXIT      +STX      110001      LENGTH      13100029
131 40      0025      RSUB      110000
132 41      0028      INPUT      BYTE      X'F1'      F1
133 42      0029      MAXLEN2      WORD      BUFEND-BUFFER      -----
134 43      0000      WRREC      CSECT      110000      ---
135 44      EXTREF      LENGTH,BUFFER      -----
136 45      0000      CLEAR      X      B410
137 46      0002      +LDT      110001      LENGTH      77100029
138 47      0006      WLOOP      TD      110010      =X'05'      E32012
139 48      0009      JEQ      110010      WLOOP      332FFA
140 49      000C      +LDCH      111001      BUFFER,X      5390003A
141 50      0010      WD      110010      =X'05'      DF2008
142 51      0013      TIXR      T      B850
143 52      0015      JLT      110010      WLOOP      3B2FEE
144 53      0018      RSUB      110000      4F0000
145 54      001B      END      FIRST      -----
146 55      001B      *      =X'05'      000005

```

Object.o

File: /home/emil/Desktop/SICXEAssembler/OBJFILE.o

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```
1  HCOPY ^000000^00103A
2  T000000^1D^1720234B10103A03A01F2980003320074B10002C3F2FEC0320120F201D
3  T00001D^0C^0100030100030F20063E2000
4  M000004^05+RDREC
5  M000011^05+WRREC
6  E000000
7
8
9
10 HRDREC ^000000^00002C
11 DBUFFER00003ABUFEND00103ALENGTH000029
12 RRDREC WRREC
13 T000000^1E^B410B400B44075100029E3201B332FFADB2015A0043320095790003AB850
14 T00001E^0E^3B2FE93B2FE9131000294F0000F1
15 M000007^05
16 M000019^05+BUFFER
17 M000022^05+LENGTH
18 M000029^06+BUFEND
19 M000029^06-BUFFER
20 E000000
21
22
23
24 HWRREC ^000000^00001B
25 RBUFFERLENGTHBUFEND
26 T000000^1B^B41077100029E32012332FFA5390003ADF2008B8503B2FEE4F0000
27 M000003^05+LENGTH
28 M00000D^05+BUFFER
29 E000000
```