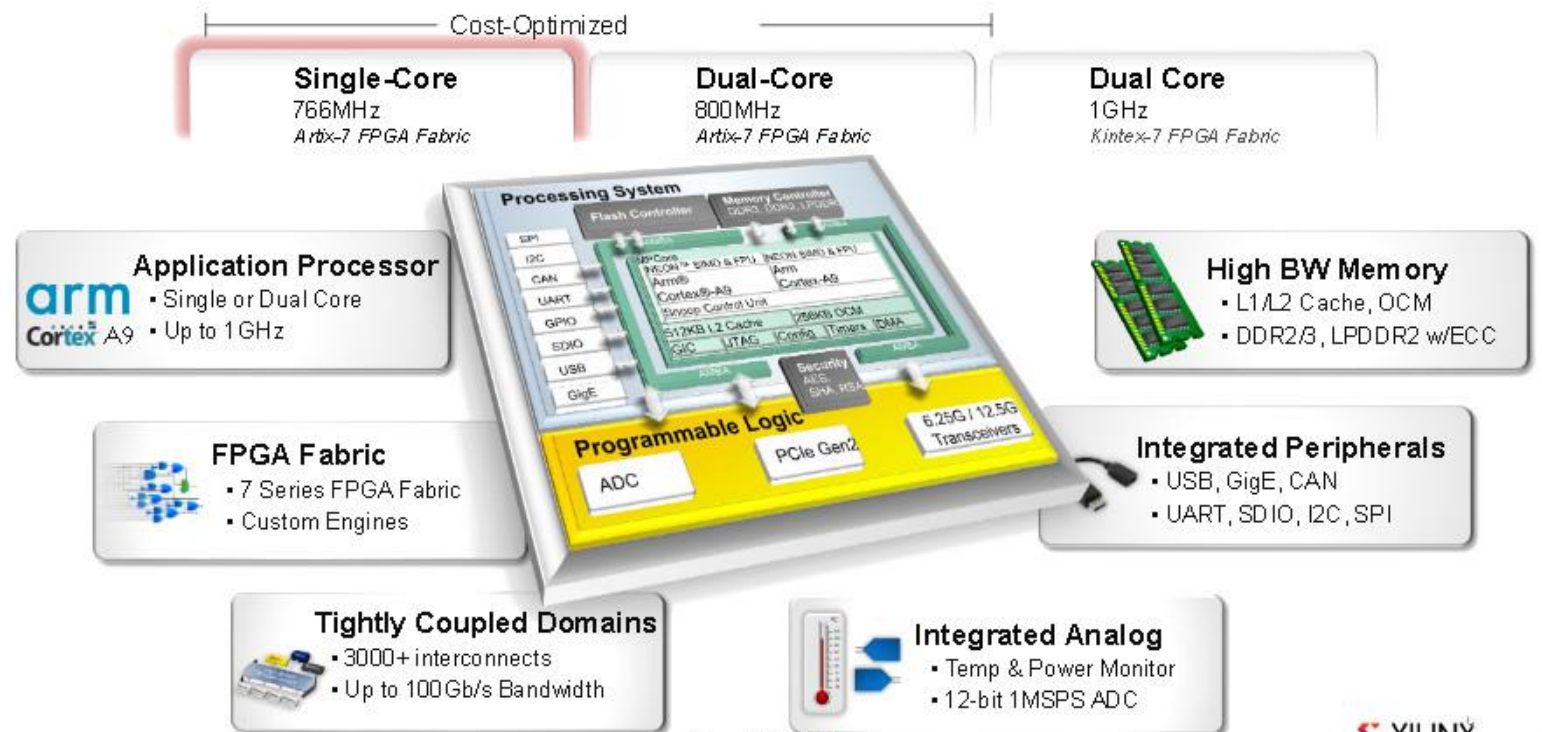
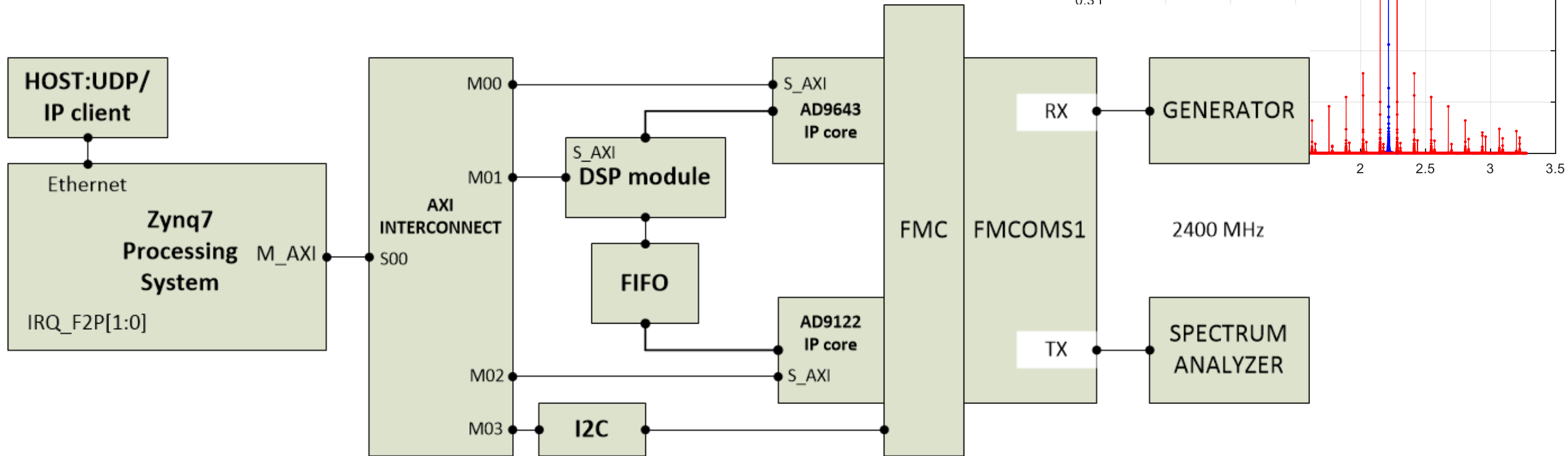


Zynq 7000: digital signal processing with FMCOMMS1 module

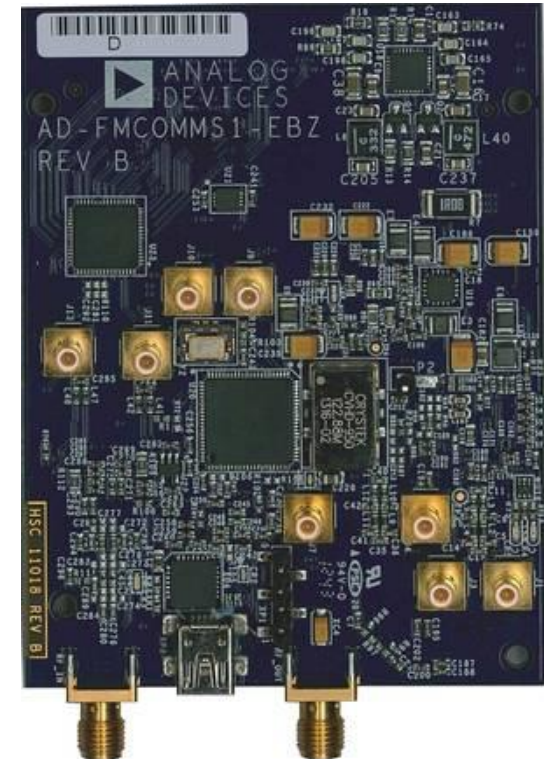
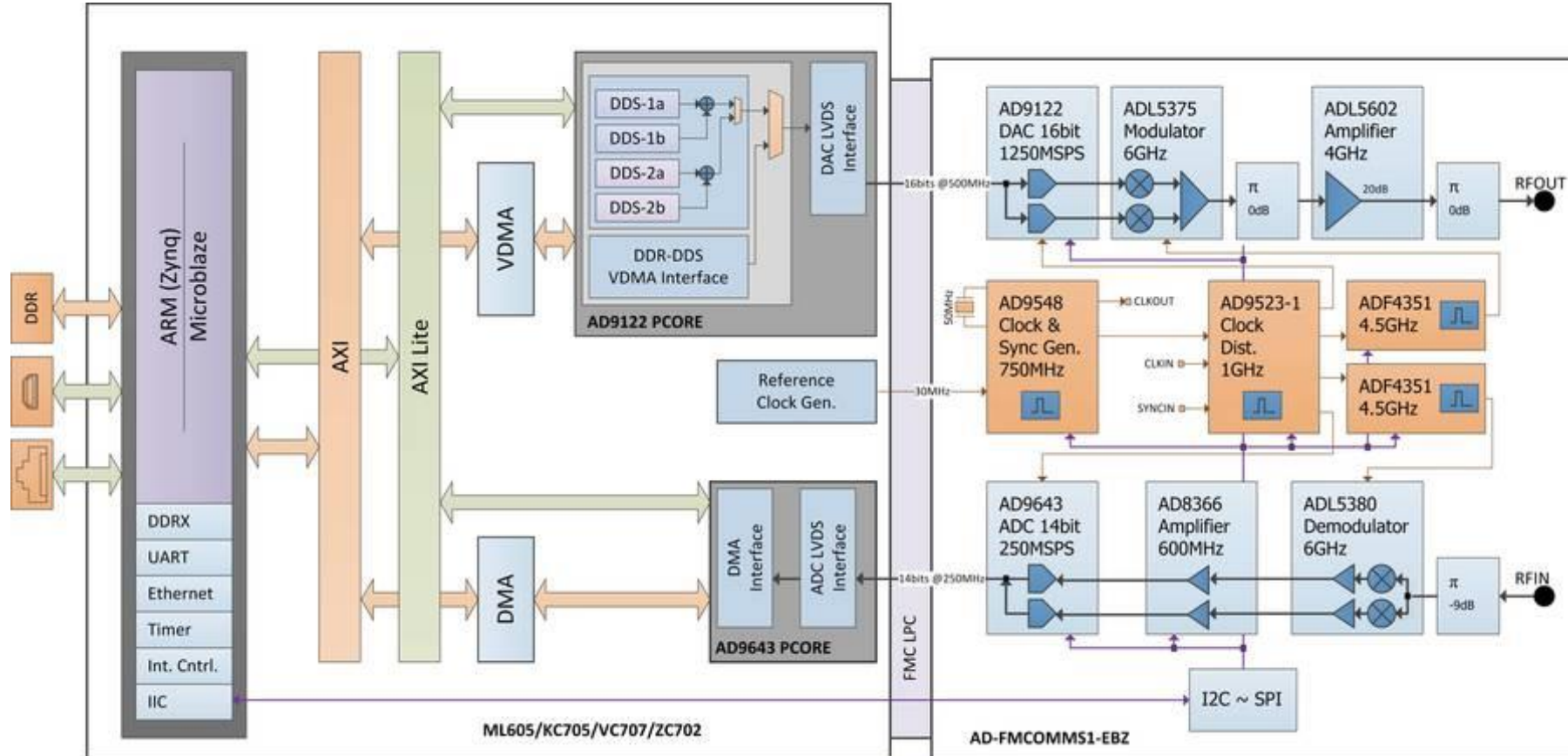


Aleksei Rostov, PhD,
Senior R&D Engineer,
FPGA/Embedded Linux Developer,
aleksei.rostov@protonmail.com

HARDWARE DESIGN



FMCOMMS1 functional scheme



FMCOMMS1 THIRD-PARTY API

