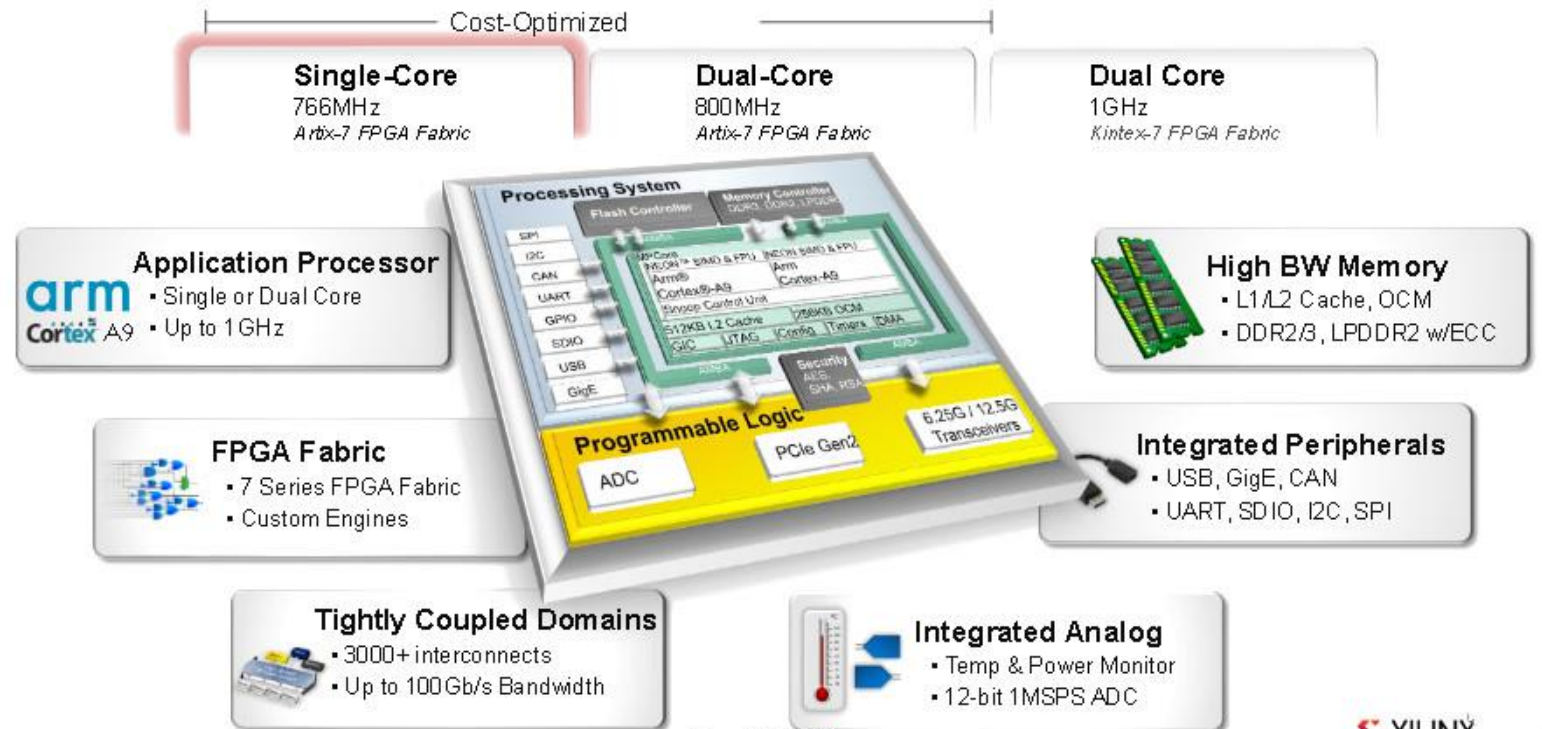
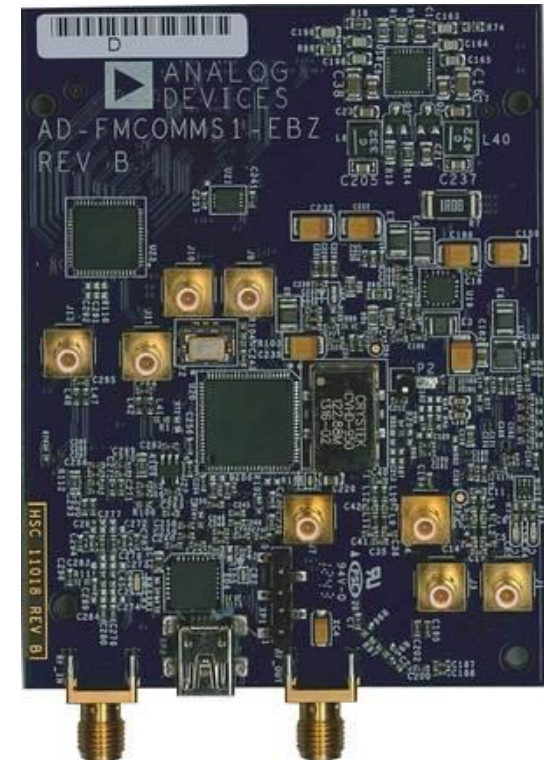
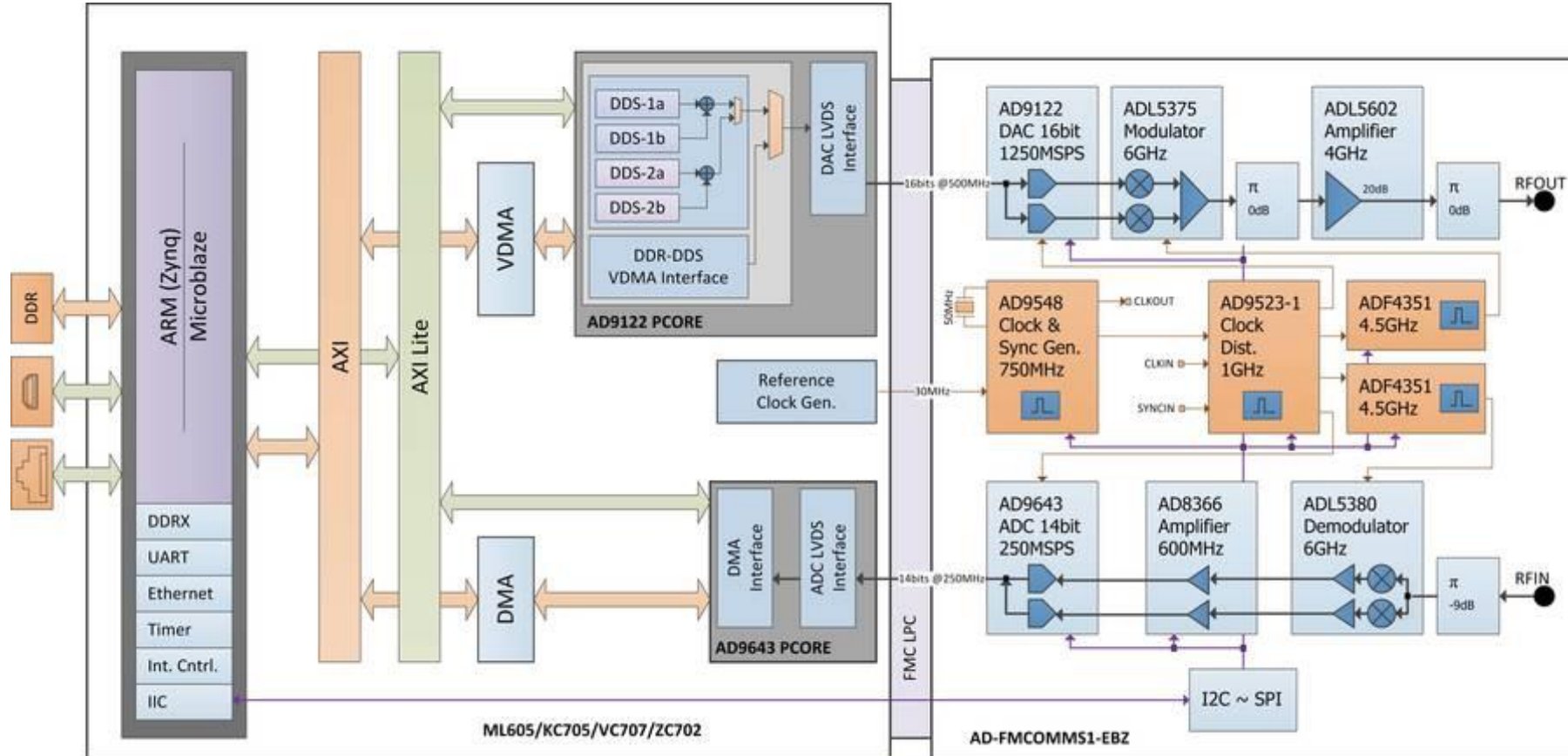


Zynq 7000: Analog Devices FMCOMMS1 module integration

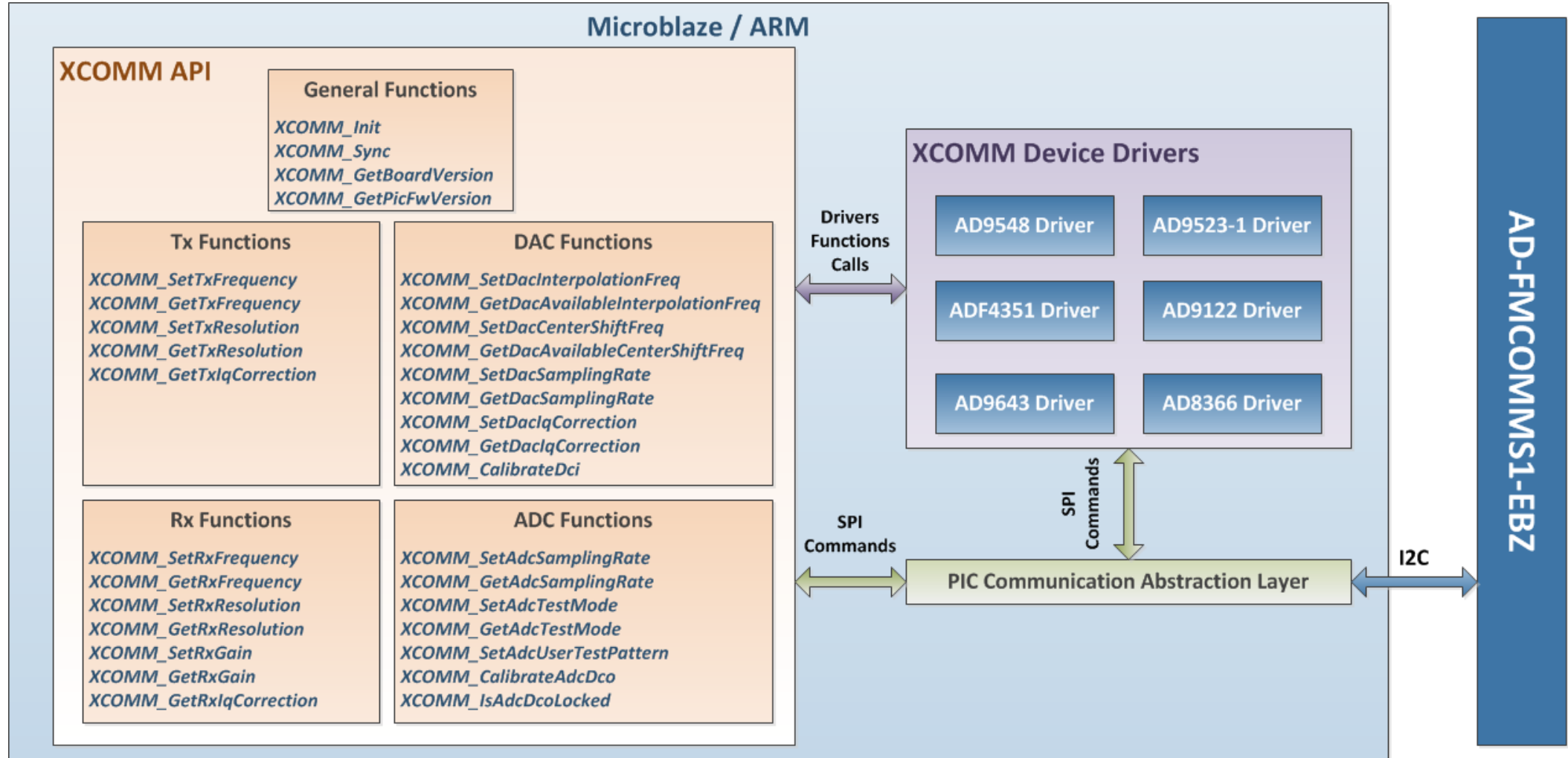


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FMCOMMS1 functional scheme



FMCOMMS1 THIRD-PARTY API



HARDWARE DESIGN

