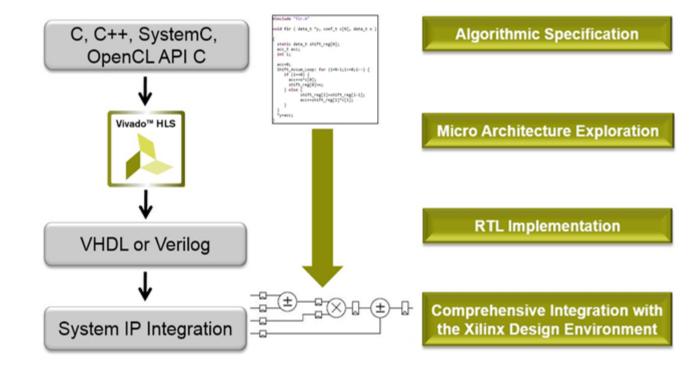
## Vivado HLS: up/down converters, IQ frequency and phase modulation

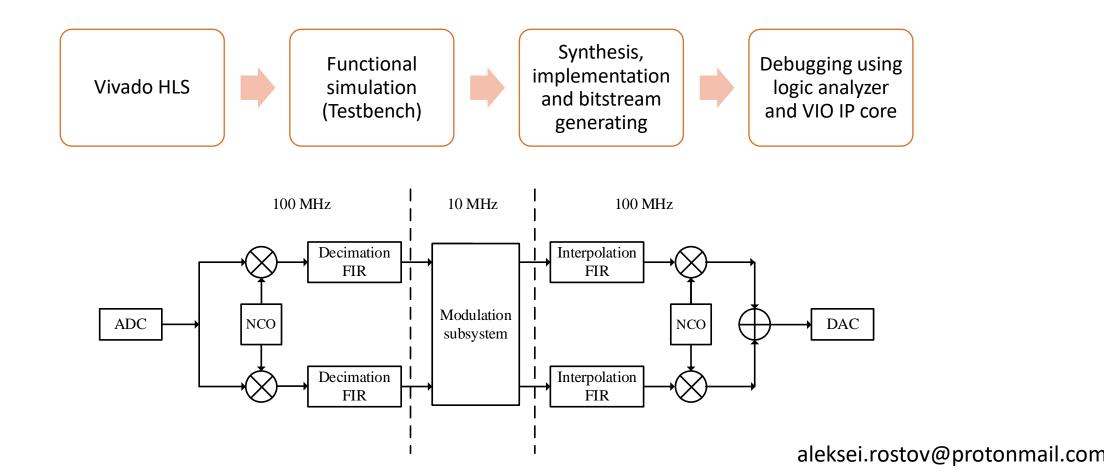
Aleksei Rostov, PhD, Senior R&D Engineer, FPGA/Embedded Linux Developer, aleksei.rostov@protonmail.com



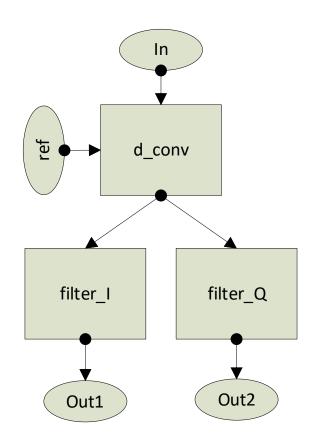
## **AGENTA**

- 1. Vivado HLS: up/down converter, frequency/phase modulator
- 2. HDL design and implementation (Verilog) of digital system

## **DESIGN FLOW**



Vivado HLS modules: simulation, optimization, IP exporting



In1 In2 filter I filter Q up\_conv Out1

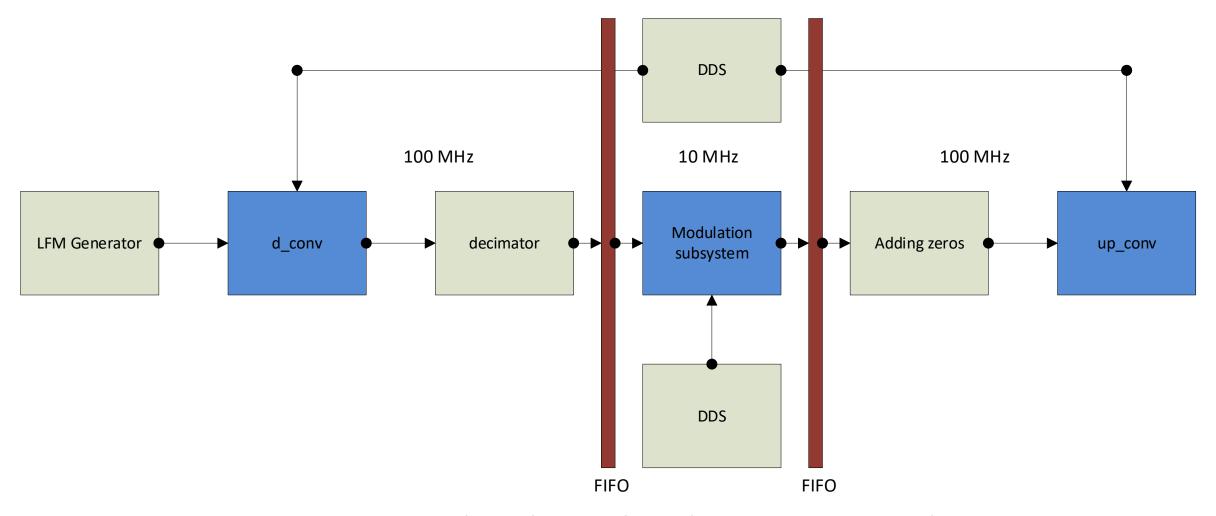
In2 In1 merging Phase, Frequency mux Out1

IQ demodulator

IQ modulator

Frequency, Phase modulator aleksei.rostov@protonmail.com

## Hardware design with Vivado HLS IP cores



Hardware design with Vivado HLS IP cores integrated