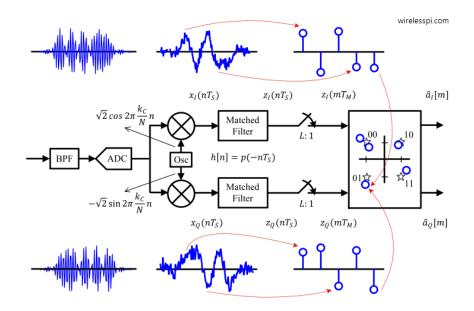
## Frequency and Phase modulation (HDL design)

Aleksei Rostov, PhD, Senior R&D Engineer, FPGA/Embedded Linux Developer, aleksei.rostov@protonmail.com



## **AGENTA**

- 1. Digital system MATLAB model with modulation subsystem
- 2. HDL design and implementation (Verilog) of digital system

