

Authors: Wojciech SP5WWP, Leo Hofer, Morgan ON4MOD

M17 Project

Sheet: /

File: OpenHT-RF.kicad_sch

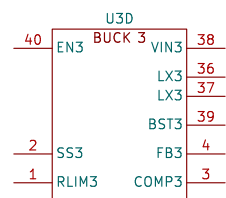
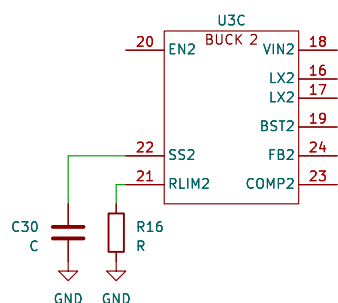
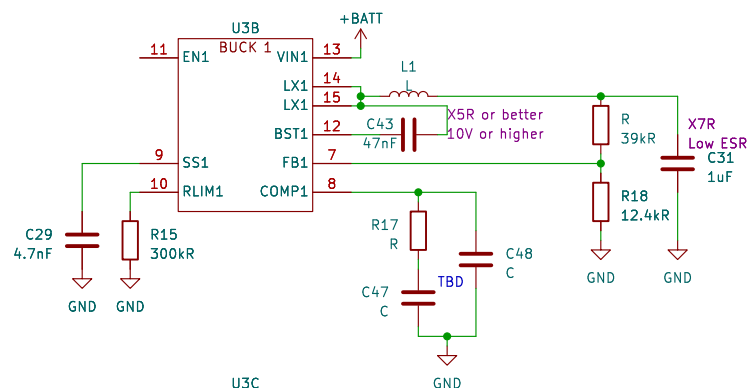
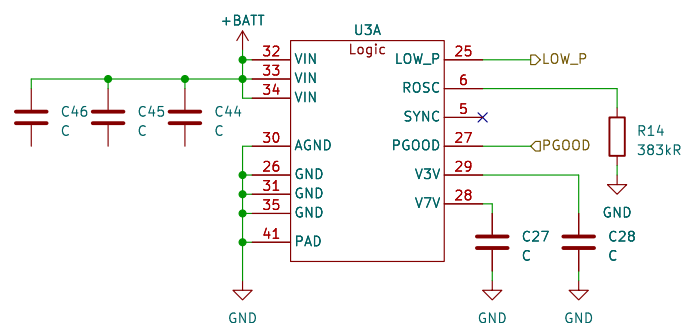
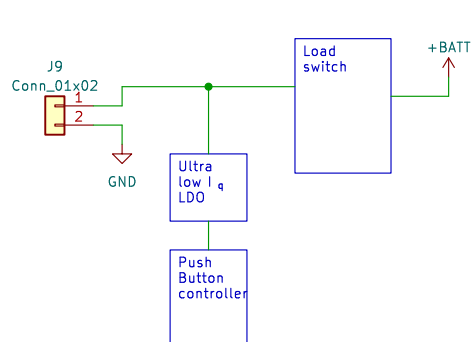
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SS capacitors:
4.7nF corresponds to a soft start of about 1ms.
This value can be changed if we want to reduce the number of different components in the BOM

Buck 1:
L: optimised for $V_{out}=3.3V$, $I_{out}=0.35A$, $f_{sw}=500kHz$
 C_{out} must be $> 0.56\mu F$ (optimised for $dV=30mV$)
 R_{lim} : set to max value (correspond to 1.2A)

Buck 2 has higher low-load efficiency and should thus be selected to provide the rail with the lesser load.

1.8V and 1.0V rails feed the FPGA. See with SP5WWP for the details on FPGA's current consumption

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Sheet: /Power supply/

File: pwr.kicad_sch

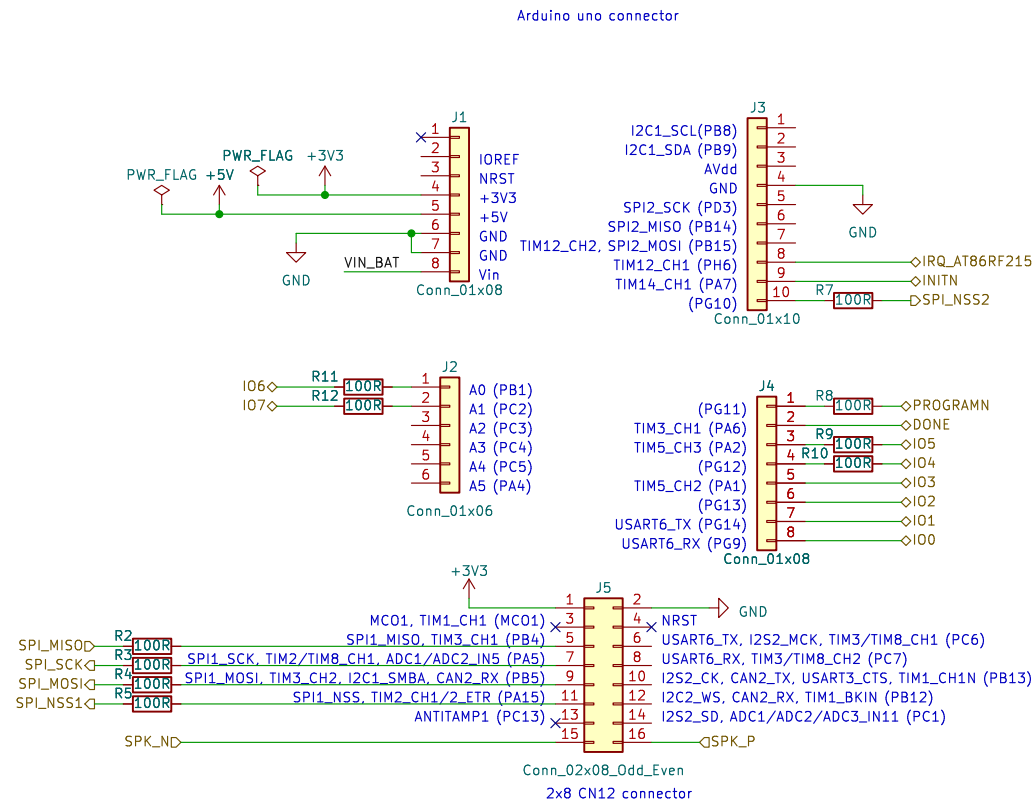
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Sheet: /Connectors/

File: conn.kicad_sch

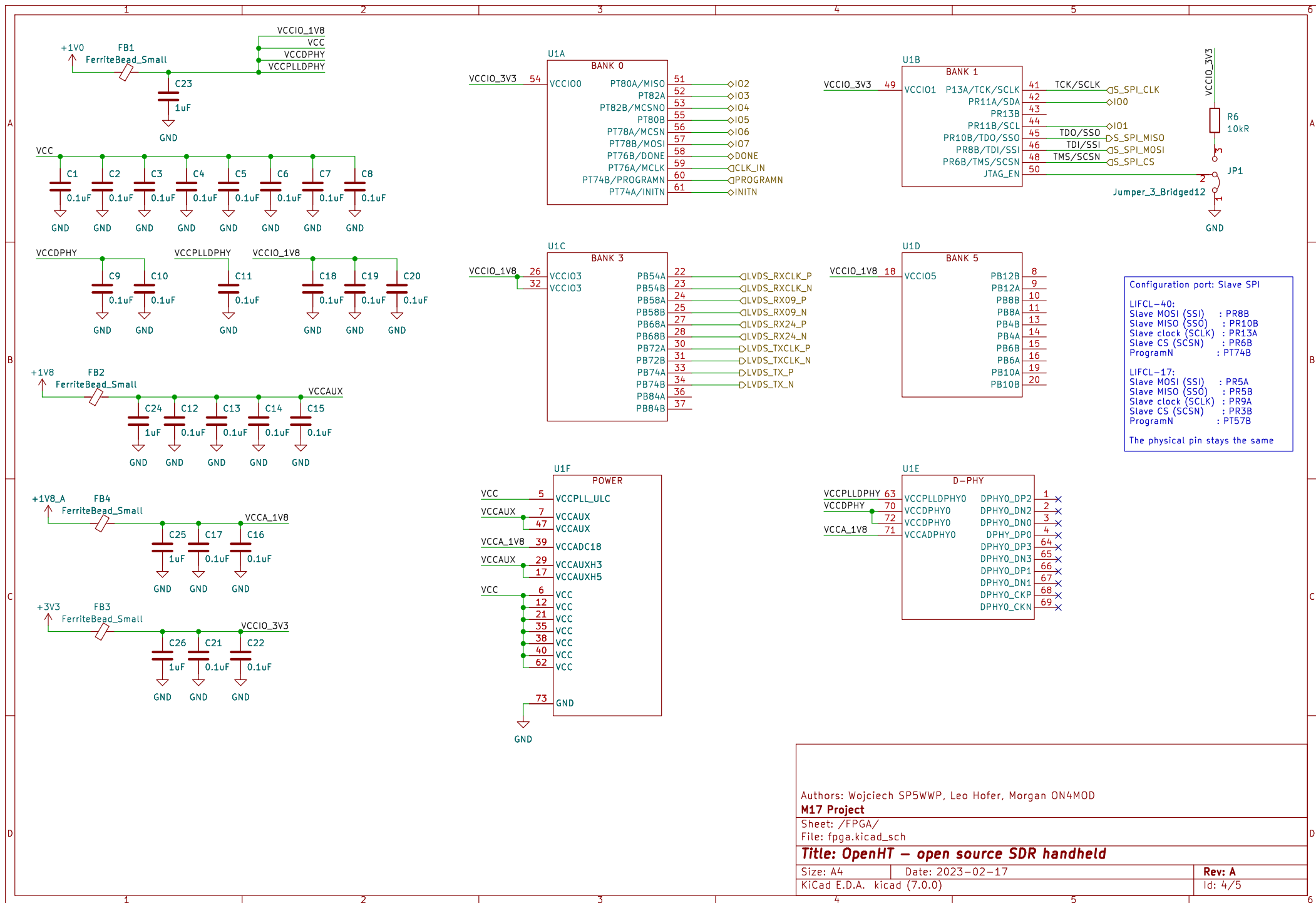
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Sheet: /FPGA/

File: fpga.kicad_sch

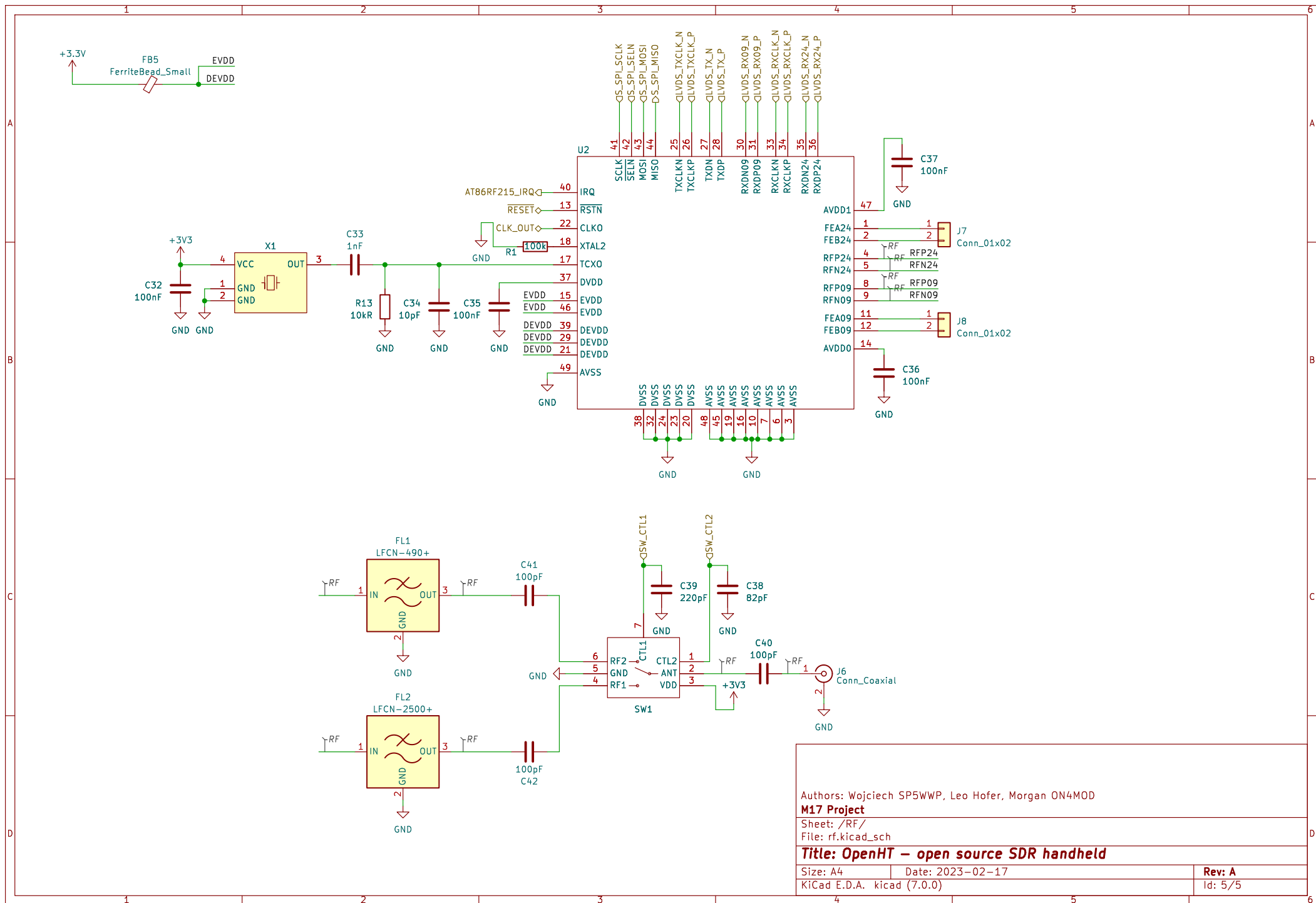
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Sheet: /RF/

File: rf.kicad_sch

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