Power supply Connectors MCU\_NRST< >PWR\_RESET PG00DQ DPG00D PB\_INT< DPB\_INT MAIN\_KILL MAIN\_KILL File: pwr.kicad\_sch ⊃SPK\_P RF\_RST< LS101 ⇒SPK\_N SW\_CTL2< SW\_CTL1< IRQ\_AT86RF215♦ FPGA ⇔RESET 1000 0010 SW\_CTL2 R101 101♦ ♦101 SW\_CTL1 1kR 102◊ ♦102 CAT86RF215\_IRQ 103♦ ≎103 1040 \$104 CLK\_INC CLK\_OUT <u></u> C101 105♦ ♦105 TLVDS SW101 LVDS\_TXCLK\_PD >LVDS\_TXCLK\_P 1uF 106♦ \$106 **YLVDS** 436351045816 LVDS\_TXCLK\_ND DLVDS\_TXCLK\_N ♦FPGA\_RST FPGA\_RSTO TLVDS LVDS\_TX\_PD DLVDS\_TX\_P TLVDS INITNO OINITN LVDS\_TX\_ND DLVDS\_TX\_N DONE♦ **◇DONE** TLVDS LVDS\_RXCLK\_P< GND GND OLVDS\_RXCLK\_P PROGRAMND >PROGRAMN TLVDS LVDS\_RXCLK\_N< ULVDS\_RXCLK\_N TLVDS SPI\_MISO OSIM\_IPR\_SD LVDS\_RX09\_P< JLVDS\_RX09\_P **YLVDS** SPI\_MOSID S\_SPI\_MOSI LVDS\_RX09\_NC ULVDS\_RX09\_N TLVDS SPI\_SCKD S\_SPI\_CLK LVDS\_RX24\_PC ULVDS\_RX24\_P **YLVDS** SPI\_NSS1D S\_SPI\_CS LVDS\_RX24\_N< JLVDS\_RX24\_N SPI\_NSS2D File: fpga.kicad\_sch S\_SPI\_MISO ⇒S\_SPI\_MOSI File: conn.kicad\_sch ⇒S\_SPI\_SCLK S\_SPI\_SELN File: rf.kicad\_sch Authors: Wojciech SP5WWP, Leo Hofer, Morgan ON4MOD M17 Project Sheet: / File: OpenHT-RF.kicad\_sch Title: OpenHT - open source SDR handheld Size: A4 Date: 2023-02-17 Rev: A KiCad E.D.A. kicad 7.0.1 ld: 1/5







