Power supply Connectors MCU_NRST< >PWR_RESET PG00Dd DPG00D PB_INT< ⇒PB_INT MAIN_KILL MAIN_KILL File: pwr.kicad_sch SPK_P RF_RST< LS101 SPK_N SW_CTL2 SW_CTL1< IRQ_AT86RF215¢ FPGA ⇔RESET Ø100 1000 >SW_CTL2 I01¢ ♦101 SW_CTL1 1kR \$I02 102♦ AT86RF215_IRQ 103♦ \$103 ¢CLK_OUT 1040 \$104 CLK_IN< **-** C101 105♦ ♦105 TLVDS SW101 LVDS_TXCLK_PD >LVDS_TXCLK_P 1uF 106♦ \$106 **YLVDS** 436351045816 LVDS_TXCLK_ND DLVDS_TXCLK_N 107♦ \$I07 TLVDS LVDS_TX_PD DLVDS_TX_P LVDS INITNO ♦INITN LVDS_TX_ND DLVDS_TX_N DONE DONE **YLVDS** GND GND LVDS_RXCLK_P LVDS_RXCLK_P PROGRAMND PROGRAMN TLVDS LVDS_RXCLK_N< LVDS_RXCLK_N TLVDS SPI_MISO< S_SPI_MISO LVDS_RX09_P< JLVDS_RX09_P **YLVDS** SPI_MOSID S_SPI_MOSI LVDS_RX09_N< ULVDS_RX09_N TLVDS SPI_SCKD S_SPI_CLK LVDS_RX24_P< JLVDS_RX24_P TLVDS SPI_NSS1D S_SPI_CS LVDS_RX24_N< ULVDS_RX24_N SPI_NSS2D File: fpga.kicad_sch S_SPI_MISO S_SPI_MOSI File: conn.kicad_sch S_SPI_SCLK S_SPI_SELN File: rf.kicad_sch Authors: Wojciech SP5WWP, Leo Hofer, Morgan ON4MOD M17 Project Sheet: / File: OpenHT-RF.kicad_sch Title: OpenHT - open source SDR handheld Size: A4 Date: 2023-02-17 Rev: A KiCad E.D.A. kicad (7.0.0) ld: 1/5







