Power supply Connectors MCU_NRST< >PWR_RESET PG00DQ DPG00D PB_INT< DPB_INT MAIN_KILL MAIN_KILL File: pwr.kicad_sch ⊃SPK_P RF_RST< LS101 ⇒SPK_N SW_CTL2< SW_CTL1< IRQ_AT86RF215♦ FPGA ⇔RESET 1000 0010 SW_CTL2 R101 101♦ ♦101 SW_CTL1 1kR 102◊ ♦102 CAT86RF215_IRQ 103♦ ≎103 1040 \$104 CLK_INC CLK_OUT <u></u> C101 105♦ ♦105 TLVDS SW101 LVDS_TXCLK_PD >LVDS_TXCLK_P 1uF 106♦ \$106 **YLVDS** 436351045816 LVDS_TXCLK_ND DLVDS_TXCLK_N ♦FPGA_RST FPGA_RSTO TLVDS LVDS_TX_PD DLVDS_TX_P TLVDS INITNO OINITN LVDS_TX_ND DLVDS_TX_N DONE♦ **◇DONE** TLVDS LVDS_RXCLK_P< GND GND OLVDS_RXCLK_P PROGRAMND >PROGRAMN TLVDS LVDS_RXCLK_N< ULVDS_RXCLK_N TLVDS SPI_MISO OSIM_IPR_SD LVDS_RX09_P< JLVDS_RX09_P **YLVDS** SPI_MOSID S_SPI_MOSI LVDS_RX09_NC ULVDS_RX09_N TLVDS SPI_SCKD S_SPI_CLK LVDS_RX24_PC ULVDS_RX24_P **YLVDS** SPI_NSS1D S_SPI_CS LVDS_RX24_N< JLVDS_RX24_N SPI_NSS2D File: fpga.kicad_sch S_SPI_MISO ⇒S_SPI_MOSI File: conn.kicad_sch ⇒S_SPI_SCLK S_SPI_SELN File: rf.kicad_sch Authors: Wojciech SP5WWP, Leo Hofer, Morgan ON4MOD M17 Project Sheet: / File: OpenHT-RF.kicad_sch Title: OpenHT - open source SDR handheld Size: A4 Date: 2023-02-17 Rev: A KiCad E.D.A. kicad 7.0.1 ld: 1/5







