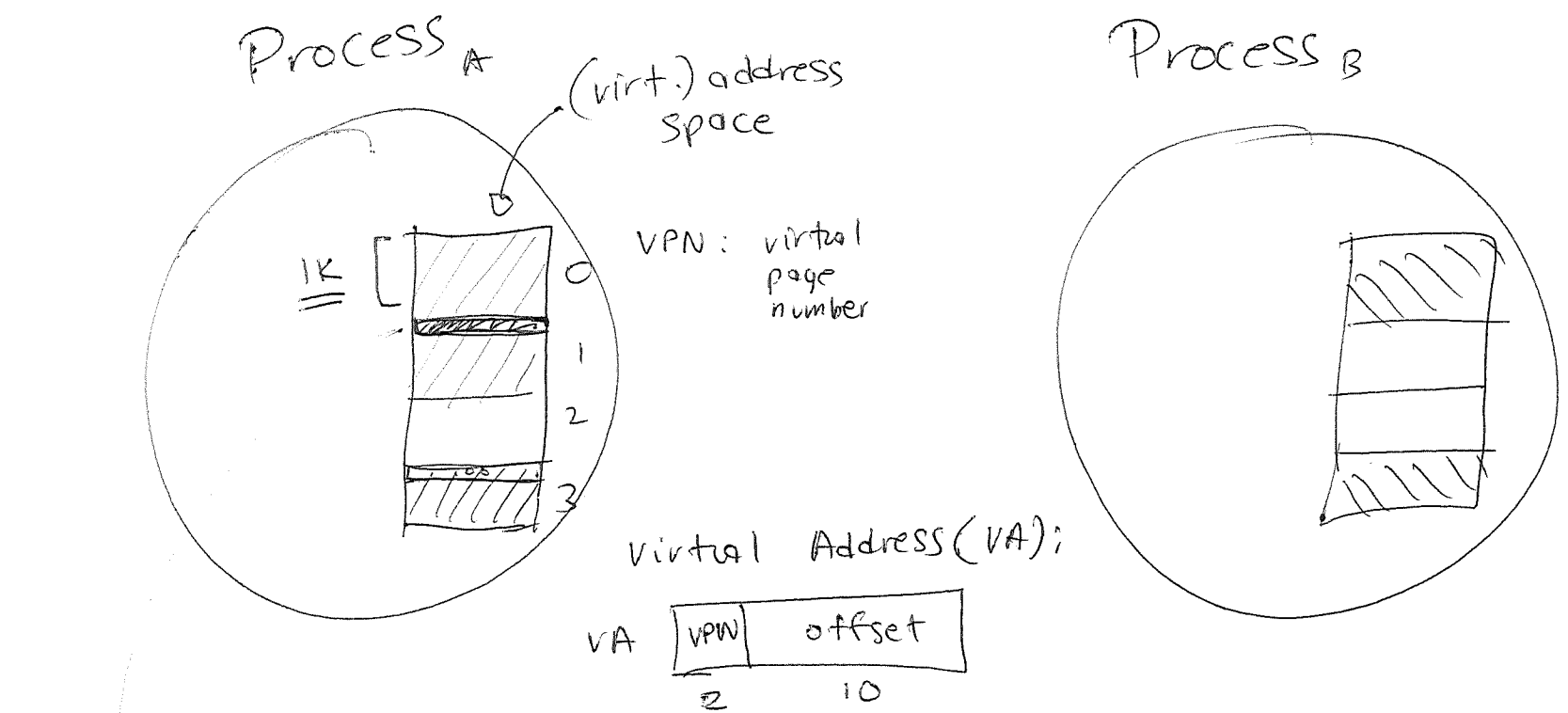
LECTURE 6:

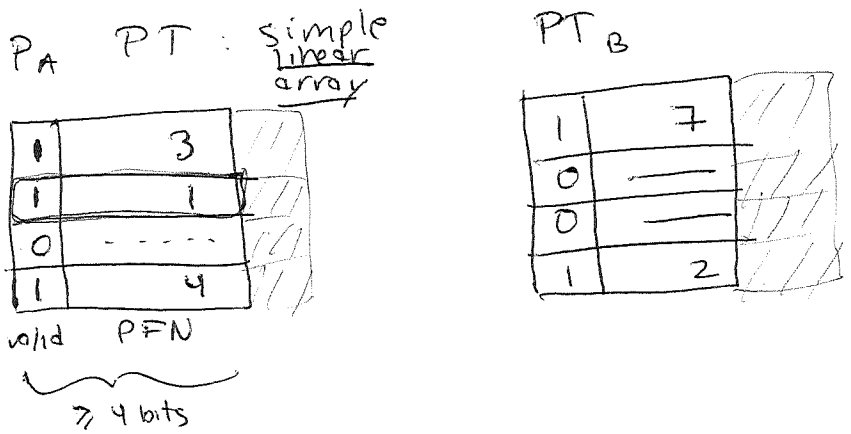
Detailed Review Example (Paging)



load 0x000, R1
(3072)

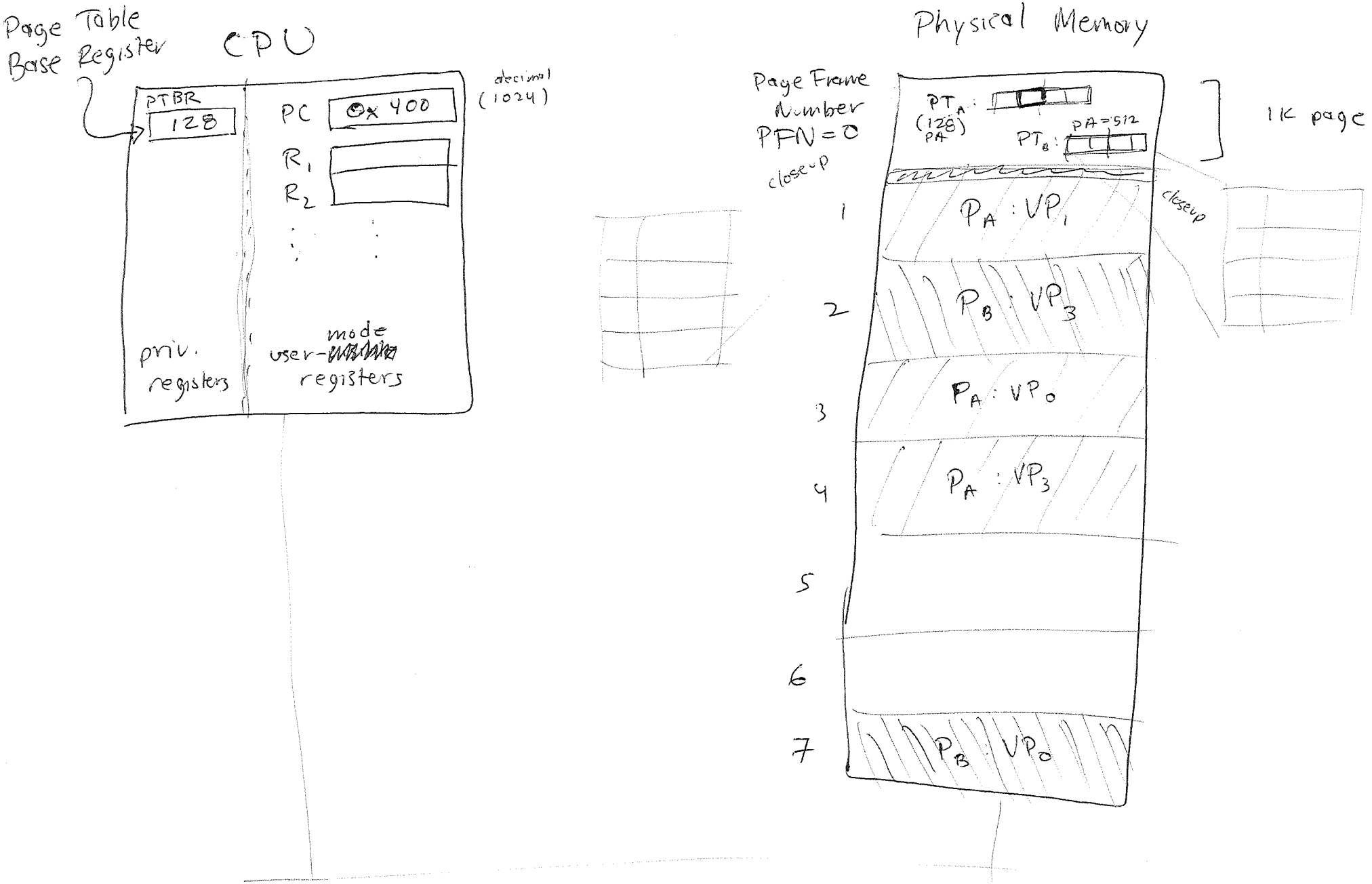
Q1) But where are the page tables?

Q2) what is in page table?



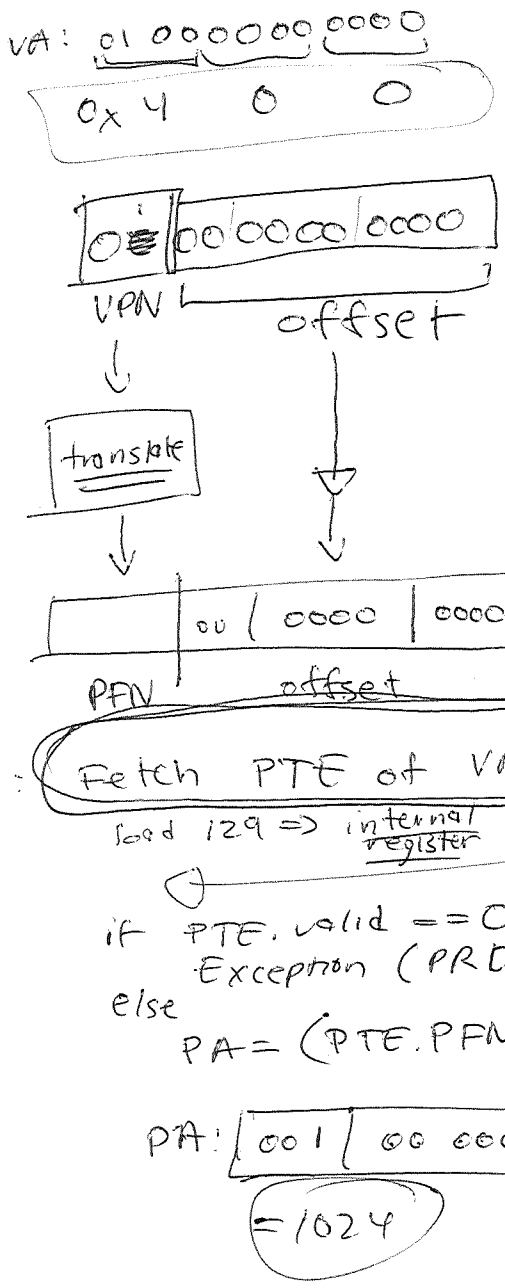
Q3) Execute an instruction

11 00 0600 0000



Thus: Phys. Addr (PA) has 13 bits

H/W: Fetch instruction @ VA = 1024



PTBR : 128 + (VPN * sizeof(PTE))

\+ = 129

PTE | v=1 | PFN = 1

Now: Fetch inst @ PA 1024

=> load 0x000, R1