Lecture 3: Scheduling

Last time: CPU virtualization [tall '11] More Real workload: interactive: short CPU bursts, tren long (niceled) Mechanisms: Limited Direct Execution long running CDU norkloads more Real Scheduler: @ boot : setup trap handlers can't assume knowledge of jobs =) Os involved on sys calls (privilege) protect then run jobs: =) what to do? HISTORY 3 05 involved on timer interrupts (switch) MLFQ: basic idea: assure new job is interactive, result: efficient, but w/ control Learn if this is trailnot over time! [OS as manager] start u/ RR, but many queues: (easier to unterstand) Today: Policies High Pri based on notion of time sharing Q2 - AB ASsume: simplistic Q, -> C =) set of jobs, all arrive @ once Qo > D, E each job has a given =) each just uses CPU (no I/o) =) each runs for same length of time (may change) Base =) time is known & priori Rules! ) if R(A) > P(B) => A runs =) one metric: tornaround time 2) if P(A) == P(B) => RR (A,B) Key: pri changer defiltcomplete Tarrive Algorithm #1: FIFO / FCFS (queue) over the : how? A, B, C get greved up , each run 10 time 3) start jobs @ nighest god! Attempt #7: TAVE 4) MANT 10 issued before  $T_{A} = 10$   $T_{B} = 20$   $T_{c} = 30$   $T_{AVG} = 20$ a) time slice is up=) (e.g. 10) Stap @ some Pr But, Relax #3: jobs of different lengths a) how does FIFO do? convoy: C stock behind B b) it not, move down one revel TAVE - 180 1 01 (EXAMPLES) 10. Algorithm #2: SJF long running batch, late arriving interchie =optimal (given our assumptions) Problems? 1) Starration, But, Relax #2: not all at some time -) problem? I) seve the (A:10, B:100 arrive @ T=0; C:10 arrives@T=20) Scheduler Attempt #2: solve Algorithm#3: STCF (allows pre-emption) Staruation But, Relax # 5: other methes: Response Time = Trist run Tarrive) Attempt #3: Algorithm #4: RR (oncept (time slice) solve gaming timer interript every X ms; gline slice is nox (n>1) Also: time slice But: What about I/O? length (interactive jobs use short bursts 事RAVG=? of CPU) TAVE = > (just intuition)