Name	Port/IO	H1	Port/IO	Name	Legend	Name	Port/IO	H2	Port/IO	Name	
GPIO0		1 2		GPIO1	PRI	SGPIO0		1	2	SGPIO1	Add: TPs and Port/Pin
GPIO2		3 4		GPIO3	SEC	SGPIO2		3	4	SGPIO3	
RESERVED		5 6		RESERVED	SHARE	I2C3_SDA		5	6	I2C3_SCL	
GPIO4		7 8		GPIO5	AUX	SGPIO4		7	8	PDM 1 (12V)	
GPIO6		9 10		GPIO7	PWR	GND		9	10	PDM 2 (12V)	
GPIO8		11 12		GPIO9	NC	PDM 3 (BAT)		11	12	PDM 4 (BAT)	
GPIO10		13 14		GPIO11		PDM 5 (5V)		13	14	GND	
GPIO12		15 16	NC	GPIO13		PDM 6 (5V)		15	16	PDM 7 (5V)	
RESERVED		17 18		RESERVED		GND		17	18	PDM 8 (3v3)	
I2C2_SDA		19 20		I2C2_SCL		PDM 9 (3v3)		19	20	PDM 10 (3v3)	
SYS_SPI_SCK		21 22		SYS_SPI_MISO		GND		21	22	GND	
SYS_SPI_MOSI		23 24		SYS_SPI_NSS		12V		23	24	12V	
~FAULT		25 26		VREF0		5V_SYS	5V	25	26 5V	5V_SYS	
SENSE		27 28		VREF1		VCC_SYS	3V	27	28 3V	VCC_SYS	
~RESET	SYS_RESET	29 30		VREF2		DGND	GND	29	30 GND	DGND	
OFF_VCC		31 32		5V_USB		AGND		31	GND	DGND	
MHX_CRAP	SGPIO7	33 34	SGPIO8	MHX_CRAP		RESERVED		33	34	RESERVED	
MHX_CRAP	SGPIO9	35 36	SGPIO10	MHX_CRAP		PCM_IN		35	36	PCM_IN	
MHX_CRAP	SGPIO11	37 38	SGPIO12	MHX_CRAP		RESERVED		37	38	RESERVED	
MHX_CRAP	SGPO13	39 40	NC	MHX_CRAP		RESERVED		39	40	RESERVED	
SYS_SDA	SYS_SDA	41 42		VBACKUP		IDIODE_OUT		41 4	42	IDIODE_OUT	
SYS_SCL	SYS_SCL	43 44		RSVD0		IDIODE_OUT		43	44	IDIODE_OUT	
RSVD1		45 46		RSVD2		VBATT		45	46	VBATT	
USER0	~BUFEN_PRI	47 48	~BUFEN_SEC	USER1		GND	GND	47	48 GND	GND	
USER2	I2C2_SDA	49 50	SGPIO5	USER2		USER9	SPI2_NSS	49	50 SPI2_SCK	USER10	
USER4	I2C2_SCL	51 52	SGPIO6	USER3		USER11	SPI2 MISO	51	SPI2 MOSI	USER12	