

Concurrent Systems — Exam

June 2015

Name: _____

Duration: 120 minutes — No document authorized

1.

a) Explain informally what is a *barrier* and how it can be used?

b) Explain informally the difference between the various cache associativity designs discussed in the course (*fully associative*, *direct mapped*, *k-way set associative*)?

c) The *Anderson* and *CLH* locks solve some major limitations of spin locks. Which are these limitations?

d) The class `AtomicMarkableReference<V>` provides a method `v get(boolean[] mark)` that “returns the current values of both the reference and the mark”. Can you explain why it takes an array as parameter?

e) What do we mean when we talk about *linear scalability* for a concurrent algorithm?

f) Explain informally what is the *ABA problem*.

Consider the following interface for a *Consensus* object:

The consensus requires each thread to start with a non-null input value, given as parameter to the `decide()` method in this simplified interface. All threads must eventually agree on a common input value, which is returned by the `decide()` method.

```
public class LockConsensus implements Consensus {
    Object decision = null;

    synchronized Object decide(Object v) {
        if (decision == null)
            decision = v;
        return decision;
    }
}
```

[illegible]

This image shows a single sheet of white paper with horizontal ruling lines. The lines are evenly spaced and run across the width of the page. There are no margins, text, or other markings on the paper.

In computer science, load-link and store-conditional (LL/SC) are a pair of instructions used in multithreading to achieve synchronization. Load-link returns the current value of a memory location, while a subsequent store-conditional to the same memory location will store a new value only if no updates have occurred to that location since the load-link. Together, this implements a lock-free atomic read-modify-write operation.¹

```
public class AtomicValue {
    private int value;

    public AtomicValue (int v) {
        value = v;
    }

    // Return current value
    public native int loadLinked(); // Code not shown

    // Store new value if not modified since last loadLinked()
    // Return true on success, false otherwise
    public native bool storeConditional(int v); // Code not shown

    // Insert your code here
}
```

a) Can you extend this class by adding methods: `int getAndIncrement()`, `int getAndSet(int value)`, and `bool compareAndSet(int expectedValue, int newValue)`, implemented using only `loadLinked()` and `storeConditional()`? If so, write the corresponding code. Otherwise, explain why this is not possible.

[illegible]

¹ <https://en.wikipedia.org/wiki/Load-link/store-conditional>

[illegible]

b) [Bonus] Can you think of a reason why LL/SC would be more powerful than compare-and-swap?

[illegible]

4.

Consider an integer register (i.e., variable) r and three threads τ_1 , τ_2 , and τ_3 . Draw a graphical representation of the following histories and indicate if they are linearizable and/or sequentially consistent? If so, write the equivalent sequential history.

a)

τ_2 $r.write(1)$

τ_1 $r.read()$

τ_3 $r.write(2)$

τ_1 $r:1$

τ_3 $r:void$

τ_2 $r:void$

τ_2 $r:read()$

τ_2 $r:2$

b)

τ_2 $r.write(1)$

τ_1 $r.read()$

τ_3 $r.write(2)$

τ_1 $r:1$

τ_3 $r:void$

τ_2 $r:void$

τ_2 $r:read()$

τ_2 $r:1$

5.

Consider the code below. According to the Java memory model, can the reader method ever divide by zero? If so, how can we fix the program with a minimal amount of changes?

```
class Example {
    int x = 0;
    boolean v = false;
    public void writer() {
        x = 42;
        v = true;
    }
    public void reader() {
        if (v) {
            int y = 100 / x;
        }
    }
}
```

[illegible]

The following code shows an incorrect implementation of *CLHLock* in which a thread reuses its own node instead of its predecessor node. Explain how this implementation can go wrong.

[illegible]