

redpitaya

Red Pitaya d.o.o.
Velika pot 22
5250 Solkan
Slovenia

www.redpitaya.com

Electrical schematics for:

name: STEM122-16SDR
version: V1r1
variant: Series1

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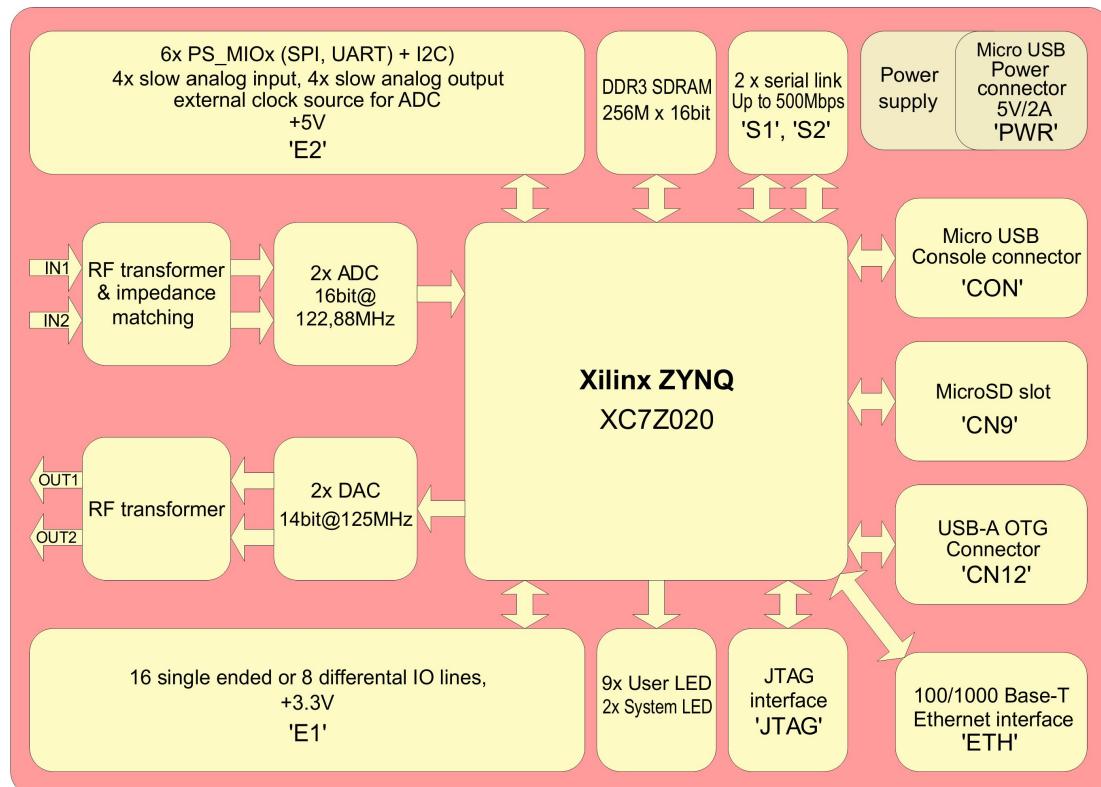
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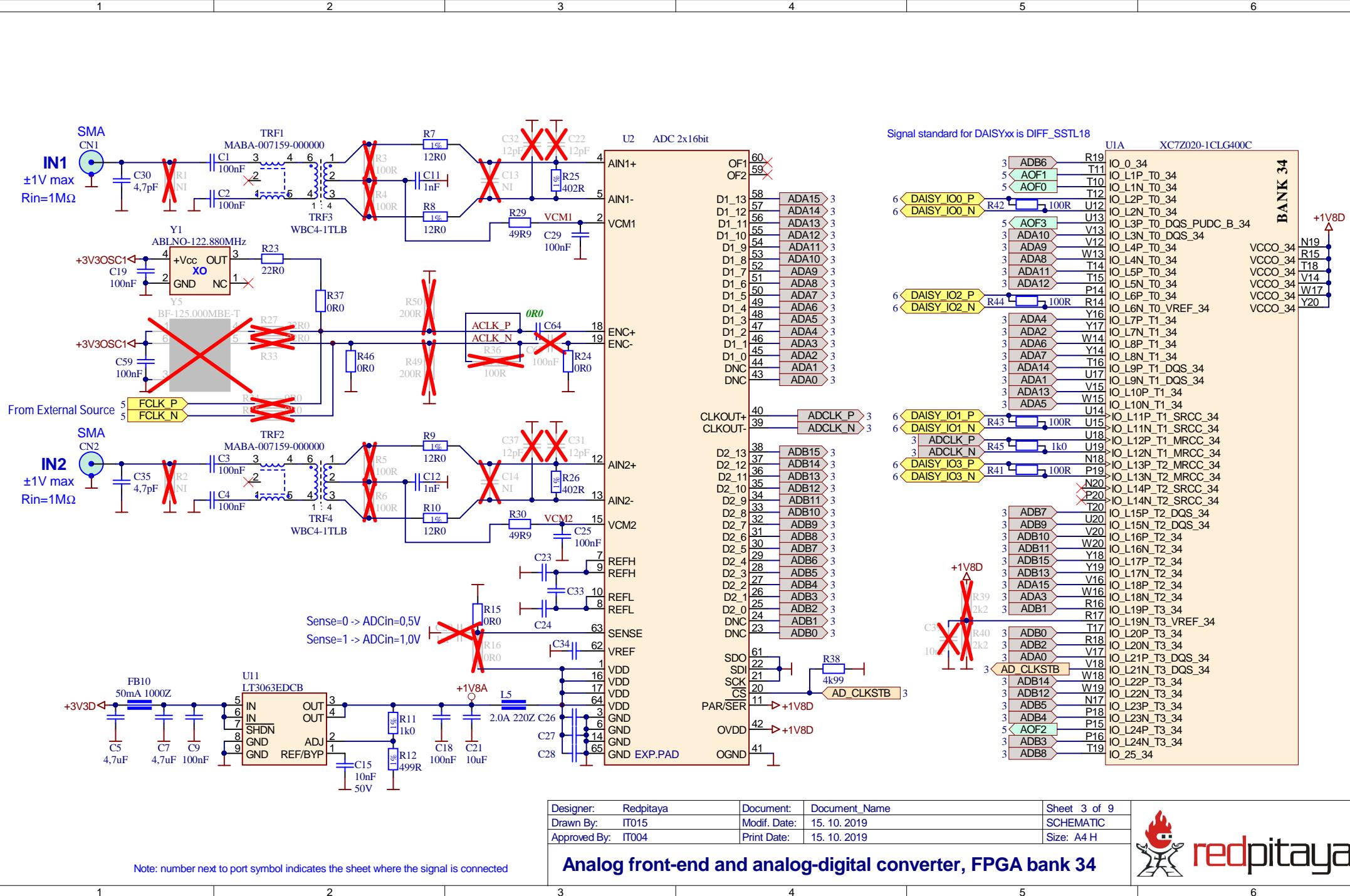
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Drawn By:	IT015	Modif. Date:	15. 10. 2019	SCHEMATIC
Approved By:	IT004	Print Date:	15. 10. 2019	Size: A4 H

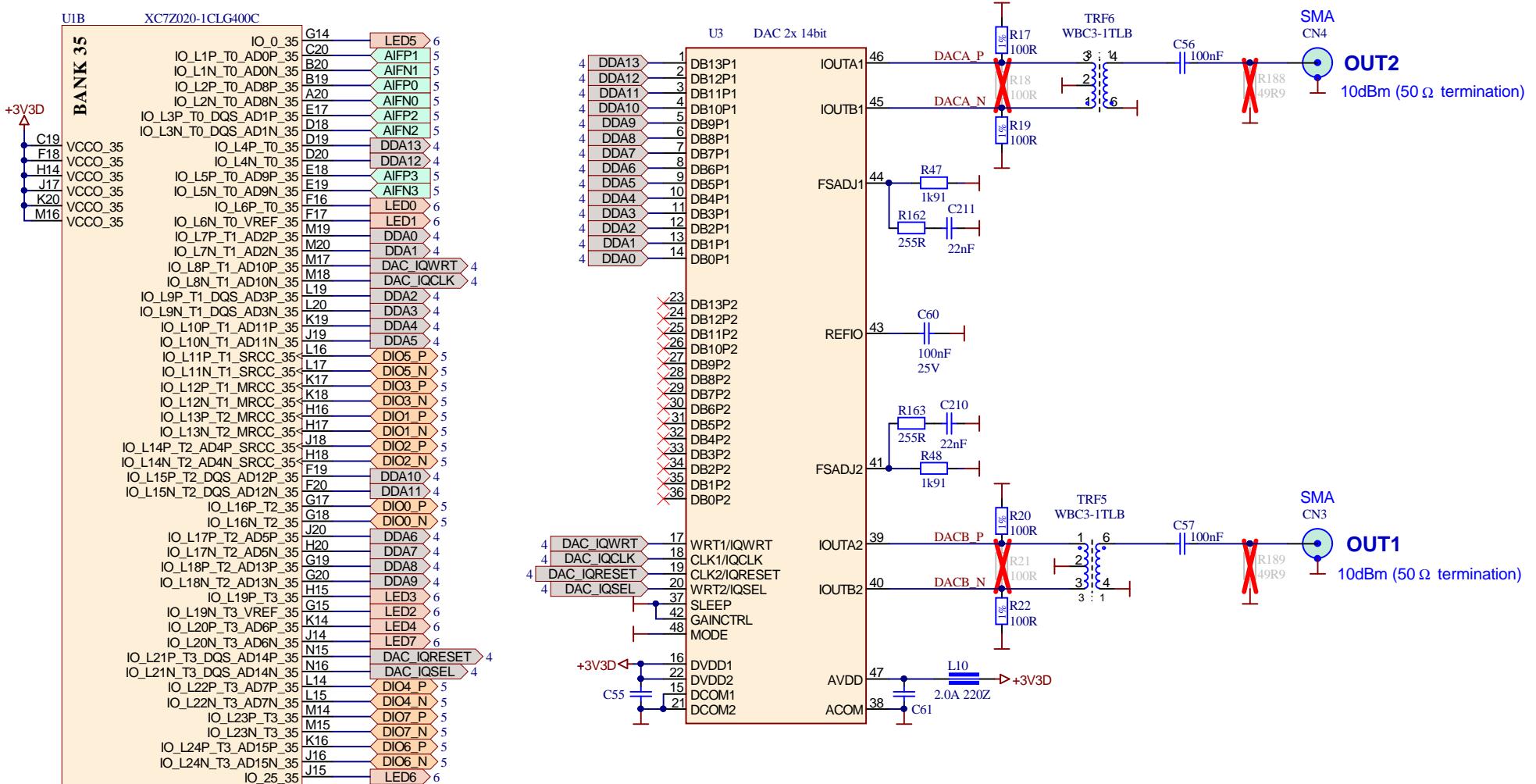
Red Pitaya Software Defined Radio





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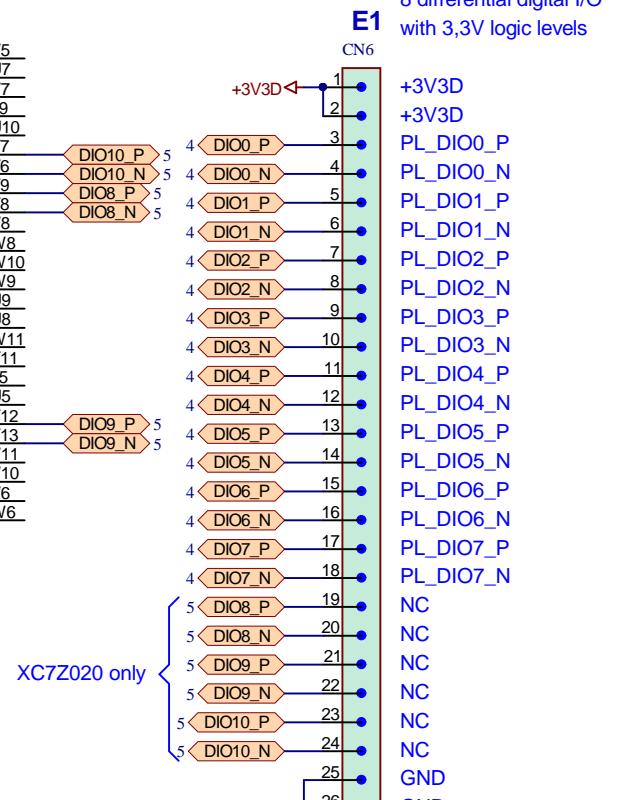
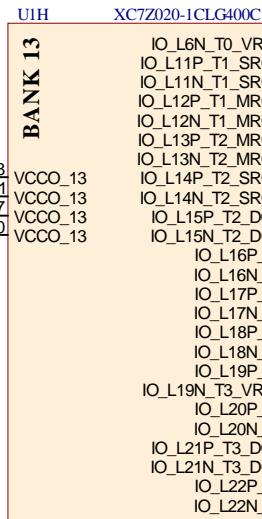
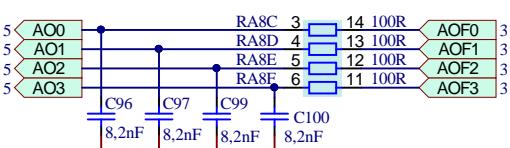
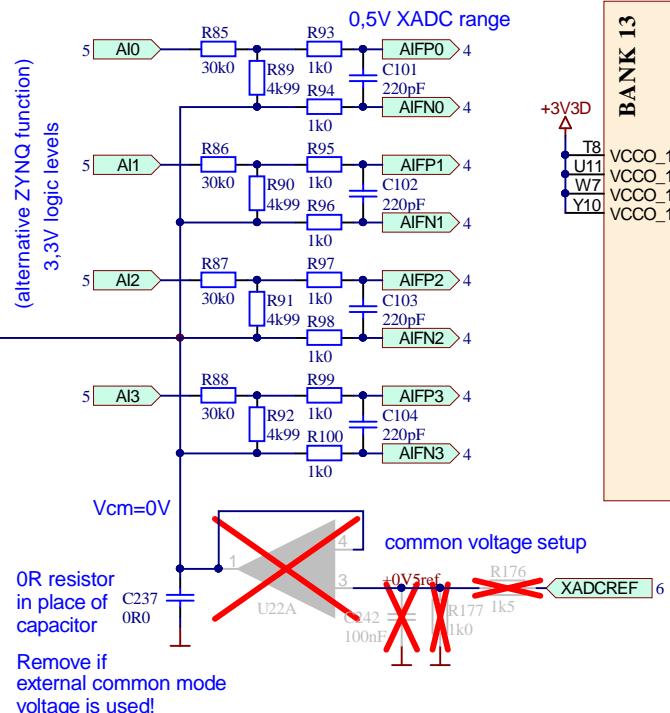
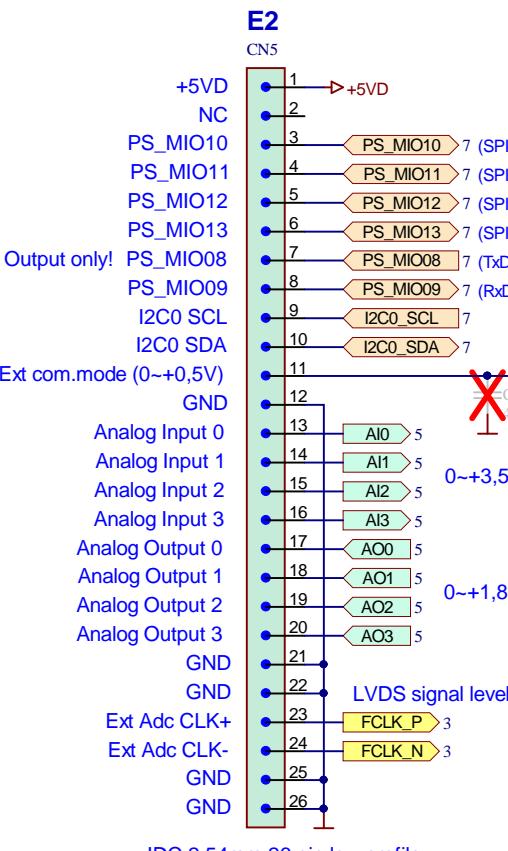


Note: number next to port symbol indicates the sheet where the signal is connected

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1 2 3 4 5 6

PS_MIO08 is output only and at power-up must be low level (no external pull-ups)!

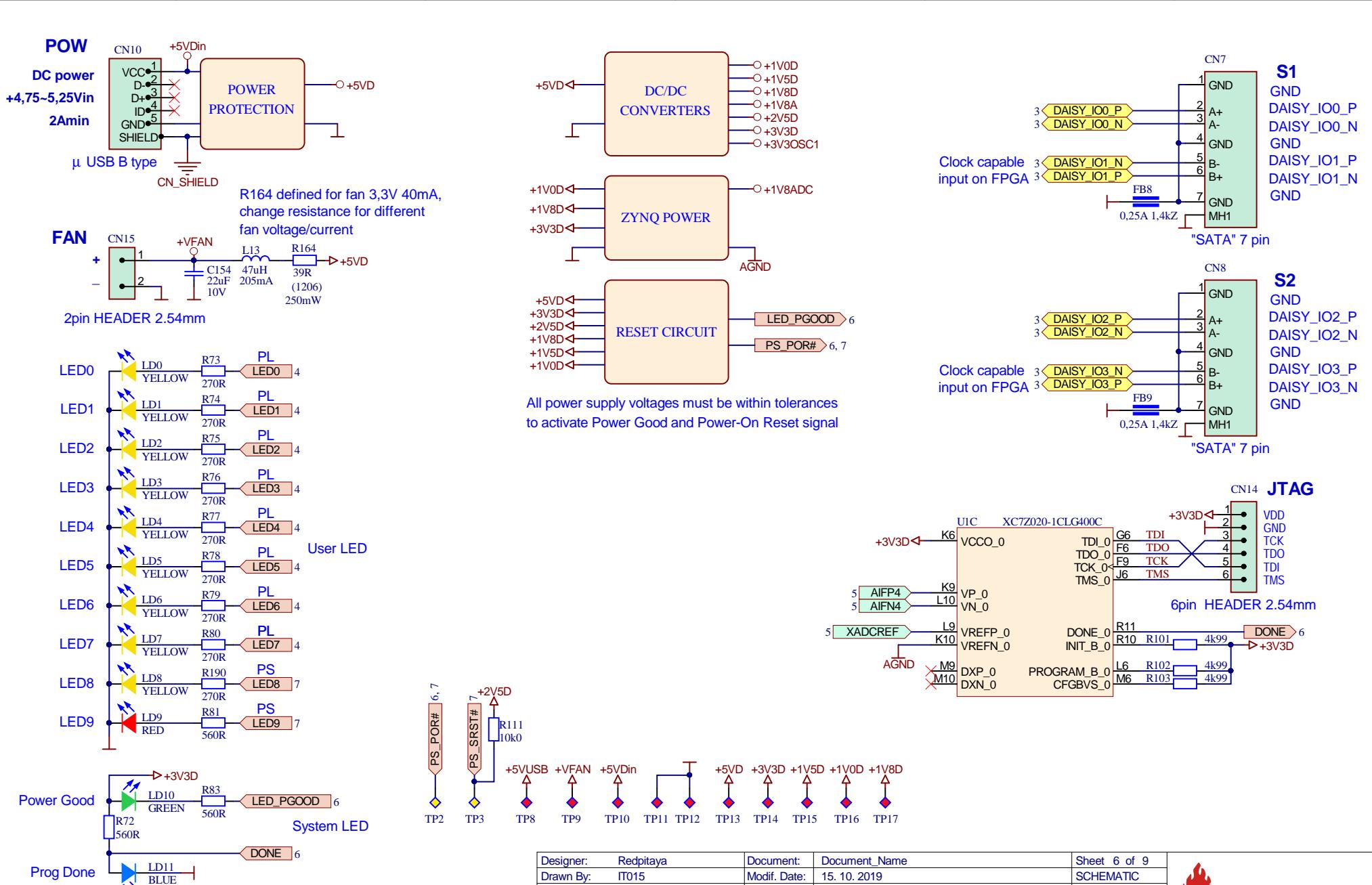


IDC 2.54mm 26 pin low profile

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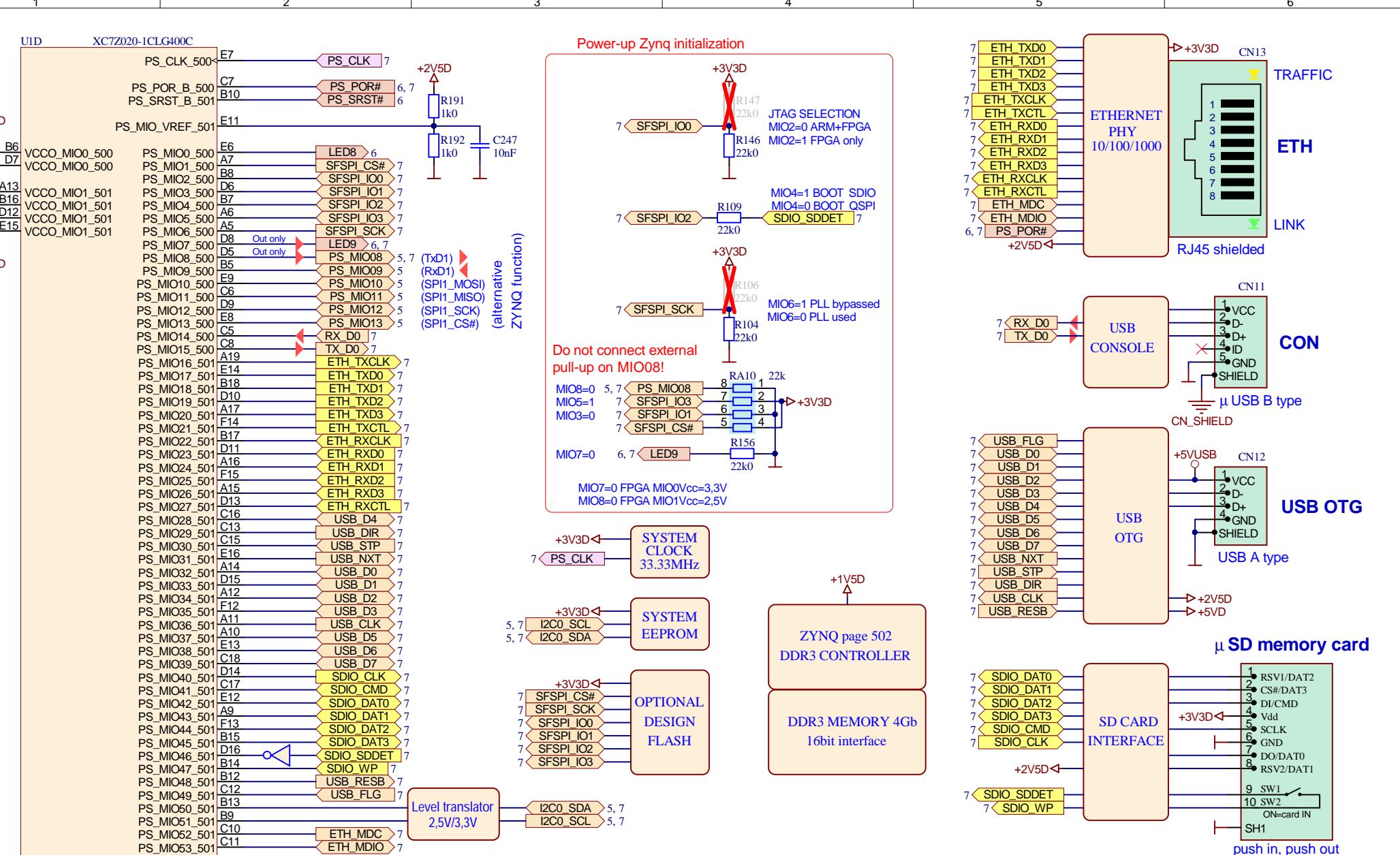
I/O digital, I/O slow analog, Extension connectors

Note: number next to port symbol indicates the sheet where the signal is connected



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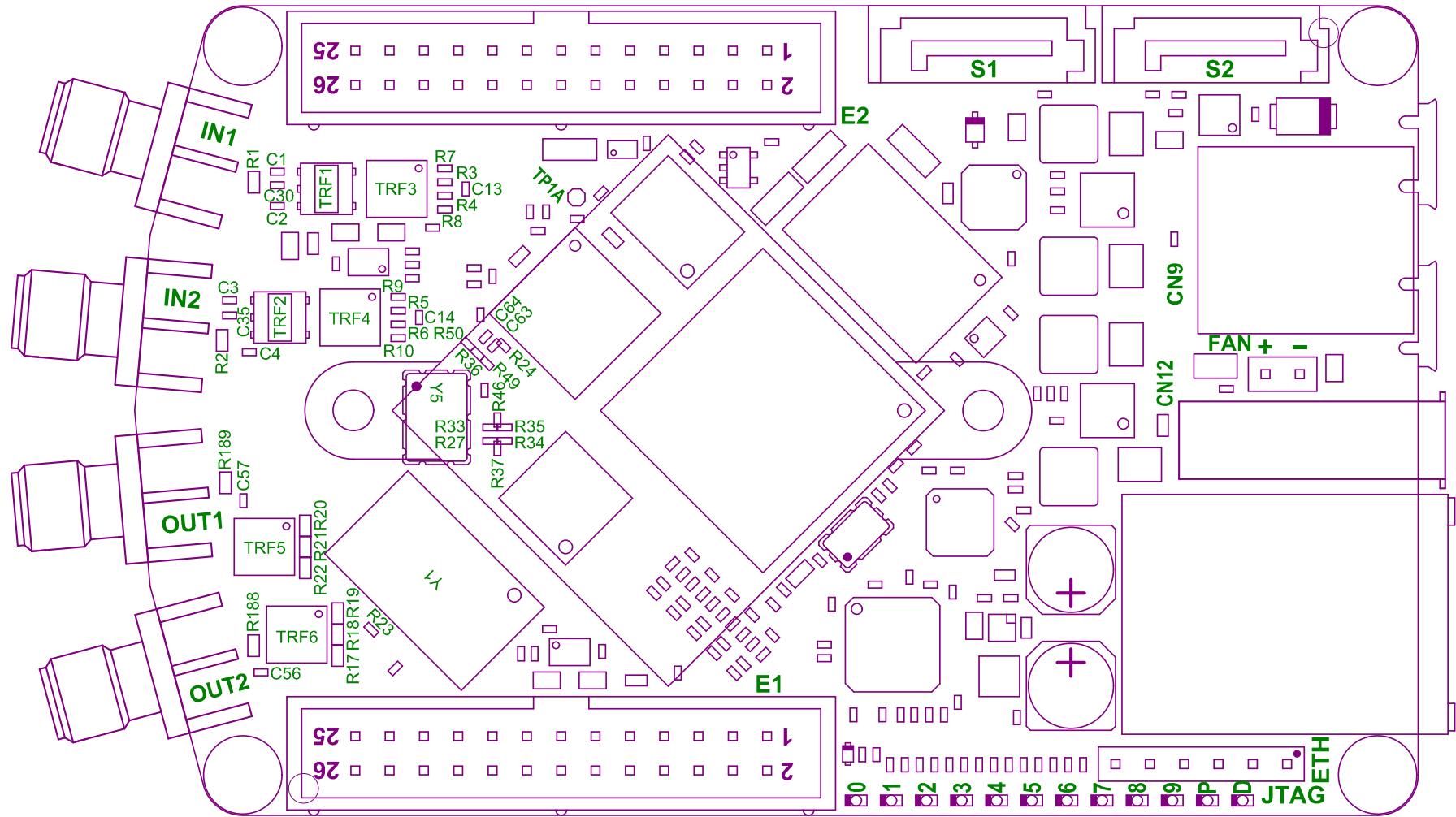
LED, Power, Fan, Serial I/O, JTAG, testpoints



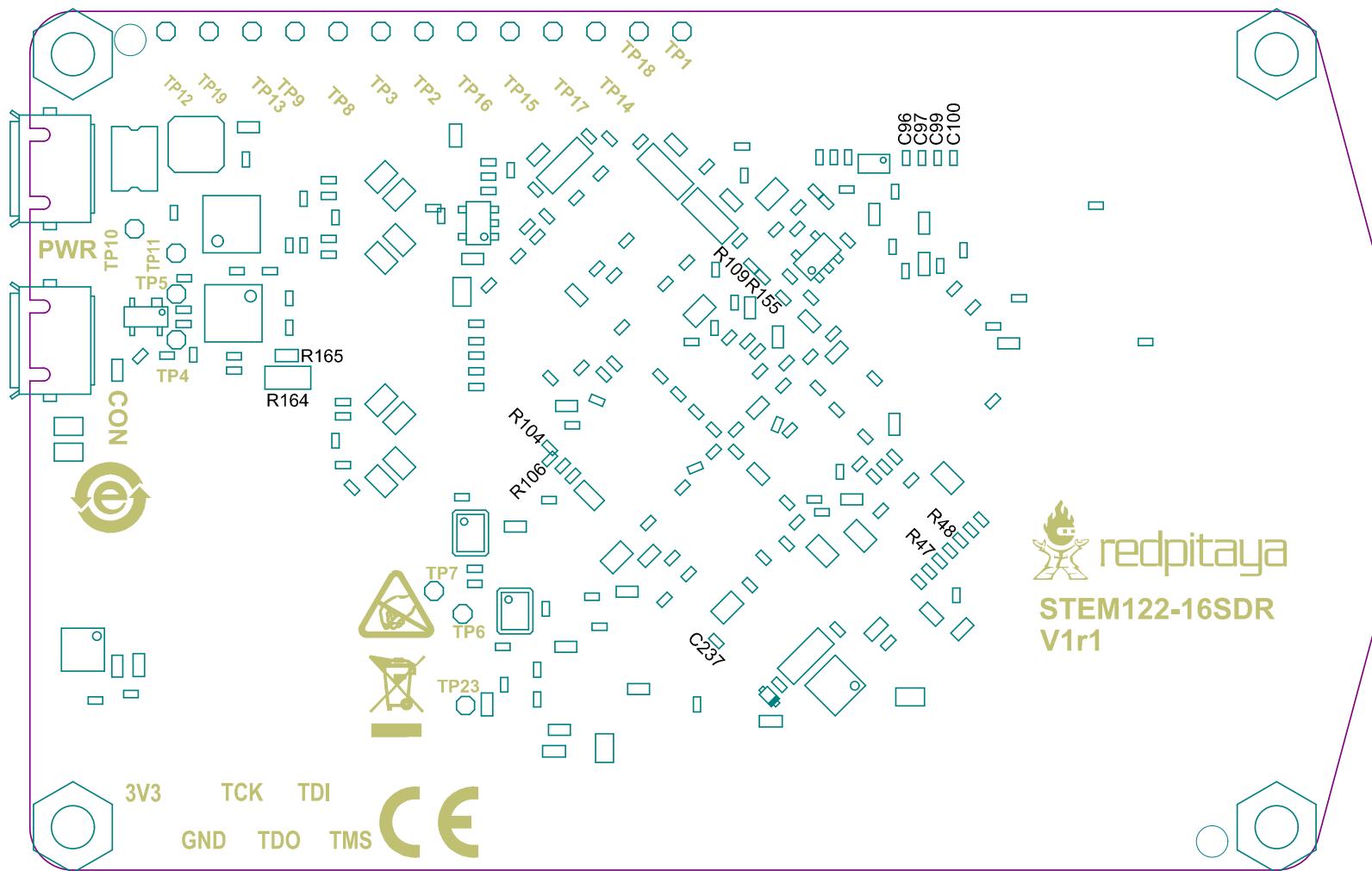
Note: number next to port symbol indicates the sheet where the signal is connected

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Zynq PS bank500 and 501, Ethernet, Console, USB, memory



Assembly bottom: STEM122-16SDR V1r1
- user important components



**Assembly top: STEM122-16SDR V1r1
- user important components**