## **6.824** Spring 2015 Paper Questions

For each paper, your assignment is two-fold. By 10PM the evening before lecture:

- Submit your answer for each lecture's paper question via the <u>submission web</u> <u>site</u>, and
- Submit your own question about the paper (e.g., what you find most confusing about the paper or the paper's general context/problem). You cannot use the question below. To the extent possible, during lecture we will try to answer questions submitted the evening before.

You can also upload your questions and answers using curl:

```
## Answer goes into lecN.txt
$ curl -F file=@lec2.txt \
    -F key=XXXXXXXX \
    http://6824.scripts.mit.edu/submit/handin.py/upload
## Question goes into sqN.txt
$ curl -F file=@sq2.txt \
    -F key=XXXXXXXX \
    http://6824.scripts.mit.edu/submit/handin.py/upload
```

## Lecture 9

Memory Coherence in Shared Virtual Systems. Answer this question using ivy-code.txt, which is a version of the code in Section 3.1 with some clarifications and bug fixes. You can see that the manager part of the WriteServer sends out invalidate messages, and waits for confirmation messages indicating that the invalidates have been received and processed. Suppose the manager sent out invalidates, but did not wait for confirmations. Describe a scenario in which lack of the confirmation would cause the system to behave incorrectly. You should assume that the network delivers all messages, and that none of the computers fail.

Questions or comments regarding 6.824? Send e-mail to <u>6.824-staff@pdos.csail.mit.edu</u>.

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