Yosys Optimization Error: Optimization Stuck in PEEPOPT Pass.

Hello,

I encountered an issue while using Yosys to synthesize a Verilog file.

I used the Verilog file rtl.v, and during the Yosys synthesis, the process got stuck for a long time.

I have minimized the test case as much as possible to help with troubleshooting. Attached is the Verilog file (rtl.v) that triggers this issue. I hope to get the community's help and attention.