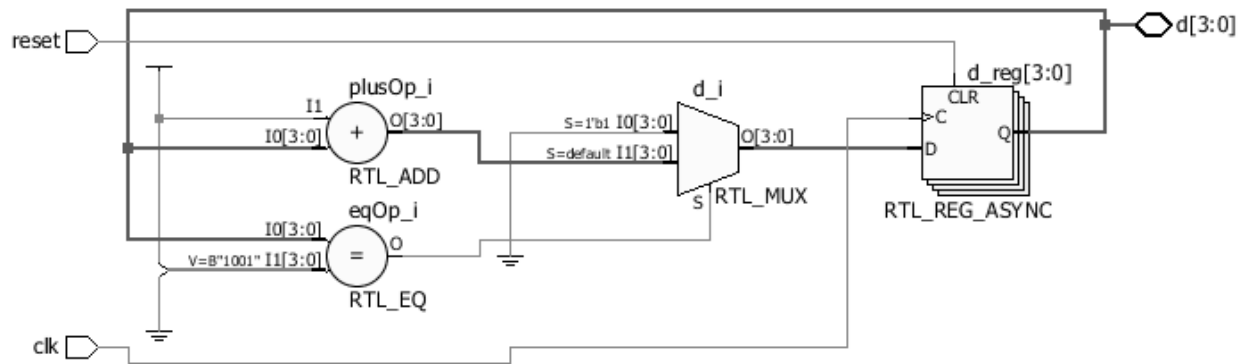


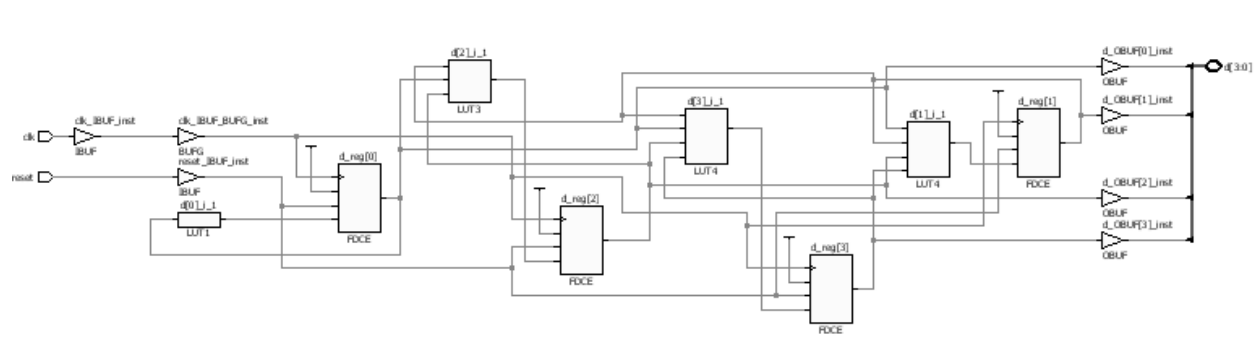
Decade Counter (IC 7490)

Pin no:23P35A0402

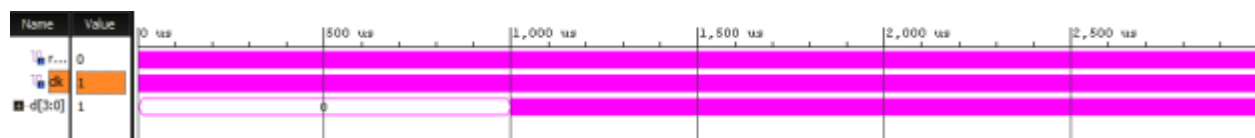
RTL SCHEMATIC:



SYNTHESIS DESIGN:



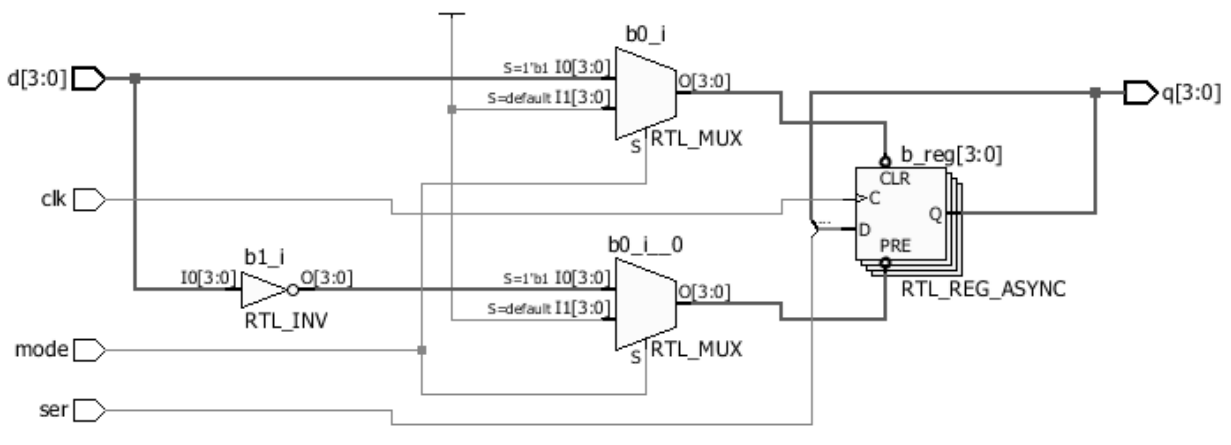
TIMING DIAGRAM:



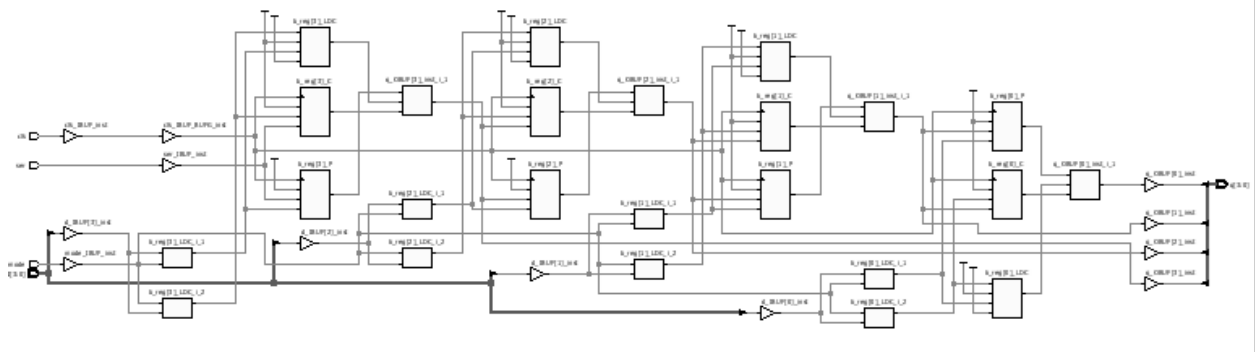
Shift Register (IC 7495)

Pin no:23P35A0402

RTL SCHEMATIC:



SYNTHESIS DESIGN:

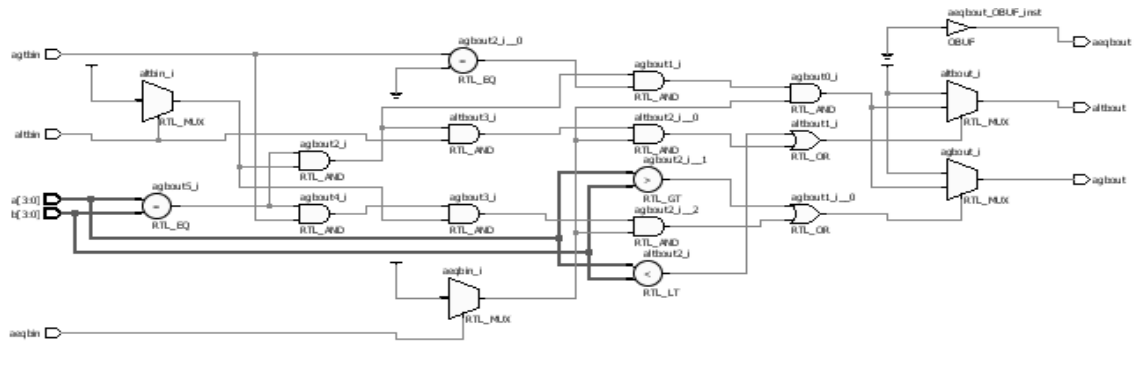


TIMING DIAGRAM:

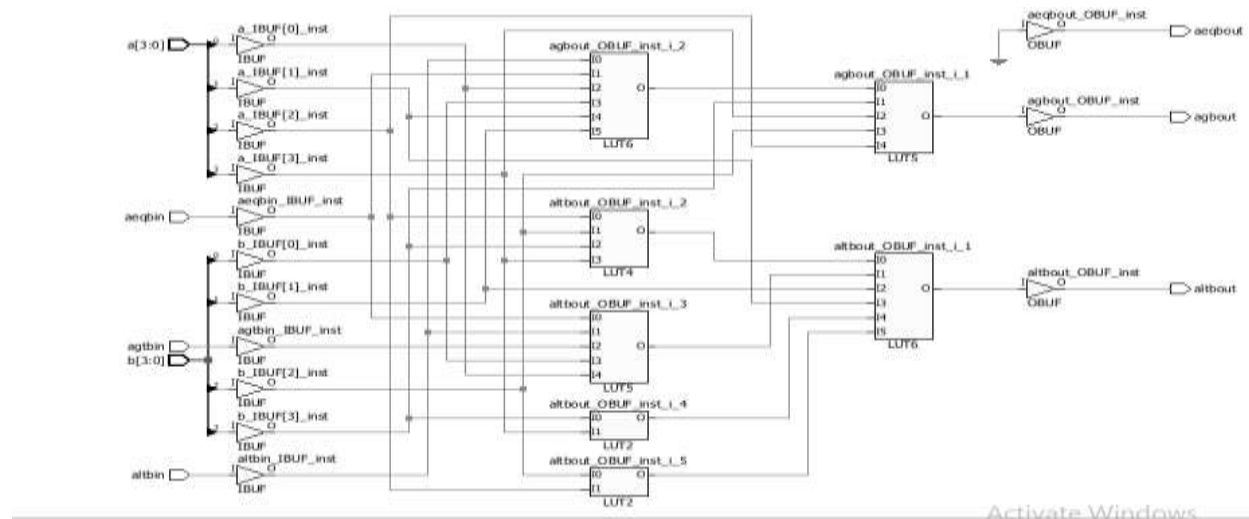


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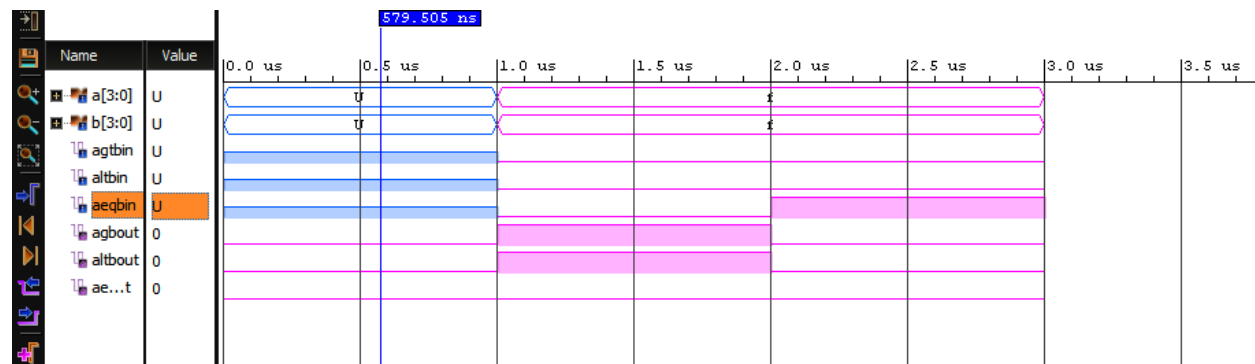
RTL SCHEMATIC:



SYNTHESIS DESIGN:

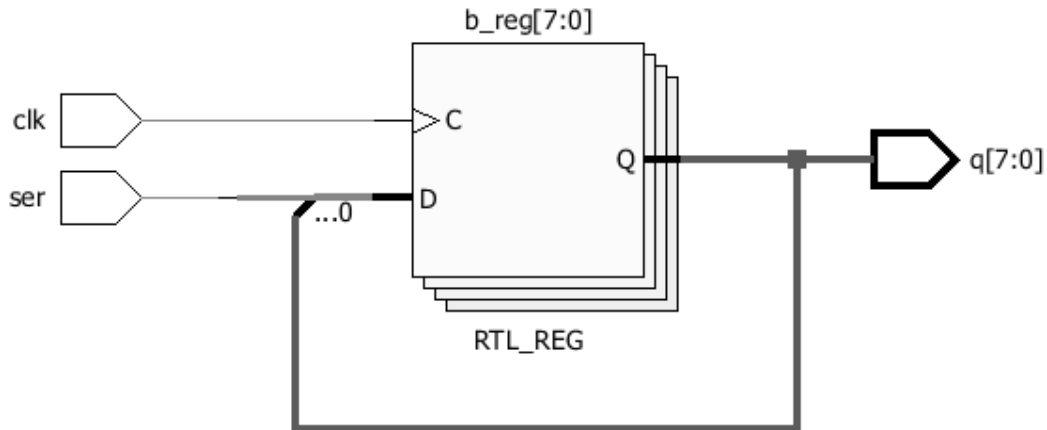


TIMING DIAGRAM:

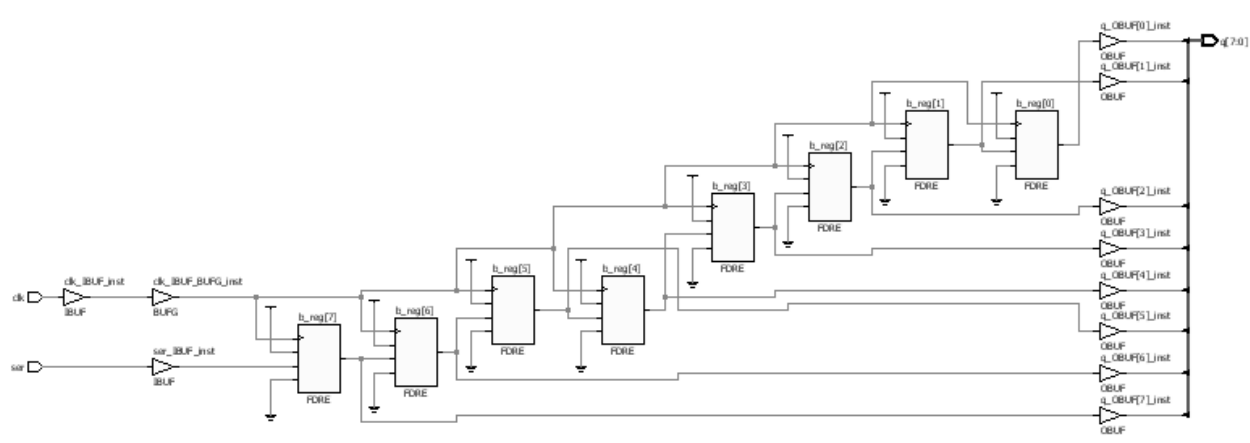


8-BIT SERIAL IN PARALLEL OUT AND PARALLEL IN SERIAL OUT(SIPO)

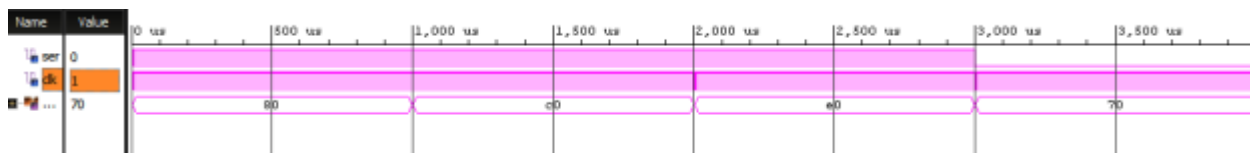
RTL SCHEMATIC:



SYNTHESIS DESIGN:

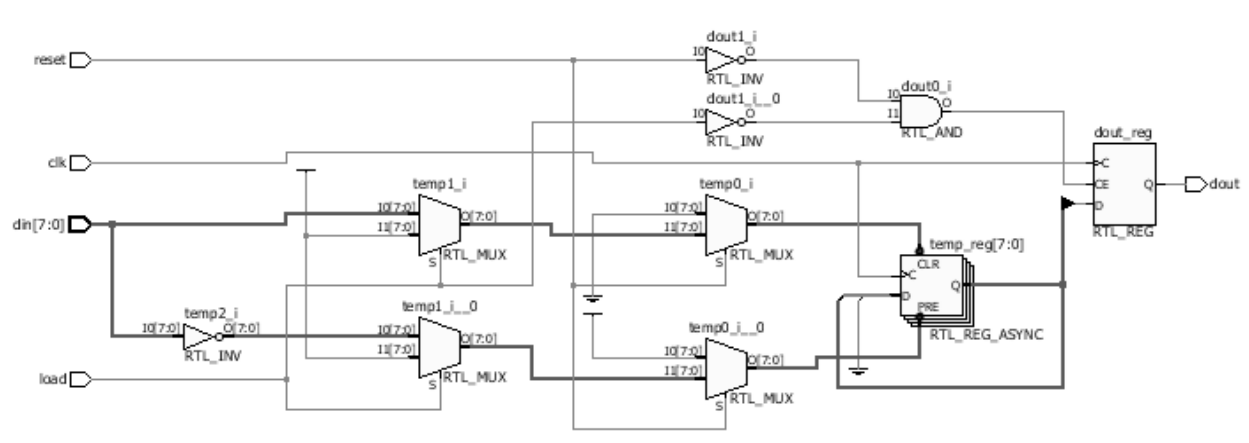


TIMING DIAGRAM:

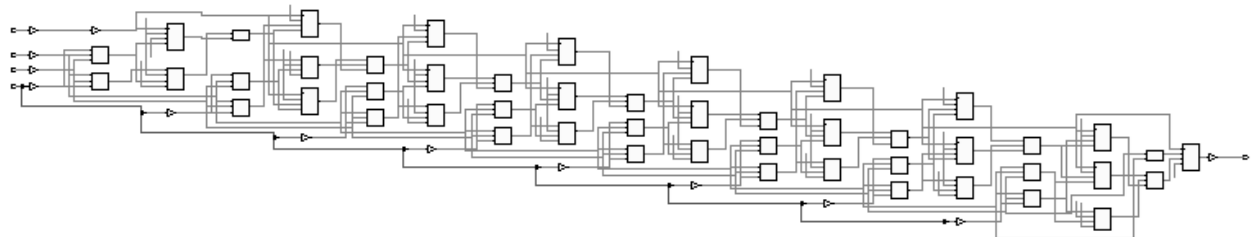


8-BIT SERIAL IN PARALLEL IN AND SERIAL OUT(PISO):

RTL SCHEMATIC:



SYNTHESIS DESIGN:



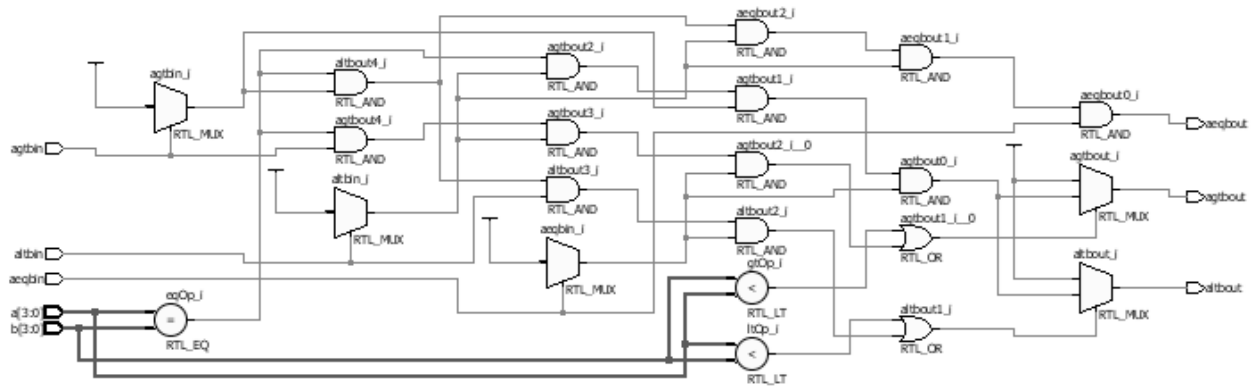
TIMING DIAGRAM:



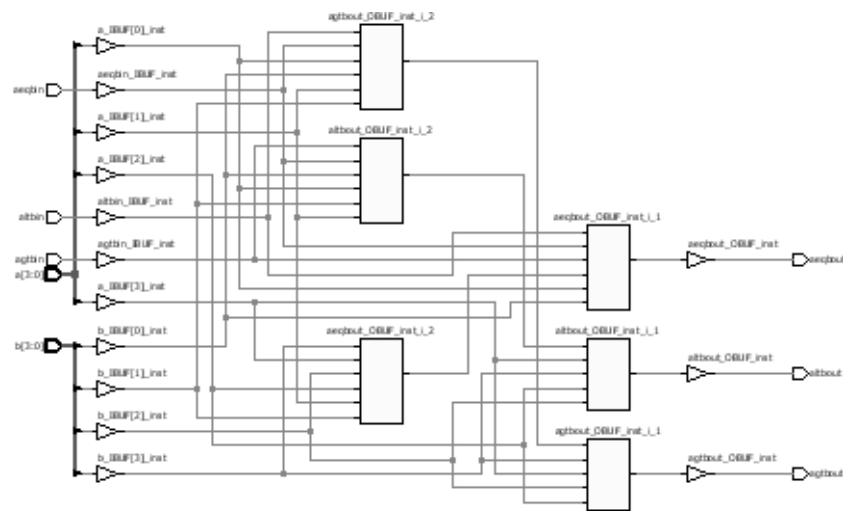
4-BIT COMPARATOR

Pin no:23P35A0402

RTL SCHEMATIC:



SYNTHESIS DESIGN:



TIMING DIAGRAM:

