### PCB

Board size: 20.0x20.0 mm (0.79x0.79 inches)

- This is the size of the rectangle that contains the board
- Thickness: 1.6 mm (63 mils)
- Material: FR4Finish: HALLayers: 2
- Copper thickness: 35  $\mu m$

Solder mask: TOP / BOTTOM

• Color: Green

Silk screen: TOP / BOTTOM

• Color: White

## Important sizes

Clearance: 0.2 mm (8 mils)

Track width: N/A mm (N/A mils)

• By design rules: 0.2 mm (8 mils)

Drill: 1.3 mm (51 mils)

- Vias: N/A mm (N/A mils) [Design: 0.4 mm (16 mils)]
- Pads: 1.3 mm (51 mils)
- $\bullet\,$  The above values are real drill sizes, they add 0.1 mm (4 mils) to plated holes (PTH)

Via: N/A/N/A mm (N/A/N/A mils)

- By design rules: 0.4/0.3 mm (16/12 mils)
- Micro via: no [0.2/0.1 mm (8/4 mils)]
- Burried/blind via: no

Outer Annular Ring: 0.55 mm (22 mils)

• By design rules: 0.55 mm (22 mils)

Eurocircuits class: 4A - Using min drill 1.3 mm for an OAR of 0.55 mm

#### General stats

Components count: (SMD/THT)

Top: 2/1 (SMD + THT)
Bottom: 0/0 (NONE)

Defined tracks:

Used tracks:

Defined vias:

Used vias:

Holes (excluding vias):

- 1.2 mm (47 mils) (2)
- 2.2 mm (87 mils) (4)

Oval holes:

Drill tools (including vias and computing adjusts and rounding):

- 1.3 mm (51 mils) (2)
- 2.2 mm (87 mils) (4)

## Schematic

Schematic in SVG format

# **PCB Layers**

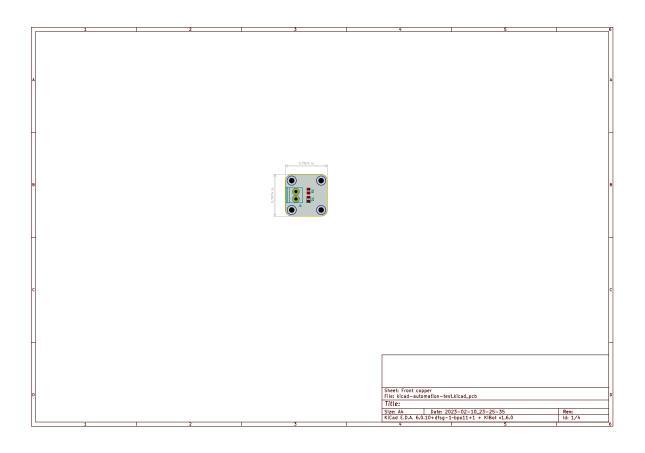


Figure 1: PCB Front copper

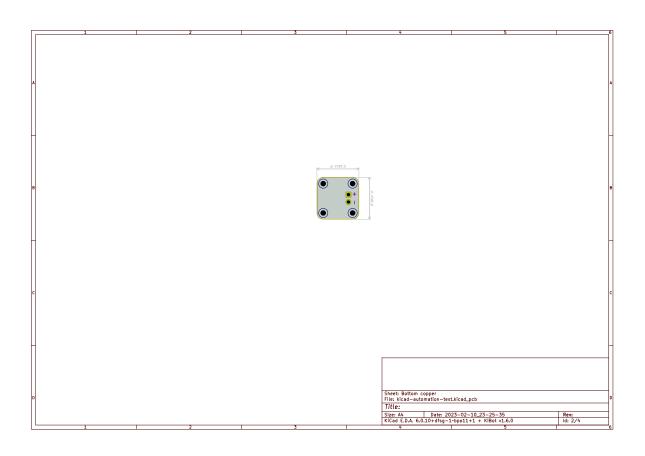


Figure 2: PCB Bottom copper

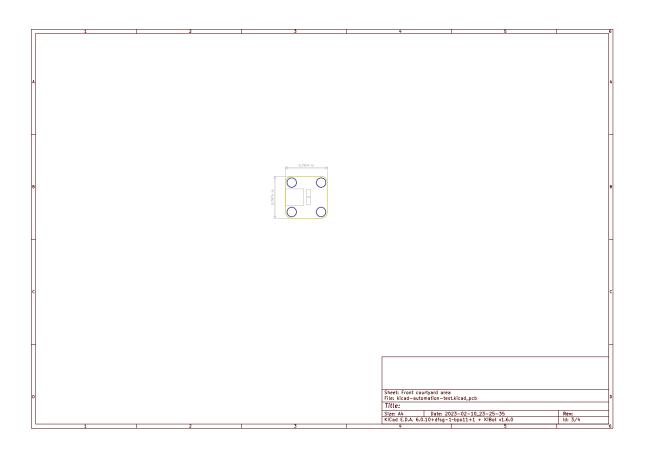


Figure 3: PCB Front courtyard area

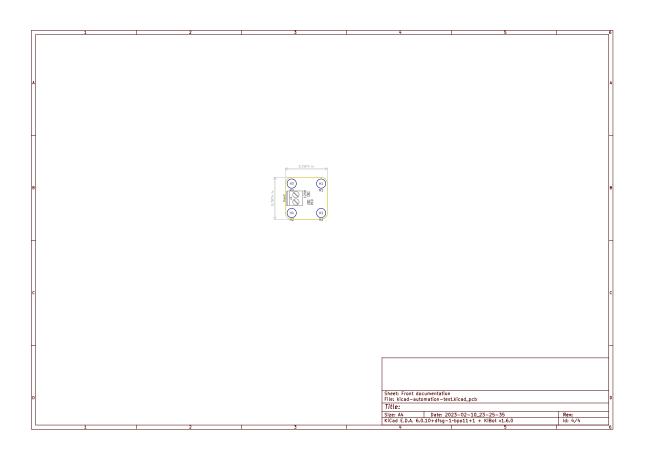


Figure 4: PCB Front documentation