Bonus point assignment – week 3
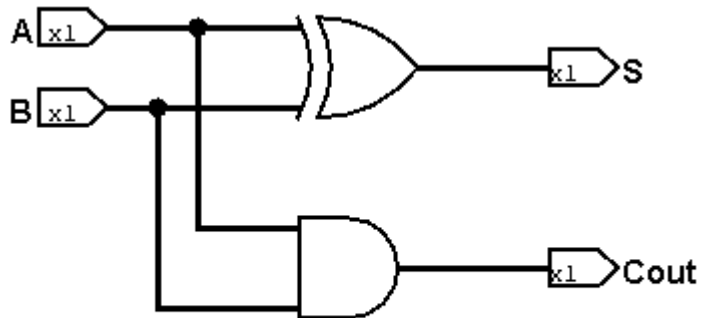
Complete the **half adder**, **full adder** and **4-bit adder** assignment as described in the PowerPoint slides of week 3 in Logisim. Save the chip design and also export three PNG pictures of the separate finished designs. See the PowerPoint slides of week 3.
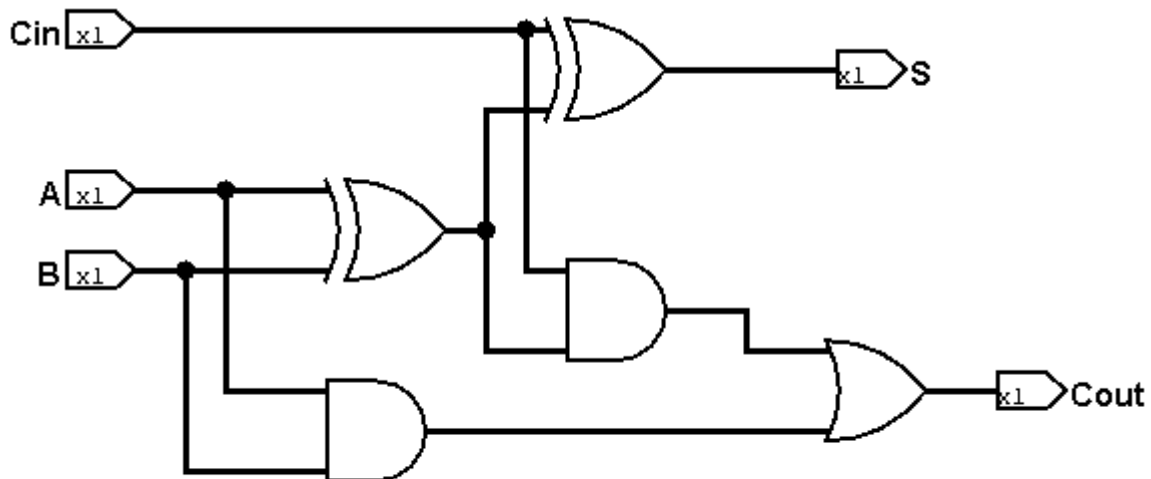
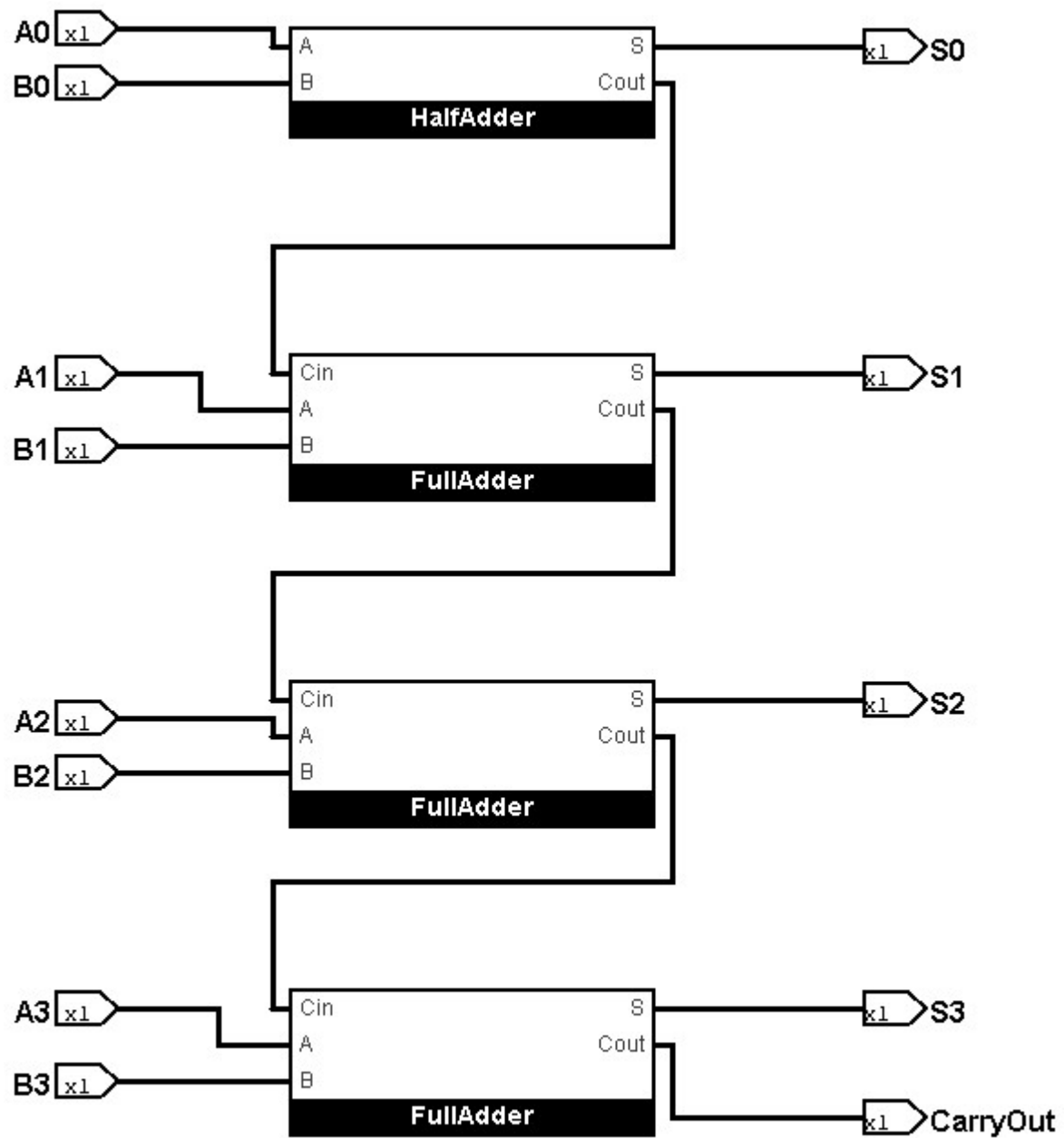Paste the three exported PNG pictures in here.

HALF ADDER:

Max van Steenbergen - 572430

FULL ADDER:

Max van Steenbergen - 572430

4-BIT ADDER:

Max van Steenbergen - 572430