

Event Isolation Flagging Module

Progress Update

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Overview

Concept

- Function of the event isolation flagging module

- Flow diagrams

- Block diagrams

Implementation

- Implementation in VHDL

- Testing in Modelsim

- Incorporation into the full AMC40 Firmware

The event isolation flagging module

- ▶ Sorts columns in SPP
- ▶ Checks each column to see if it is isolated
- ▶ Either adds a flag bit or bypasses it

Flow diagrams

Top level

Flow diagrams

Sorter

Block diagrams

Top level

Block diagrams

Active controller

Block diagrams

Data processor

Implementation in VHDL

Testing in Modelsim

Incorporation into the full AMC40 Firmware

Summary

- ▶ Implementation in Modelsim is complete
- ▶ Mid way through testing

- ▶ Outlook
 - ▶ Complete testing in Modelsim
 - ▶ Test as a standalone module in Quartus
 - ▶ Incorporate into full AMC40 firmware