

Hardware-stage isolated cluster flagging algorithm for the LHCb VELO upgrade

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May 2017

Abstract

Development of an FPGA isolated cluster flagging algorithm in VHDL for the new Vertex Locator in the LHCb 2021 upgrade

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2 Physics

3 LHCb Experiment

4 Vertex Locator

5 Upgrade

6 Data Readout

7 Isolated Clusters

8 Results

9 Discussion