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(19) **United States**(12) **Patent Application Publication****Castany et al.**(10) **Pub. No.: US 2007/0152887 A1**(43) **Pub. Date:****Jul. 5, 2007**(54) **MULTI-BAND MONOPOLE ANTENNAS FOR
MOBILE NETWORK COMMUNICATIONS
DEVICES****Publication Classification**(76) Inventors: **Jordi Soler Castany**, Mataro (ES);
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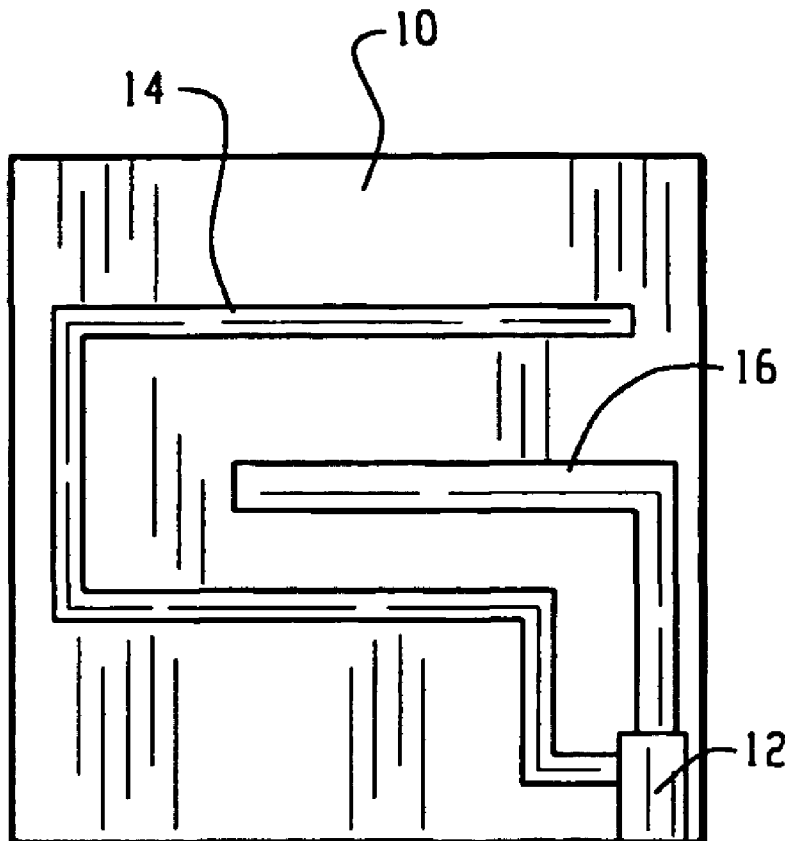
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ABSTRACT(21) Appl. No.: **10/587,119**(22) PCT Filed: **Jan. 28, 2005**(86) PCT No.: **PCT/EP05/00879**

§ 371(c)(1),

(2), (4) Date: **Aug. 29, 2006****Related U.S. Application Data**(60) Provisional application No. 60/540,448, filed on Jan.
30, 2004.

Multiband monopole antennas are disclosed. The antennas disclosed can include a substrate for mounting conductors, one or more conductors for receiving networking signals mainly in a first frequency band, and one or more conductors for receiving networking signals mainly in a second frequency band. The conductors can have a polygonal shape or the conductors can have a linear, space-filling, or grid dimension shape. The conductors can be connected at a feed point. One or more antenna can be incorporated into a single printed circuit board. When multiple antennas are used with the same printed circuit board, the conducting material of the printed circuit board located between the antenna attachment points can be interrupted to improve the isolation of each antenna.



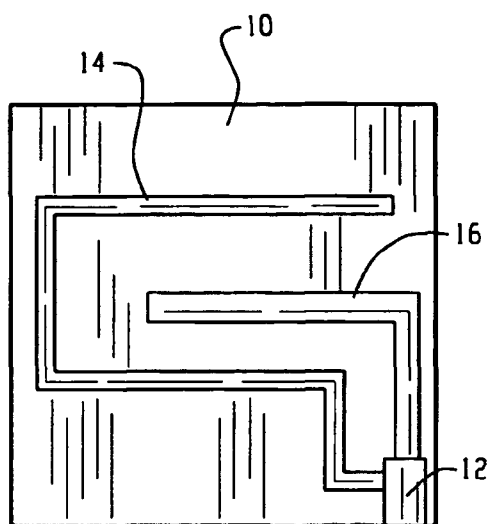


Fig. 1

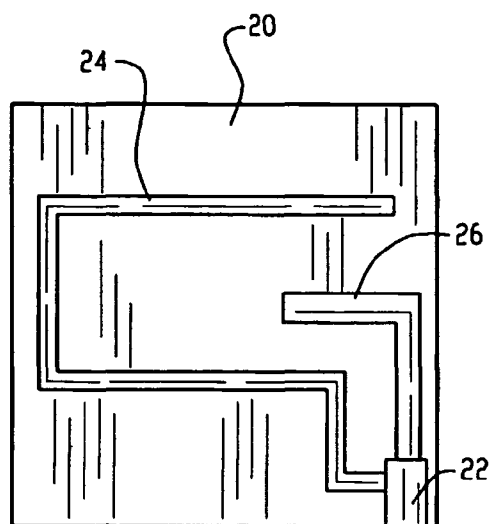


Fig. 2

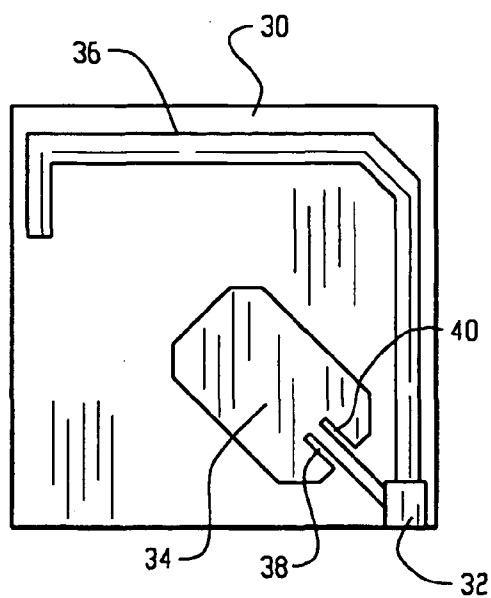


Fig. 3

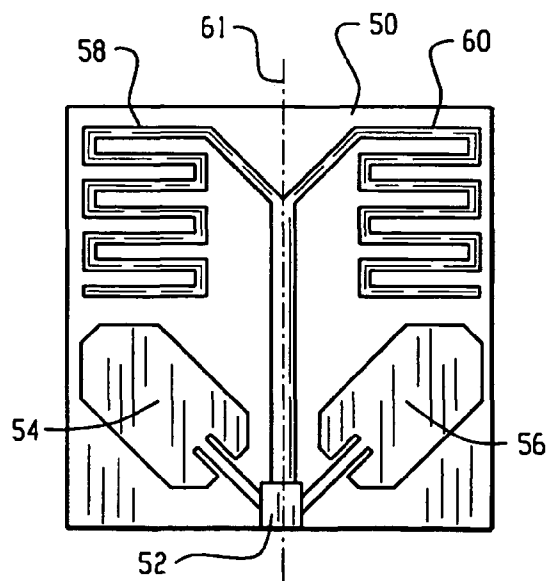


Fig. 4

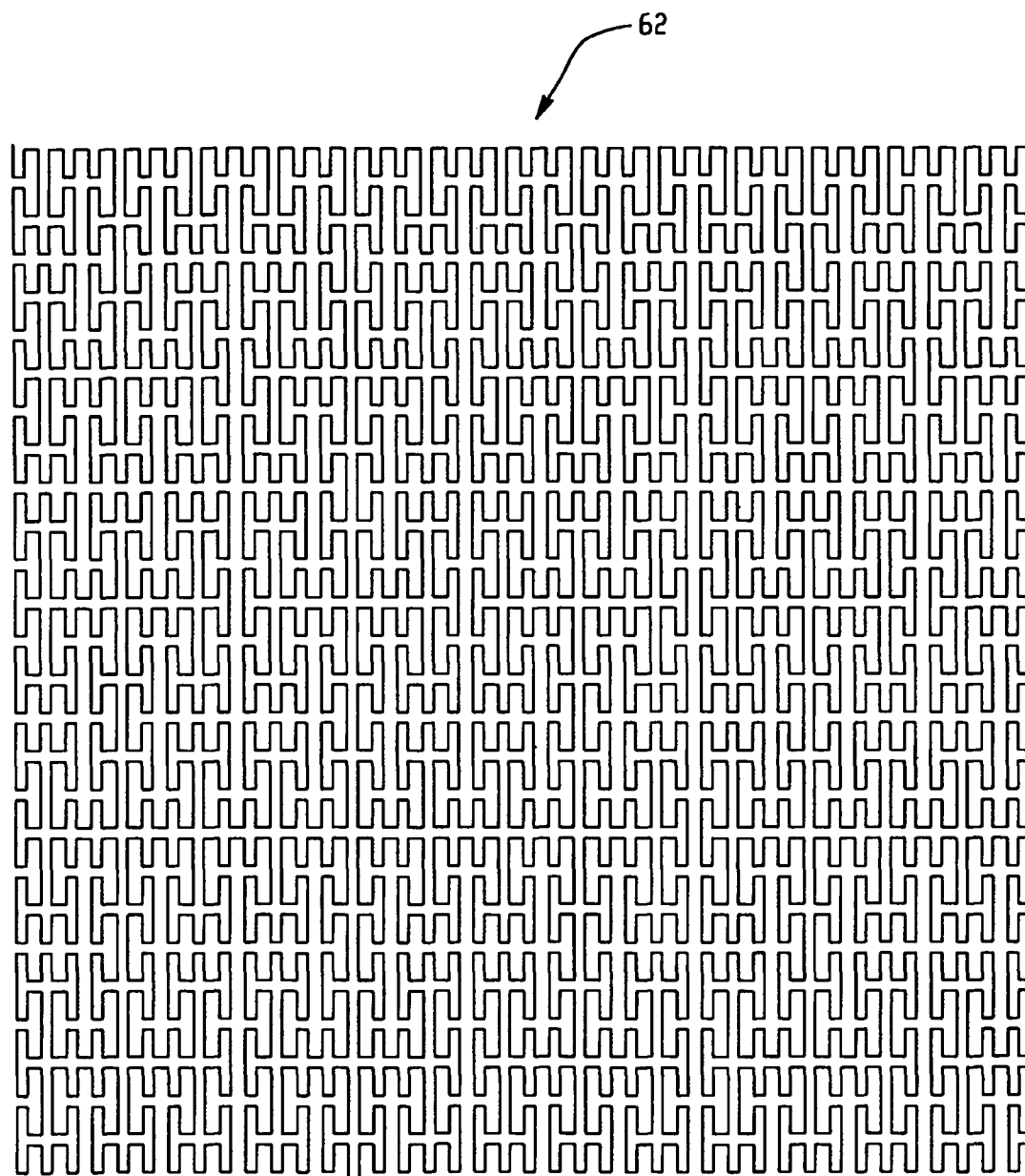


Fig. 5

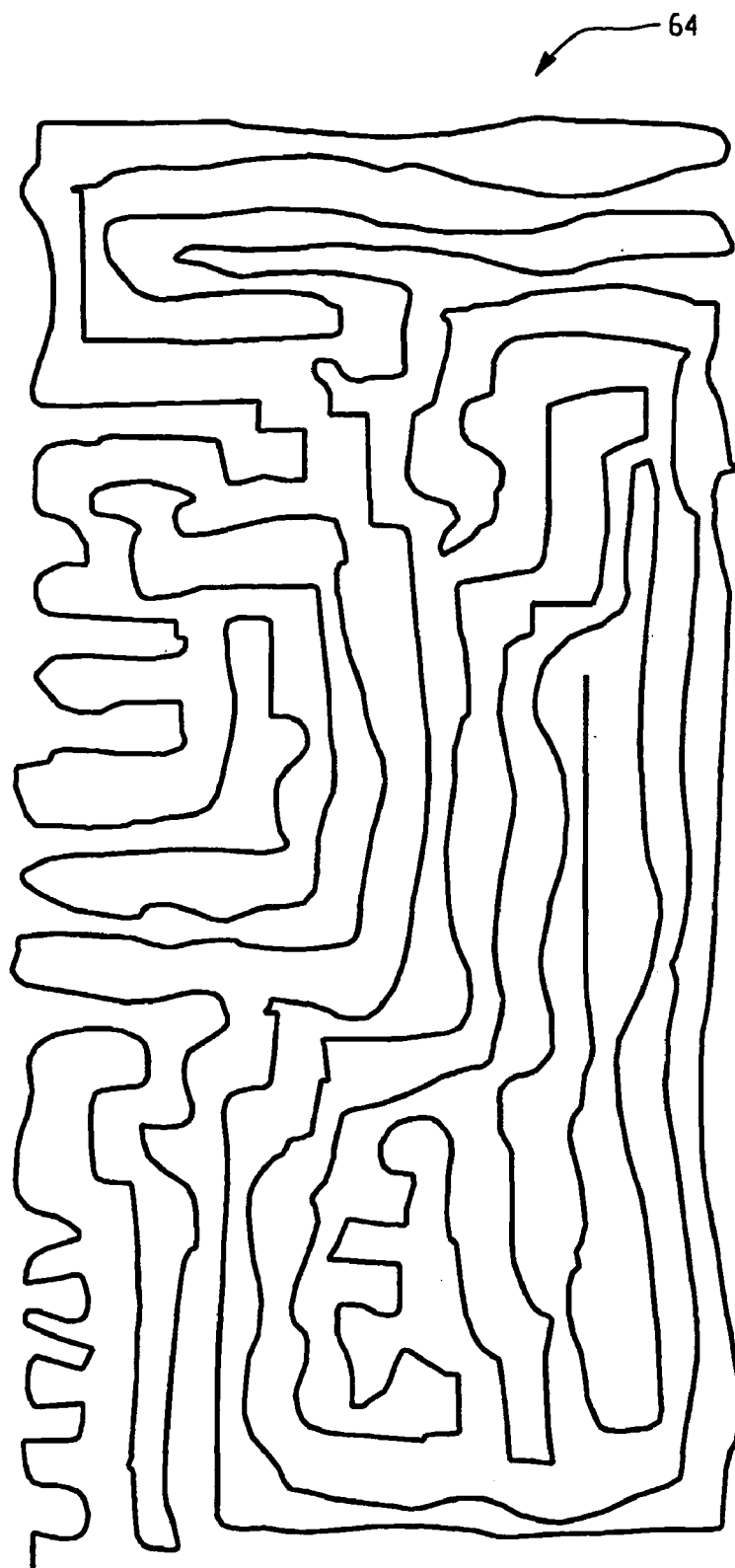


Fig. 6

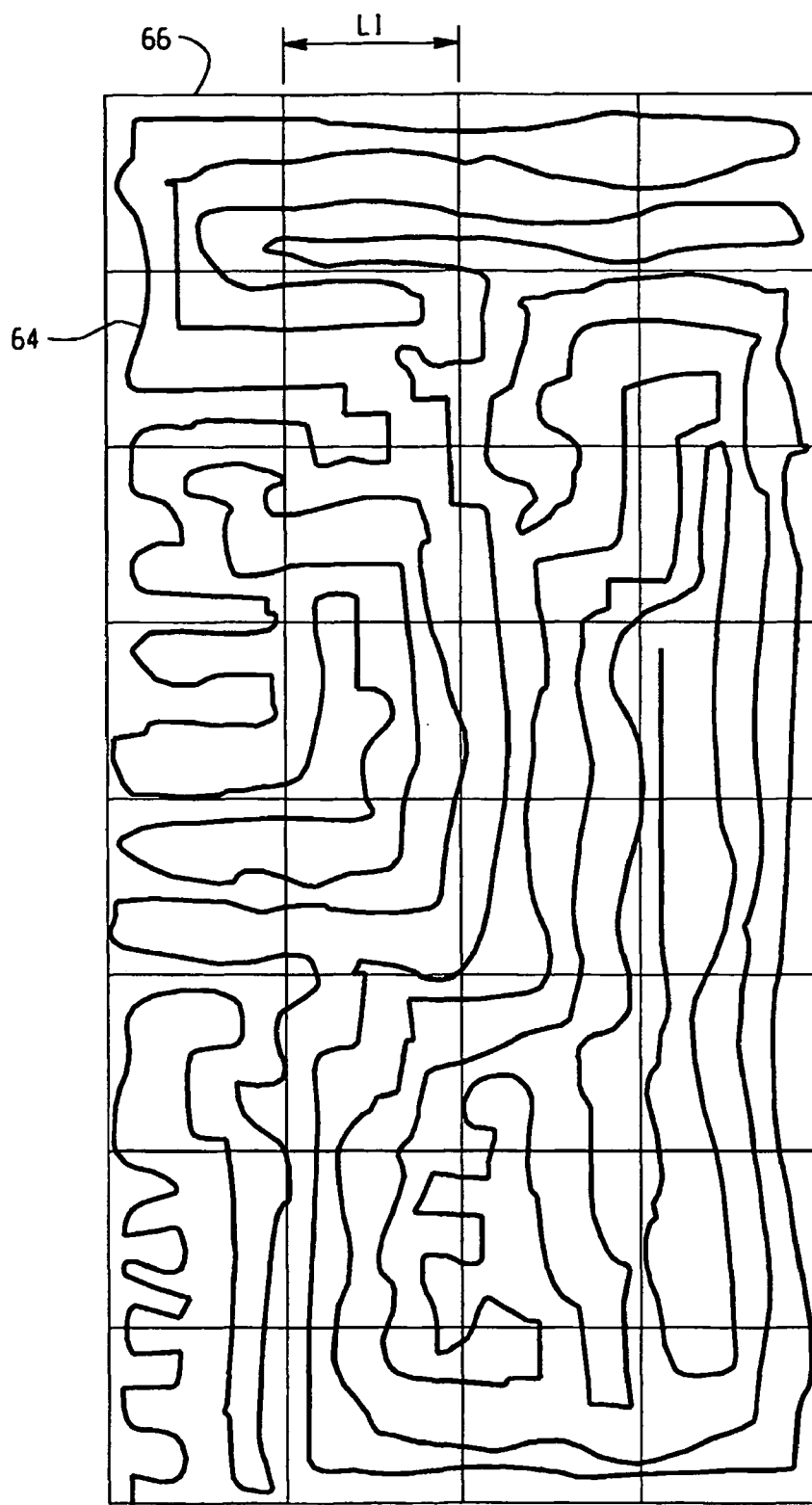


Fig. 7

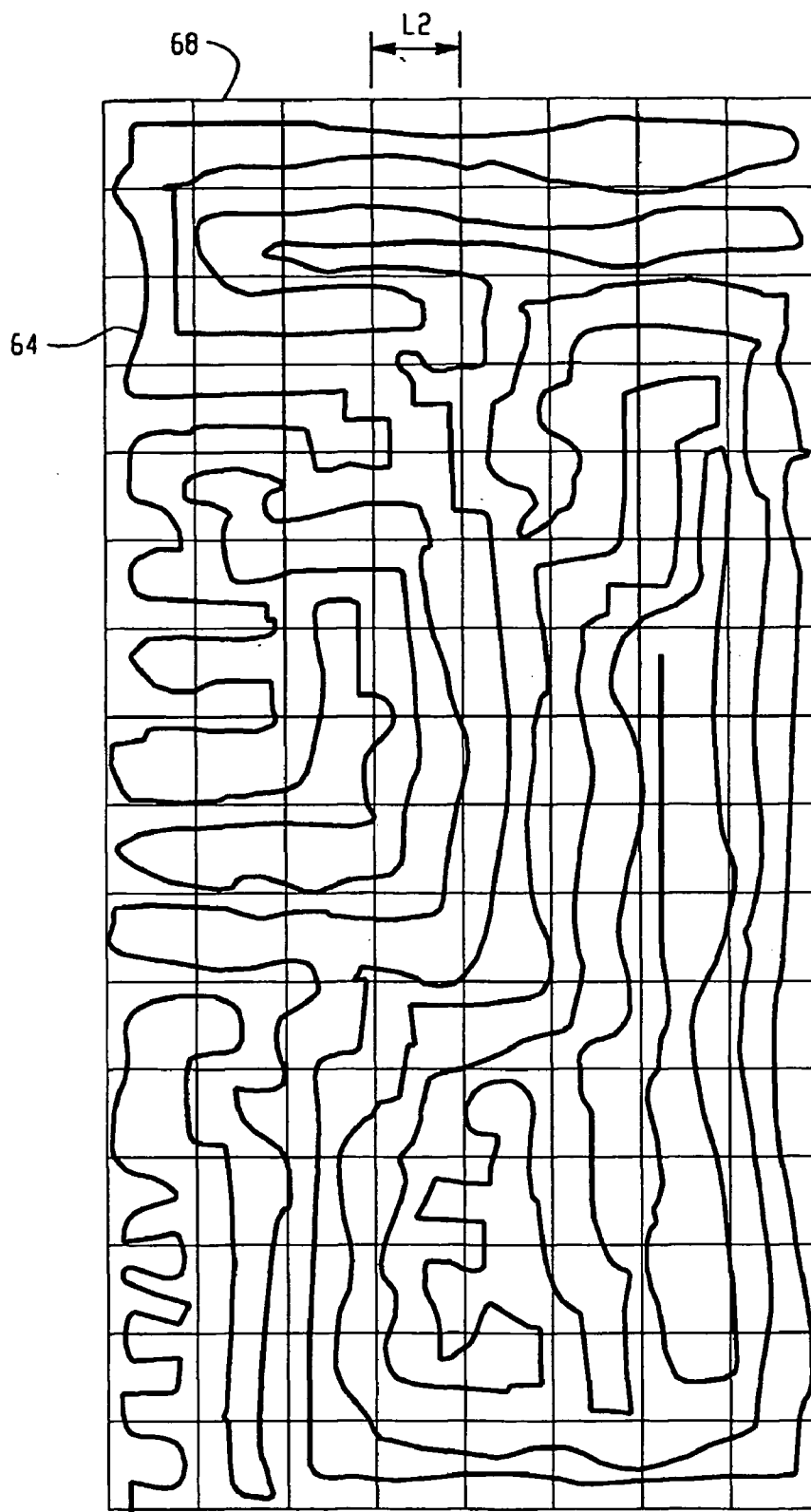


Fig. 8

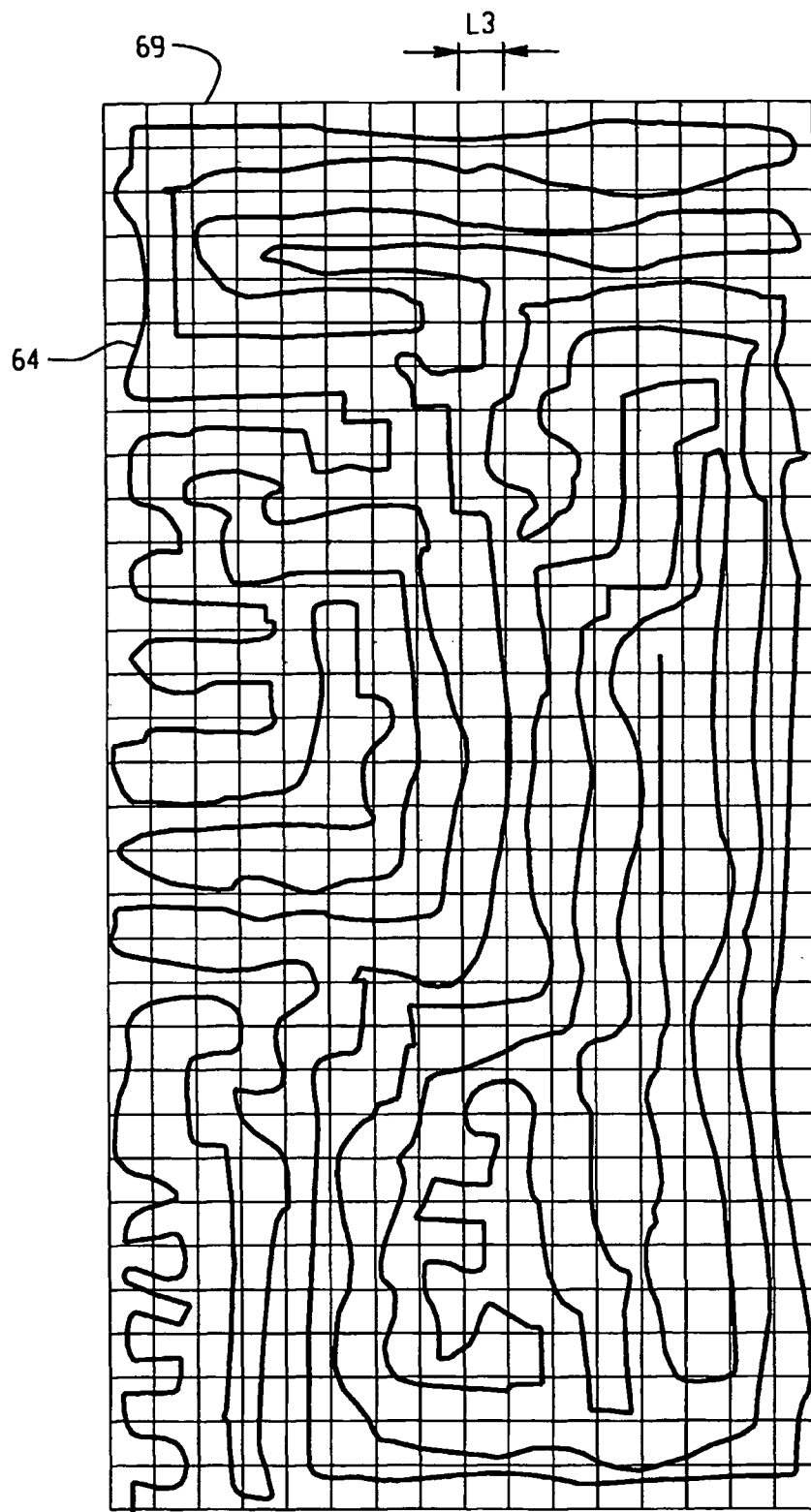


Fig. 9

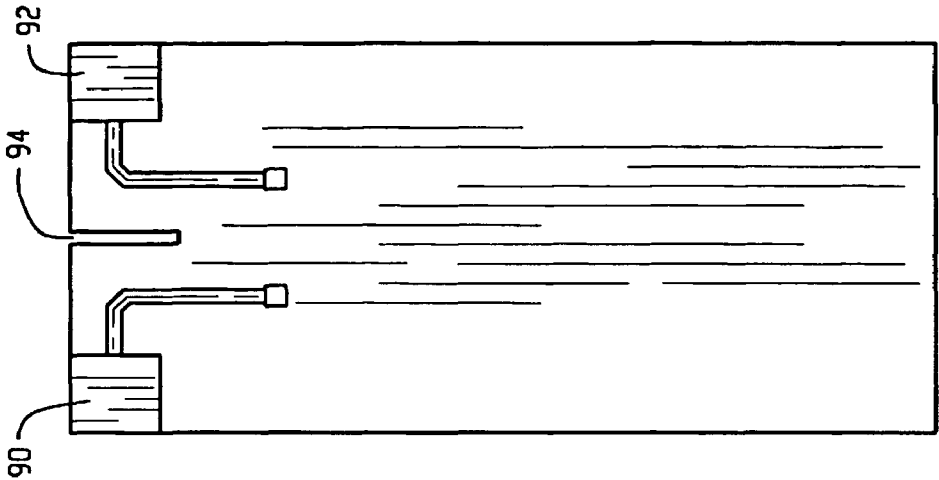


Fig. 11

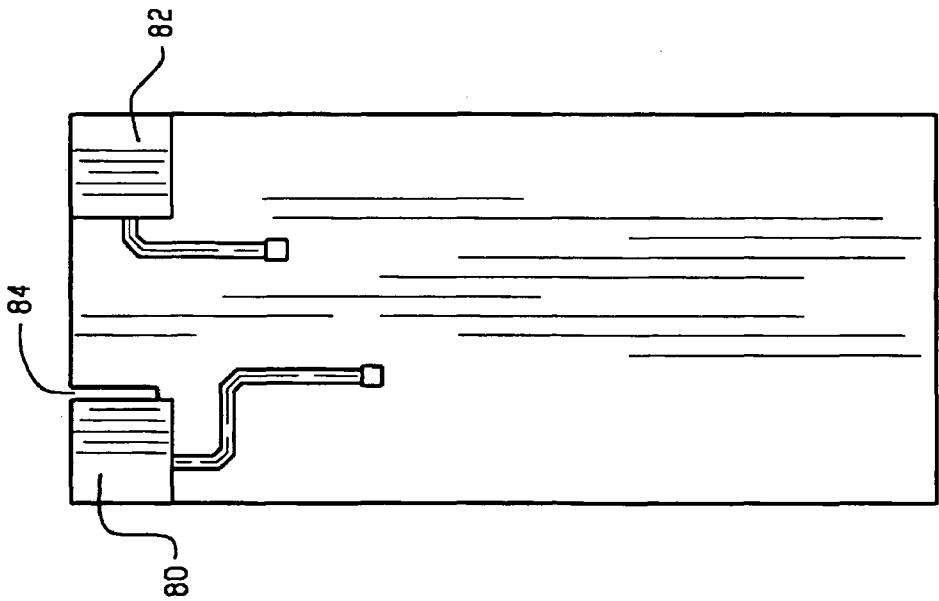


Fig. 10

MULTI-BAND MONOPOLE ANTENNAS FOR MOBILE NETWORK COMMUNICATIONS DEVICES

INTRODUCTION

[0001] This invention relates generally to the field of multi-band monopole antennas. More specifically, multi-band monopole antennas are provided that are particularly well-suited for use in mobile network communications devices, such as PCMCIA wireless cards, electronic devices with integrated WI-FI and WiMAX modules, compact flash wireless cards, wireless USB/UART dongles, and other wireless networking devices.

BACKGROUND

[0002] Multi-band antenna structures for use in a mobile network communications device are known in this art. In known wireless PCMCIA cards, for example, two dual-band antennas are typically used. The two antennas in a PCMCIA card, for example, are used with a diversity system in which the signal received from each antenna is compared and the best signal at any given time is used. A diversity system is particularly useful for indoor and multipath reception.

SUMMARY

[0003] Multiband monopole antennas are disclosed. The antennas disclosed can include a substrate for mounting conductors, a first conductor for receiving networking signals mainly in a first frequency band, and a second conductor for receiving networking signals mainly in a second frequency band. The first conductor can have a polygonal shape with an aspect ratio of length to width of less than about 5 to about 1. The second conductor can be linear, space-filling, or grid dimension. The first and second conductors can be connected at a feeding point.

[0004] The antennas disclosed can also include a substrate for mounting conductors, first and second conductors for receiving networking signals mainly in a first frequency band, and third and fourth conductors for receiving networking signals mainly in a second frequency band. The first and second conductors can be symmetrical polygonal shapes that have an aspect ratio of length to width of less than about 5 to about 1. The third and fourth conductors can be symmetrical linear, space-filling, or grid dimension shapes. The first and second conductors can be symmetrically oriented with respect to each other about a central axis on the antenna substrate and the third and fourth conductors can be symmetrically oriented with respect to each other about the central axis on the antenna substrate. The first, second, third and fourth conductors can be connected at a feeding point.

[0005] The antennas can be formed on simple, readily available circuit board materials as separate units or formed directly onto a printed circuit board. Two or more of the disclosed antennas can be used on a single printed circuit board. When two or more antennas are used with the same printed circuit board, the conducting material of the printed circuit board located between the antenna attachment points can be interrupted to improve the isolation of each antenna.

BRIEF DESCRIPTION OF THE DRAWINGS

[0006] FIG. 1 shows a top view of a multi-band monopole antenna for use in mobile network communications devices;

[0007] FIG. 2 shows a top view of another multi-band monopole antenna for use in mobile network communications devices;

[0008] FIG. 3 shows a top view of a non-symmetrical multibranch monopole antenna for use in mobile network communications devices;

[0009] FIG. 4 shows a top view of a symmetrical multi-branch monopole antenna for use in mobile network communications devices;

[0010] FIG. 5 shows one example of a space-filling curve;

[0011] FIGS. 6-9 illustrate an exemplary two-dimensional antenna geometry forming a grid dimension curve;

[0012] FIG. 10 shows a suggested cardbus PCB layout for use with the antenna shown in FIG. 4; and

[0013] FIG. 11 shows another suggested cardbus PCB layout for use with the antenna shown in FIG. 4.

DETAILED DESCRIPTION

[0014] Referring now to the drawing figures, FIG. 1 and FIG. 2 are top views of two exemplary multi-band monopole antennas for use in mobile network communications devices. The antennas of FIG. 1 and FIG. 2 include substrates (10, 20) and multibranch monopole conductors with the branches being connected at common points called feeding points (12, 22). The antenna substrates of FIG. 1 and FIG. 2 can, for example, be a 10 mm×10 mm×0.8 mm circuit board with a copper base conductor. The number of branches of a monopole antenna is directly related to the number of frequency bands or groups of bands that can be received. The antennas of FIG. 1 and FIG. 2 have two branches and are, thus, capable of receiving two different frequency bands. The branches of the antennas of FIG. 1 and FIG. 2 are non-symmetrical with the longer branch (14, 24) receiving a lower frequency band and the shorter branch (16, 26) receiving a higher frequency band. The length of the branches can be configured to receive signals specified in networking standards such as the 802.11b/Bluetooth standard (2.4-2.5 GHz) and the 802.11a band (4.9-5.875 GHz). Thus, the antennas of both FIG. 1 and FIG. 2 can be configured, for example, to receive both 802.11b/g band frequencies on the longer branch (14, 24) and 802.11a band frequencies on the shorter branch (16, 26). Coupling between branches in multibranch antennas is possible and such coupling can be taken into account during the design of the antenna. Further, services other than networking broadcast on these frequencies and the antennas disclosed herein can be used with those services as well.

[0015] Another multi-band monopole antenna design is shown in FIG. 3. The antenna of FIG. 3 is a non-symmetrical multibranch monopole. The antenna of FIG. 3 includes a substrate 30, a feeding point 32, and two conductor branches (34, 36). The shorter branch 34 is a polygonal monopole with notches (38, 40). The polygonal monopole could also have a multilevel shape such as that described in U.S. Patent Application Publication No. US 2002/0140615 A1, which is hereby incorporated by reference. The aspect ratio, i.e., the length compared to the width, of the shorter branch 34 of the polygonal monopole as depicted in FIG. 3 is about 3 to about 2. Preferably the aspect ratio is less than about 5 to about 1, more preferably the aspect ratio is less than about 3 to about

1, and even more preferably the aspect ratio is less than about 2 to about 1. The notches (38, 40) contribute to the antenna impedance match. One or more notches can be used, the length of each notch can vary, and, if more than one notch is used, the notches may be different lengths. A polygonal monopole can also have no notches. The longer branch 36 receives a lower frequency band and the shorter branch 34 receives a higher frequency band. The longer 36 and shorter 34 branches can be configured to receive network standard signals as discussed above with the antennas of FIG. 1 and FIG. 2.

[0016] Non-symmetrical antennas like the one shown in FIG. 3 are often designed for a specific printed circuit board (PCB) and, thus, are locked into a specific orientation on the PCB because the performance of the antenna can change with changes in the position, orientation, or identity of nearby circuitry. Symmetrical antennas on the other hand usually offer greater flexibility in terms of PCB placement because they are not as effected by changes in position, orientation, or identity of nearby circuitry.

[0017] Another multi-band monopole antenna is shown in FIG. 4. The antenna shown in FIG. 4 is a symmetrical multibranch monopole antenna. The antenna of FIG. 4 includes a substrate 50, a feeding point 52, and four conductor branches (54, 56, 58, 60). Each conducting branch has an opposing mirror image conducting branch that is symmetrical about a plane 61 that roughly divides the substrate 50 in half from top to bottom. The shorter branches (54, 56) are mirror images of each other with respect to plane 61 and the longer branches (58, 60) are mirror images of each other with respect to plane 61. The shorter branches (54, 56) are polygonal monopoles with notches as discussed above with respect to the antenna of FIG. 3. The longer branches (58, 60) receive a lower frequency band and the shorter branches (54, 56) receive a higher frequency band. The longer branches can be linear, space-filling, or grid dimension curves. The longer (58, 60) and shorter (54, 56) branches can be configured to receive network standard signals as discussed above with respect to the antennas of FIG. 1 and FIG. 2. Due to its symmetry, the antenna of FIG. 4 has greater flexibility in terms of PCB placement than the non-symmetrical antennas discussed above.

[0018] An example of a space-filling curve 62 is shown in FIG. 5. As used herein space-filling means a curve formed from a line that includes at least ten segments, with each segment forming an angle with an adjacent segment. When used in an antenna, each segment in a space-filling curve 62 should be shorter than one-tenth of the free-space operating wavelength of the antenna.

[0019] Examples of grid dimension curves are shown in FIGS. 6 to 9. The grid dimension of a curve may be calculated as follows. A first grid having square cells of length L1 is positioned over the geometry of the curve, such that the grid completely covers the curve. The number of cells (N1) in the first grid that enclose at least a portion of the curve are counted. Next, a second grid having square cells of length L2 is similarly positioned to completely cover the geometry of the curve, and the number of cells (N2) in the second grid that enclose at least a portion of the curve are counted. In addition, the first and second grids should be positioned within a minimum rectangular area enclosing the curve, such that no entire row or column on the perimeter of

one of the grids fails to enclose at least a portion of the curve. The first grid should include at least twenty-five cells, and the second grid should include four times the number of cells as the first grid. Thus, the length (L2) of each square cell in the second grid should be one-half the length (L1) of each square cell in the first grid. The grid dimension (D_g) may then be calculated with the following equation:

$$D_g = -\frac{\log(N2) - \log(N1)}{\log(L2) - \log(L1)}$$

[0020] For the purposes of this application, the term grid dimension curve is used to describe a curve geometry having a grid dimension that is greater than one (1). The larger the grid dimension, the higher the degree of miniaturization that may be achieved by the grid dimension curve in terms of an antenna operating at a specific frequency or wavelength. In addition, a grid dimension curve may, in some cases, also meet the requirements of a space-filling curve, as defined above. Therefore, for the purposes of this application, a space-filling curve is one type of grid dimension curve.

[0021] FIG. 6 shows an exemplary two-dimensional antenna 64 forming a grid dimension curve with a grid dimension of approximately two (2). FIG. 7 shows the antenna 64 of FIG. 6 enclosed in a first grid 66 having thirty-two (32) square cells, each with length L1. FIG. 8 shows the same antenna 64 enclosed in a second grid 68 having one hundred twenty-eight (128) square cells, each with a length L2. The length (L1) of each square cell in the first grid 66 is twice the length (L2) of each square cell in the second grid 68 (L2=2×L1). An examination of FIGS. 7 and 8 reveal that at least a portion of the antenna 64 is enclosed within every square cell in both the first and second grids 66, 68. Therefore, the value of N1 in the above grid dimension (D_g) equation is thirty-two (32) (i.e., the total number of cells in the first grid 66), and the value of N2 is one hundred twenty-eight (128) (i.e., the total number of cells in the second grid 68). Using the above equation, the grid dimension of the antenna 64 may be calculated as follows:

$$D_g = -\frac{\log(128) - \log(32)}{\log(2 \times L1) - \log(L1)} = 2$$

[0022] For a more accurate calculation of the grid dimension, the number of square cells may be increased up to a maximum amount. The maximum number of cells in a grid is dependent upon the resolution of the curve. As the number of cells approaches the maximum, the grid dimension calculation becomes more accurate. If a grid having more than the maximum number of cells is selected, however, then the accuracy of the grid dimension calculation begins to decrease. In some cases, the maximum number of cells is 100, but typically, the maximum number of cells in a grid is one thousand (1000).

[0023] For example, FIG. 9 shows the same antenna 64 enclosed in a third grid 69 with five hundred twelve (512) square cells, each having a length L3. The length (L3) of the cells in the third grid 69 is one half the length (L2) of the cells in the second grid 68, shown in FIG. 8. As noted above,

a portion of the antenna 64 is enclosed within every square cell in the second grid 68, thus the value of N for the second grid 68 is one hundred twenty-eight (128). An examination of FIG. 9, however, reveals that the antenna 64 is enclosed within only five hundred nine (509) of the five hundred twelve (512) cells in the third grid 69. Therefore, the value of N for the third grid 69 is five hundred nine (509). Using FIGS. 8 and 9, a more accurate value for the grid dimension (D_g) of the antenna 64 may be calculated as follows:

$$D_g = -\frac{\log(509) - \log(128)}{\log(2 \times L2) - \log(L2)} \approx 1.9915$$

[0024] The performance aspects of multi-band monopole antennas can be effected by the layout of the metal in the PCB where an antenna is mounted. As discussed above, antennas can be designed to work within particular PCB environments or a PCB can be optimized to work with a particular antenna design. The specific design of the antenna shown in FIG. 3, for example, makes it particularly well-suited for use with a cardbus PCB. To utilize the antenna shown in FIG. 3 with a cardbus PCB, two copies of the antennas shown in FIG. 3 could, for example, be mounted in the upper left corner and upper right corner of the cardbus PCB. FIGS. 10 and 11 show examples of two PCBs suitable for use with the antenna of FIG. 4. In FIG. 10 and FIG. 11, two copies of the antenna of FIG. 4, for example, could be mounted in the upper left corners (80, 90) and upper right corners (82, 92). The PCBs of FIG. 10 and FIG. 11 include slots (84, 94) in the upper portion of the PCB. The slots (84, 94) provide an interruption in or absence of conducting material between antenna attachment positions. The slots (84, 94) allow the adjustment of the electrical path of the currents and fields that propagate along the conductive edge. An interruption in or absence of conducting material between antennas mounted on a PCB increases each antenna's isolation from the other antenna thereby potentially improving performance. In addition to slots, other interruptions that can be used include, but are not limited to, holes, FracPlane™ ground plates (such as those described in U.S. Patent Application Publication No. US 2004/0217916 A1, which is hereby incorporated by reference), and periodic, quasi-periodic, space-filling, multi-level, and frequency selective geometries. Further, one or more interruptions can be used. FIGS. 10 and 11 show examples in which separate antenna components are mounted on a PCB. When an antenna is formed as a component separate from the PCB on which it will eventually be mounted, the substrate material used to make the antenna can be simple, readily available printed circuit board material. Further, directly forming an antenna on a particular PCB is also possible. In some embodiments, the antenna is formed directly on a substrate or laminate of an integrated circuit package including other electronic or radio frequency (RF) components or semiconductor dies.

[0025] This written description uses examples to disclose the invention, including the best mode, and also to enable a person skilled in the art to make and use the invention. The patentable scope of the invention is defined by the claims, and may include other examples that occur to those skilled in the art. Such other examples, which may be available either before or after the application filing date, are intended

to be within the scope of the claims if they have structural elements that do not differ from the literal language of the claims, or if they include equivalent structural elements with insubstantial differences from the literal language of the claims.

1. A multi-band monopole antenna, comprising:

an antenna substrate;

a first conductor for receiving networking signals in the frequency range of about 4.9 GHz to about 5.875 GHz, said first conductor having a polygonal shape with an aspect ratio of length to width of less than about 5 to about 1;

a second conductor for receiving networking signals in the frequency range of about 2.4 GHz to about 2.5 GHz, said second conductor adopting a linear, space-filling, or grid dimension shape; and

a feeding point for connecting the first conductor and the second conductor.

2. The multi-band monopole antenna of claim 1, wherein the first conductor has one or more notches where material is removed from the polygonal shape for matching the impedance of the antenna.

3. The multi-band monopole antenna of claim 1, wherein the first conductor has an aspect ratio of less than about 3 to about 1.

4. The multi-band monopole antenna of claim 1, wherein the first conductor has an aspect ratio of less than about 2 to about 1.

5. The multi-band monopole antenna of claim 1, wherein the first conductor has an aspect ratio of about 3 to about 2.

6. The multi-band monopole antenna of claim 1, wherein the first conductor receives network signals in the 802.11a band.

7. The multi-band monopole antenna of claim 1, wherein the second conductor receives network signals in the 802.11b band.

8. The multi-band monopole antenna of claim 1, wherein the substrate comprises a 10 mm×10 mm×0.8 mm circuit board with a copper base conductor.

9. A printed circuit board comprising one or more of the multi-band monopole antennas of claim 1.

10. The printed circuit board of claim 9, wherein two or more multi-band monopole antennas are used and conducting material of the printed circuit board located between the antenna attachment points is interrupted.

11. A symmetrical multi-band monopole antenna, comprising:

an antenna substrate;

first and second conductors for receiving networking signals in the frequency range of about 4.9 GHz to about 5.875 GHz, said first and second conductors having symmetrical polygonal shapes with an aspect ratio of length to width of less than about 5 to about 1;

third and fourth conductors for receiving networking signals in the frequency range of about 2.4 GHz to about 2.5 GHz, said third and fourth conductors adopting symmetrical linear, space-filling, or grid dimension shapes; and

a feeding point for connecting the first, second, third and fourth conductors,

wherein the first and second conductors are symmetrically oriented with respect to each other about a central axis on the antenna substrate and the third and fourth conductors are symmetrically oriented with respect to each other about the central axis on the antenna substrate.

12. The symmetrical multi-band monopole antenna of claim 11, wherein the first and second conductors have one or more notches where material is removed from the polygonal shape for matching the impedance of the antenna.

13. The symmetrical multi-band monopole antenna of claim 11, wherein the first and second conductors each have an aspect ratio of less than about 3 to about 1.

14. The symmetrical multi-band monopole antenna of claim 11, wherein the first and second conductors each have an aspect ratio of less than about 2 to about 1.

15. The symmetrical multi-band monopole antenna of claim 11, wherein the first and second conductors each have an aspect ratio of about 3 to about 2.

16. The symmetrical multi-band monopole antenna of claim 11, wherein the first and second conductor receives network signals in the 802.11a band.

17. The symmetrical multi-band monopole antenna of claim 11, wherein the second and third conductors receive network signals in the 802.11bg band.

18. The symmetrical multi-band monopole antenna of claim 11, wherein the substrate comprises a 10 mm×10 mm×0.8 mm circuit board with a copper base conductor.

19. A printed circuit board comprising one or more of the symmetrical multi-band monopole antennas of claim 11.

20. The printed circuit board of claim 9, wherein two or more symmetrical multi-band monopole antennas are used and conducting material of the printed circuit board located between the antenna attachment points is interrupted.

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