

Lecture 16

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- CMOS Inverter
 - Only dissipates dynamic power during switching
 - * No static power consumption (same applies to other CMOS gates)
- Series connected transistors become “or-ed” and parallel connected transistors become “and-ed”
- De Morgan’s Laws
 - For a given boolean expression, an equivalent expression can be formed by performing all of the following steps
 1. Replacing all variables by their inverses
 2. Replacing AND operations by OR operations (or vice versa)
 3. Inverting the complete expression