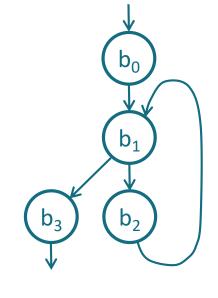
Branch prediction



Impact of branch prediction on the execution times of basic blocks

(2)

How is the direction predicted?
How is the target address predicted?

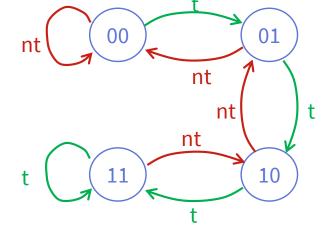




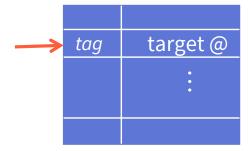




10



Branch Target Buffer (BTB)





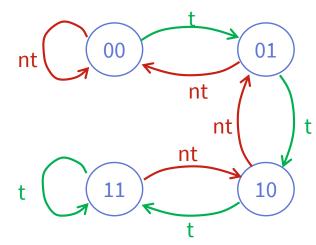
Semantics-based analysis

Pattern -	Initial counter state										
Pattern	00	01	10	00							
Tm											
N ^m											
(TN) ^m											
(NT) ^m											
(N ⁿ T) ^m , n=2											
(N ⁿ T) ^m , n>2											
(T ⁿ N) ^m , n=2											
(T ⁿ N) ^m , n>2											



Semantics-based analysis

• (TN)^m from state=01

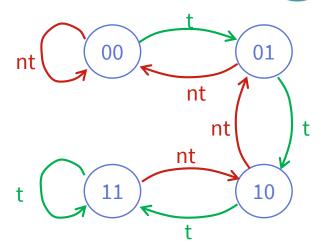


state	01							
predicted dir°								
actual dir°	Т	N	Т	N	Т	N	Т	N
mispred?								



Semantics-based analysis

• (TN)^m from state=01



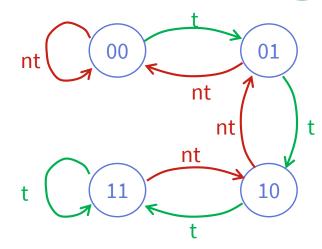
state	01	10	01	10	01	10	01	10
predicted dir°	N	Т	N	Т	N	Т	N	Т
actual dir°	Т	N	Т	N	Т	N	Т	N
mispred?	Х	Х	X	Х	Х	Х	Х	Х



Semantics-based analysis

• $(N^nT)^m (n>2)$ from state=10

```
for (int i=0; i<m; i++)
for (int j=0; j<n; j++)
...
```



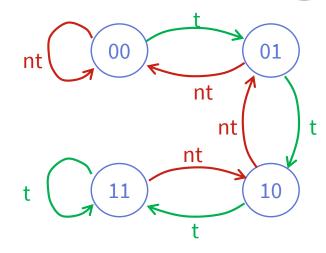
state	10					
predicted dir°						
actual dir°						
mispred?						



Semantics-based analysis

• (NⁿT)^m (n>2) from state=10

```
for (int i=0; i<m; i++)
for (int j=0; j<n; j++)
...
```



state	10	01	00	00	00	01	00	00	00	00
predicted dir°	Т	N	N	N	N	N	N	N	N	N
actual dir°	N	N	N	N	Т	N	N	N	N	Т
mispred?	Х				Х					Х



Semantics-based analysis

Pattern	Initial counter state								
Pattern	00	01	10	00					
T ^m	2	1	0	0					
N ^m	0	0	1	2					
(TN) ^m	m	2m	m	m					
(NT) ^m	m	m	2m	m					
(N ⁿ T) ^m , n=2	m	m	1+m	3+m					
(N ⁿ T) ^m , n>2	m	m	1+m	2+m					
(T ⁿ N) ^m , n=2	3+m	1+m	m	m					
(T ⁿ N) ^m , n>2	2+m	1+m	m	m					

Summary...



