REFERENCE INPUT..... 8V RMS, 2500Hz to 3500Hz

±7.5V RMS L-L OUTPUT

OUTPUT LOAD..... 2K MIN.

D.C. SUPPLY REQ'D..... +5V @ 12ma MAX (8ma TYP) (REGULATED) +12V @ 30ma MAX (20ma TYP)

-12V @ 30ma MAX (20ma TYP)

GND

RESOLUTION....... 14 BITS

ACCURACY..... +.05% @ F.S. TYPICAL, 1% WORST CASE

DIGITAL INPUT CODES: (+FS OUT = RL-RH)

SCALE	OFFSET BINARY
	MSB LSB
+FS - 1 LSB	11111111111111
+3/4 FS	1110000000000
+1/2 FS	11000000000000
+1/4 FS	1010000000000
0	1000000000000
-1/4 FS	0110000000000
-1/2 FS	0100000000000
-3/4 FS	0010000000000
-FS +1 LSB	0000000000001
-FS	000000000000

ENABLE INPUT ("-E" UNITS ONLY)..... LOGIC "O" = INPUT DATA LOAD (TTL COMPATIBLE LOGIC)

MECHANICAL..... 2.625 x 3.125 x .52" OR .60" (SEE DWG A6510)

OTHER VOLTAGES & FREQUENCIES AVAILABLE.

-2/5-" = 2VL-L OUTPUT SCALE: 5VRMS REF

OV	NPI	JT	ER	\Box	JV	ÆRS	BYONE	•

6 DUNTON COURT - EAST NORTHPORT, N.Y. 11731 - 516 261-3300

DATE: 4-9-96

DRAWN BY: MARAGUE

14 BIT DIGITAL TO 3 WIRE LVDT CONVERTER MODULE "S3LVDT414-13/26-1"

SPECIFICATIONS

DRAWING NUMBER A.6.511