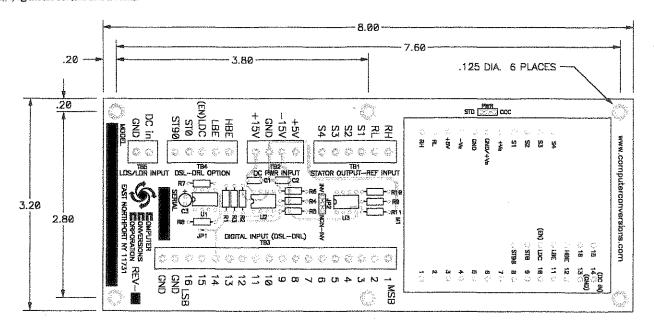
Computer Conversions Corporation, claims proprietary rights to the moterial stracticed herer. This drowing is issuad in confidence for regimeering information only and may sat be reproduced or used to moniforture optifie



REVISIONS		
DATE	DESCRIPTION	REV
12-19-82 DV	ORIGINAL RELEASE	
71784 DV	ADDED OPTIONAL INPUT OFFSET CIRCUIT.	Α
8-16-94 DV	REVISED LINE TO U1 PIN 2	В
9-14-98 DV	ADDED U3	С

COMPONENT SIDE

NOTES:

- 1. SEE SEPARATE PARTS LIST....L/M A7442 REV- C .
- 2. PLASTIC SNAP -TRAK MOUNTING OPTIONAL.
- 3. "ST-DSL/DRL" UNITS: INSTALL TB3 & OMIT TB5,
- 4. "ST-DSL/DRL" UNITS WITH SPECIAL OPTION: USE TB5
- 5. "ST-LDS/LDR" UNITS: OMIT TB3, TB4 & INSTALL TB5.
- 6. -12 SUFFIX OPTION: +/-15V INPUTS ARE +/-12V.
- 7. INSTALL PWR JUMPER: STD or CCC AS REQD.
- 8. "ST-LDS/LDR" UNITS WITH STANDARD INPUT: INSTALL JP1 AND OMIT U1 U2, C1 C3, R1 R8. "ST-LDS/LDR" UNITS WITH OPT OFFSET INPUT: OMIT JP1 AND INSTALL U1, U2, C1 C3, R1 R8.



SCALE: FULL FSCM: 51086 DWN: D.Varrone
DATE: 12-19-02 FSCM: 51086

"SNAP-TRAK" LDS/LDR MOTHER BOARD

P.C. BD. ASSEMBLY

B7442