

CAN RECEIVER

Adrian Gongora Angel Sanchez



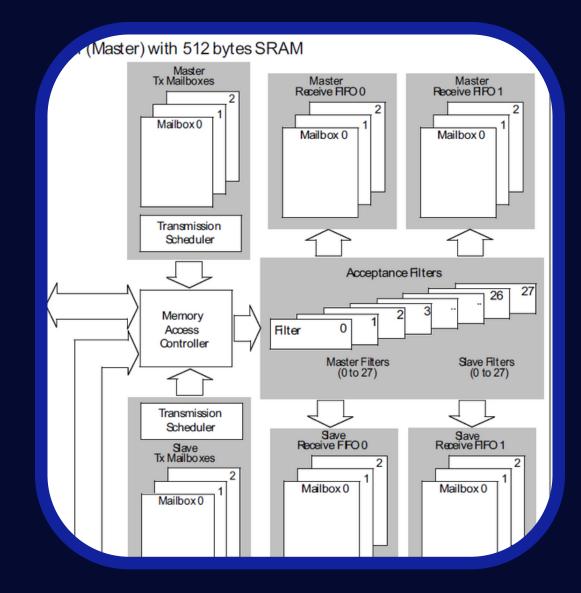
FEATURES

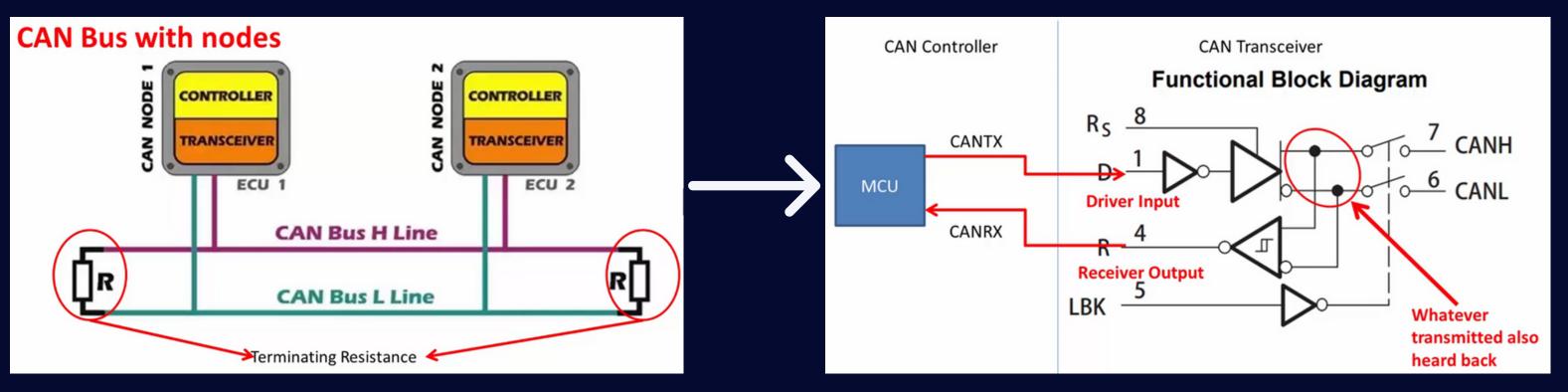
- 2 recieve FIFOs with 3 stages
- Scalable filter banks:
 - 28 filter bakns shared between CAN1 and CAN2
- Identifier list feature
- Configurable FIFO overrun
 - Time Stamp on OF reception

RXPATH

To understand reception we need to know the **PATH** that a message takes from the **BUS** to the **MCU**.

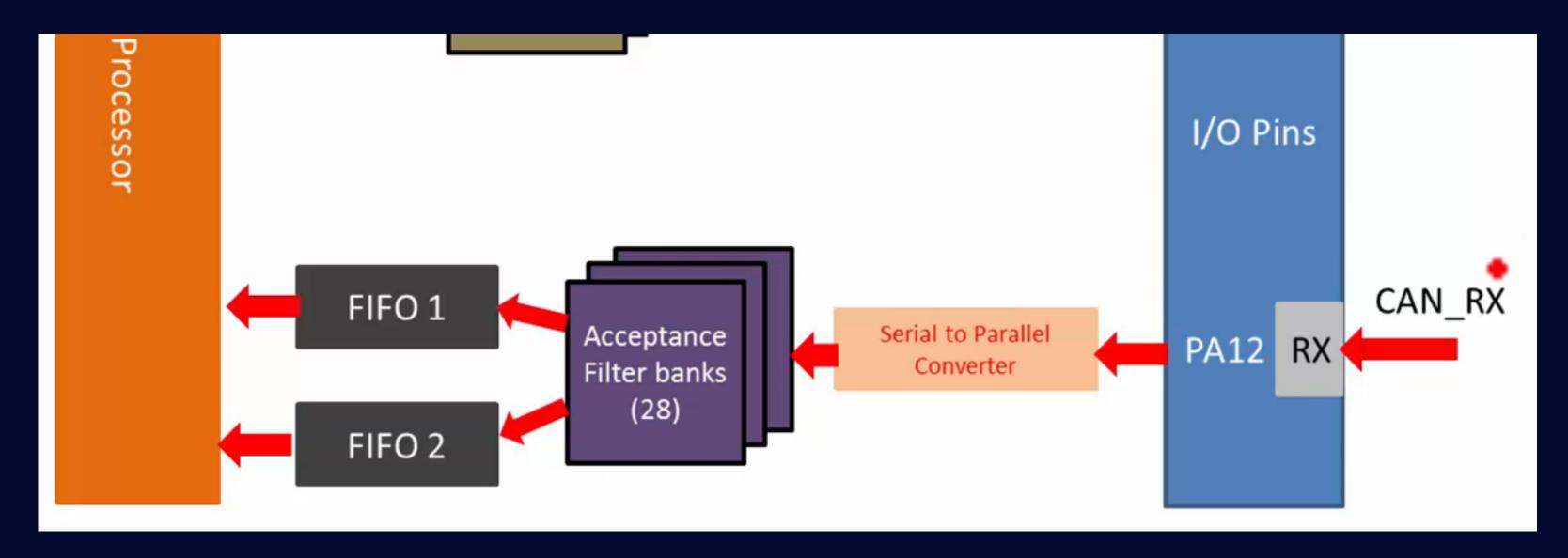
CAN BUS -> TRANSRECIVER -> CAN CONTROLLER (bxCAN)







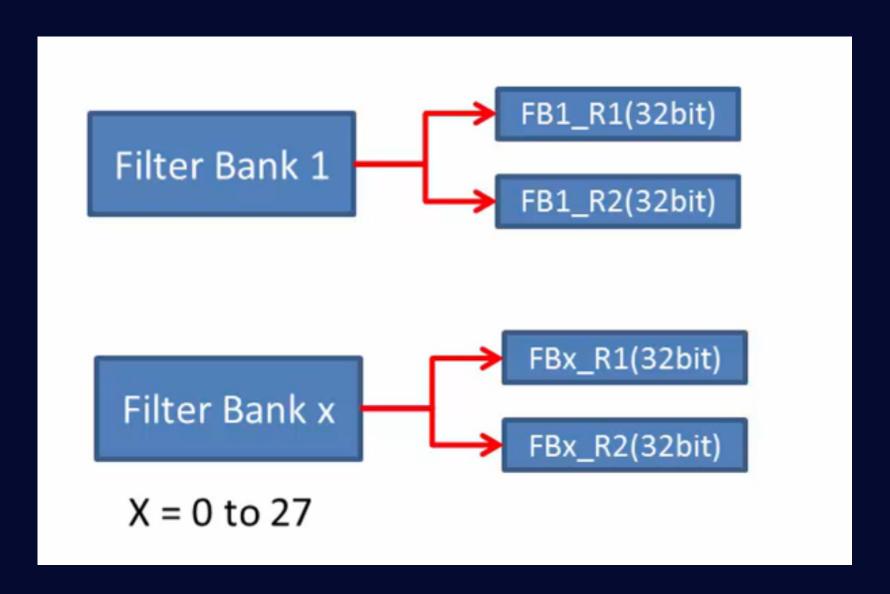
The bxCAN RX PATH - Filtering



Filter banks is a hardware mechanism that can be programmed to ignore and discard certain CAN frames and allow other to pass on to FIFOs.

There are 28 filter banks for CAN1&2 each with 2 32-bit registers

The bxCAN RX PATH - Filtering



There are 28 filter banks for CAN1&2 each with two 32-bit registers

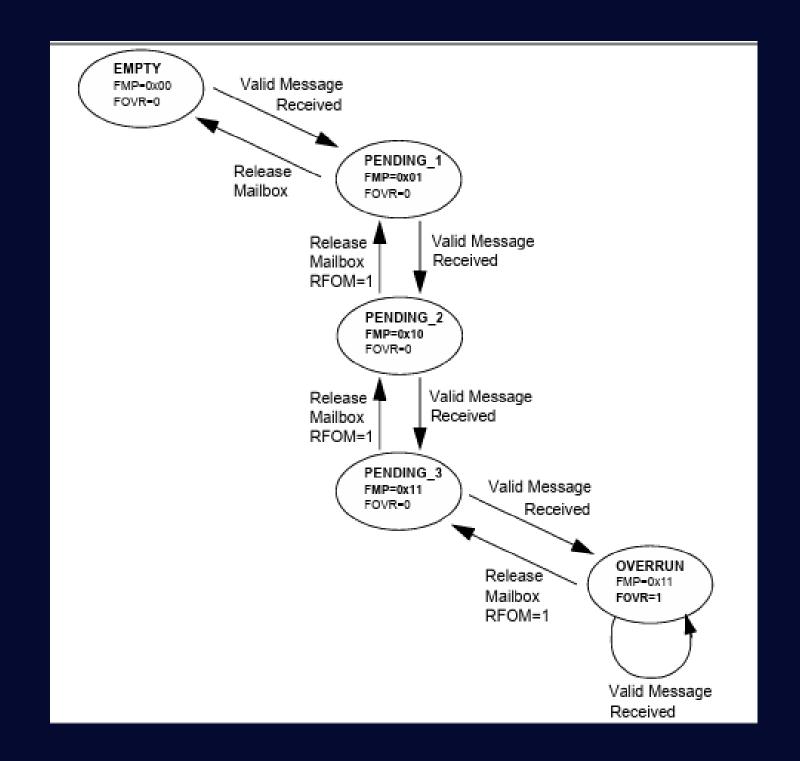
Examples:

Accept only extented id frames

Accept only indentifiers with hex value 0x02



- For recieving we have 2 FIFOs (First in-First Out), these are used to store incomming messages.
- 3 complete messages can be stored at each
- Managed completely by hardware.
- We can say that a message is valid when recieved according to the CAN protocol.
- The application accesses the messages stored in the FIFO throught the FIFO output mailbox.
- The important bit is the acknowledge field



- Reception Handling is more complex than the Transmission, we have the 3 mailboxes organized by the FIFO and the Acceptance filters.
- The acknowledge field is controlled by the recieving node, the state of this field will tell us the outcome.

Data Frame Format																													
١	<u></u>								-						•	•													ļ
	Arbitration ID					Data Length Code (DLC) Data						CRC Field							End of Frame							l			
	e.	-				•		—			*						_		\rightarrow	Bit	miter		_	_	-	×			l
	Start of Frame	D10 1D9 1D7	D 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	ID2 ID1 ID0 Requ. Remote	IDE	Daviesex	DL3 DL1 DL2	DB7	DB5 DB4	DB3 DB1 DB1	CRC14	CRC13 CRC12	CRCII	CRC8	CRC6	CRC4	CRC2	CRCI	CRC Delimiter	Acknow. Slot Bit	Acknow. Delimite	EOF6	EOF4	EOF2	EOFI	FS2	IFSI	IFSO	
DATA	0	00000	010	1000	0 0	0		0 (00	000	0	10	000	11	0 0	000	0	00	1	0	1	1	11	1 1	11	ı	1	1]
CAN HI	<u></u>				w	~~~		5						1		~~	~~	~~					~~~	_~	~~~	·~	v		_
CAN LO	L		\int		w~-										1	~~	~ ~	~~			~~	····				~~	, elfer	*	*

Offset to receive mailbox base address (bytes)	Register name
0	CAN_RIXR
4	CAN_RDTxR
8	CAN_RDLxR
12	CAN_RDHxR

- When a message is stored, it becomes available in the FIFO output mailbox.
- Once the software handle the message, the software must release the FOM, activating the CAN_RFR Reg to make the incoming message available.

