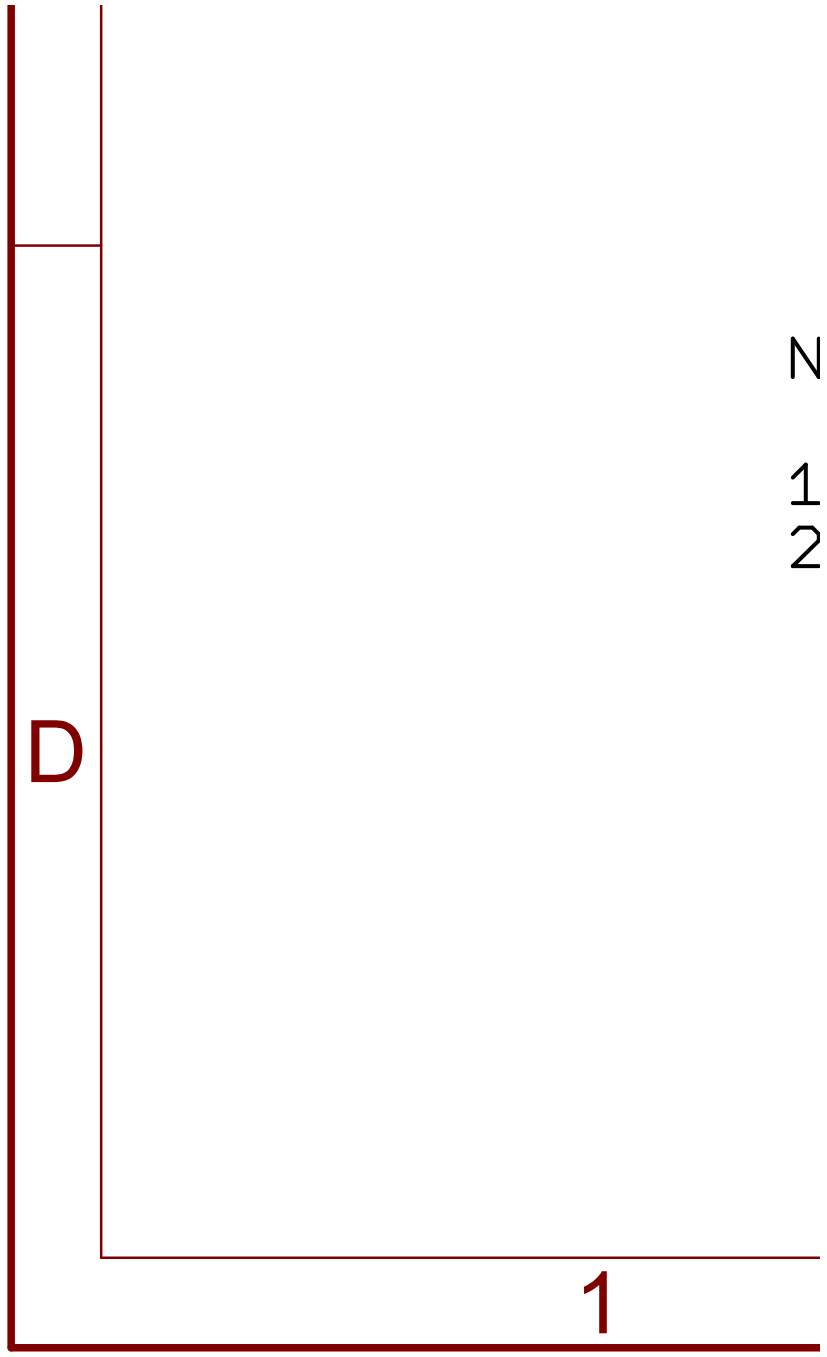

D
1
N
1
2

lotes:

. D1 and D2 are Blue LEDs they might not
. When VCC is less than 5V change the sy

t light when using Vi_BAT <4V~
stem clock of the ATTINY85 to 8MHz or less

s (refer to the datasheet P163 Section 2

T.A.M.I hackerspace crew
LICENSE: CERN OHL 1.7

ATT
12/
She

5

:1-3 Speed>

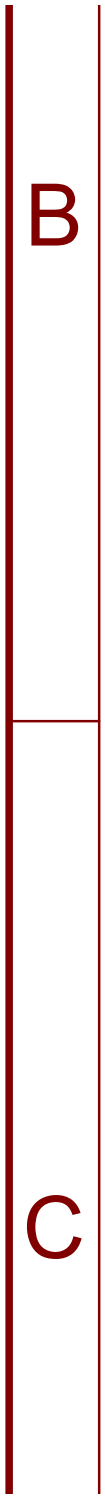
D

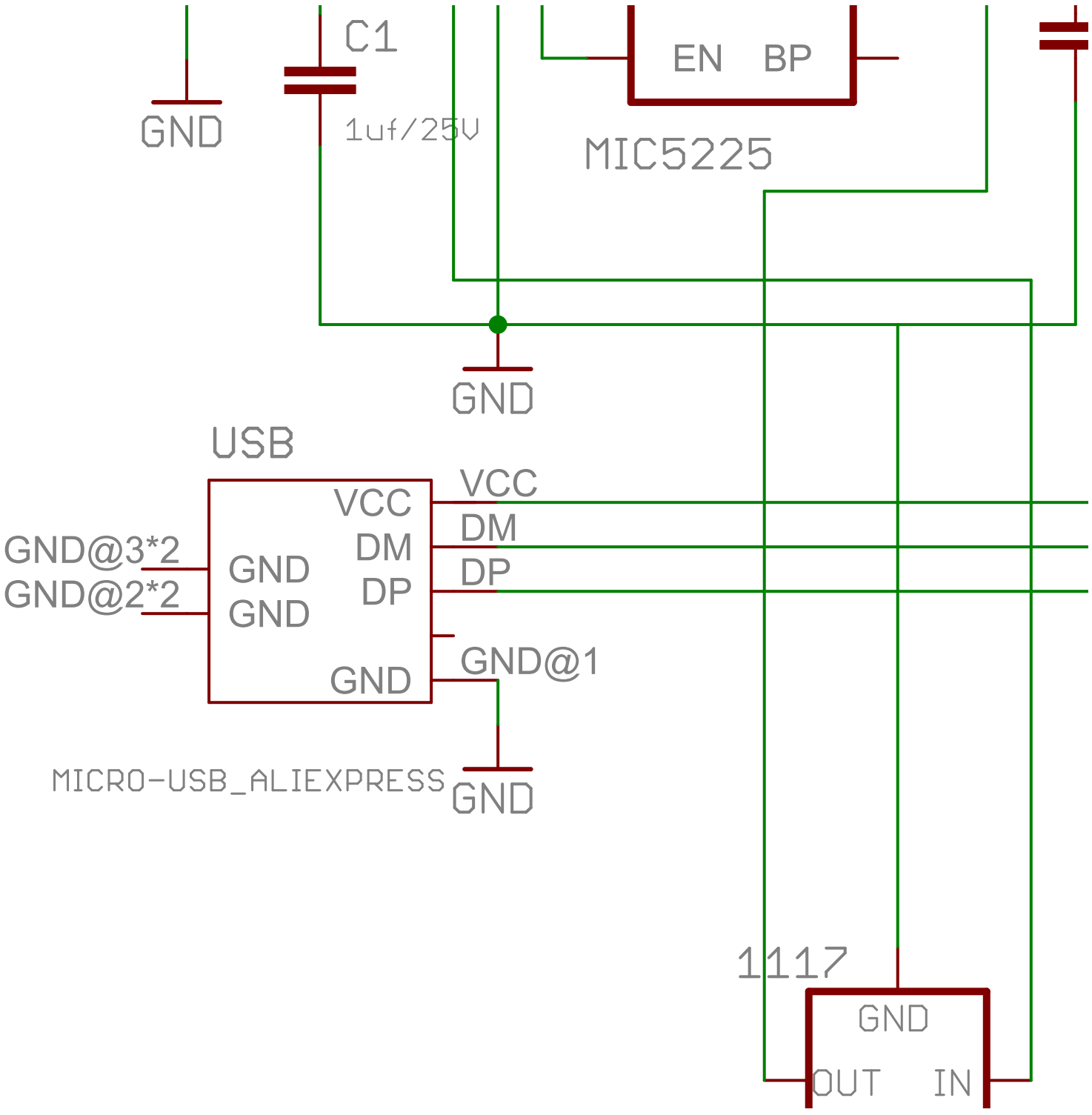
AMI-V0.00

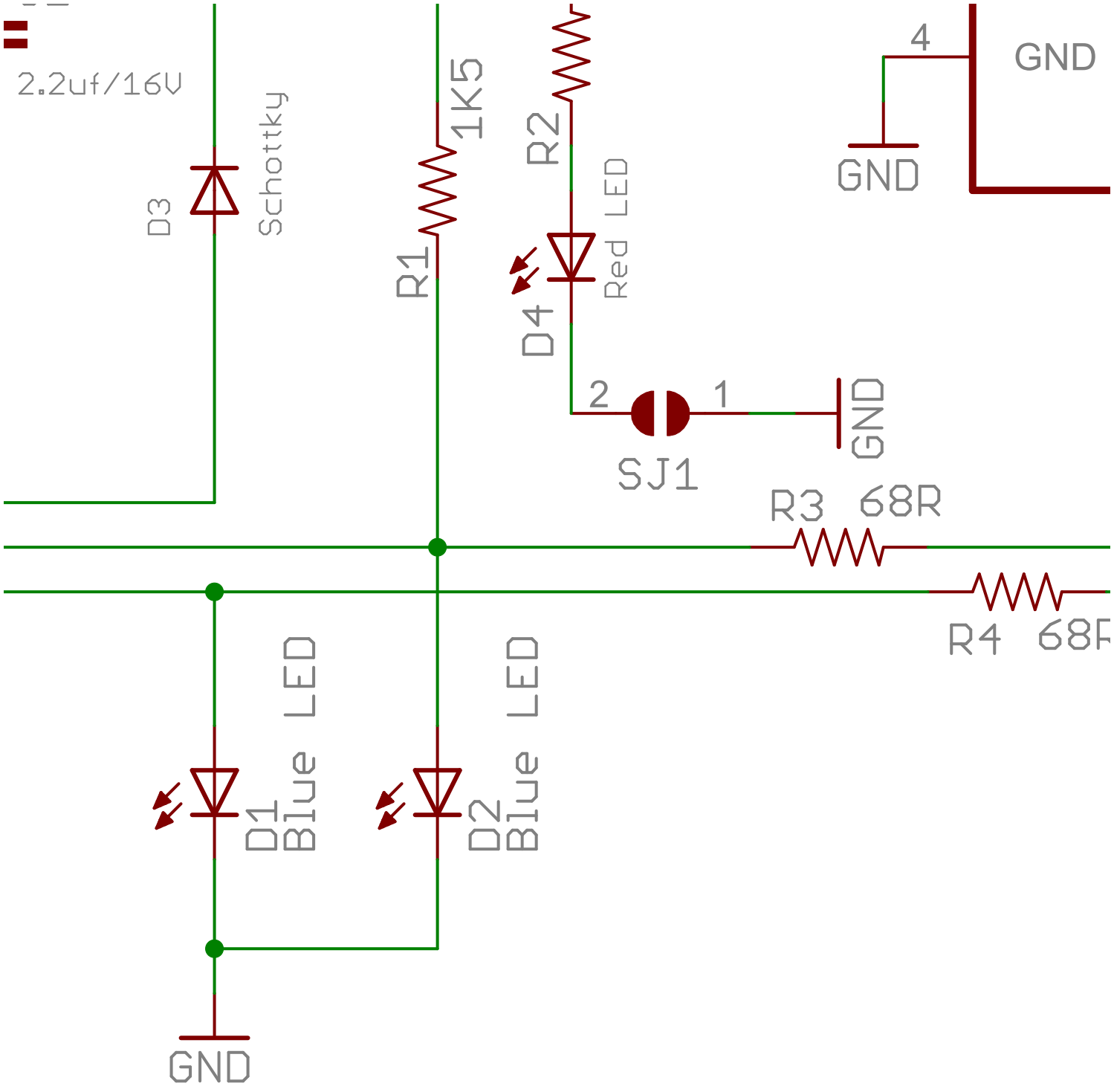
01/2015 00:20:52

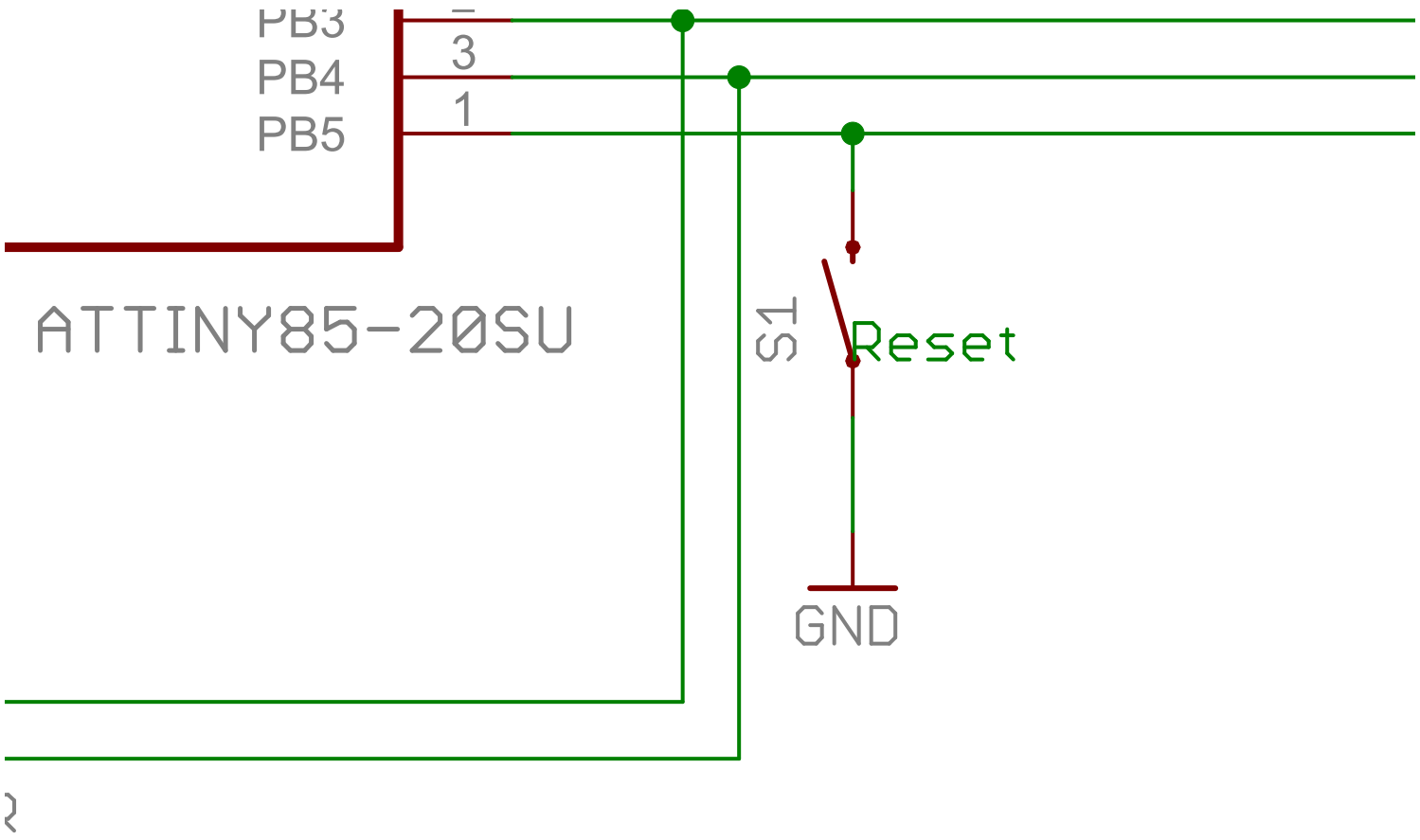
et: 1/1

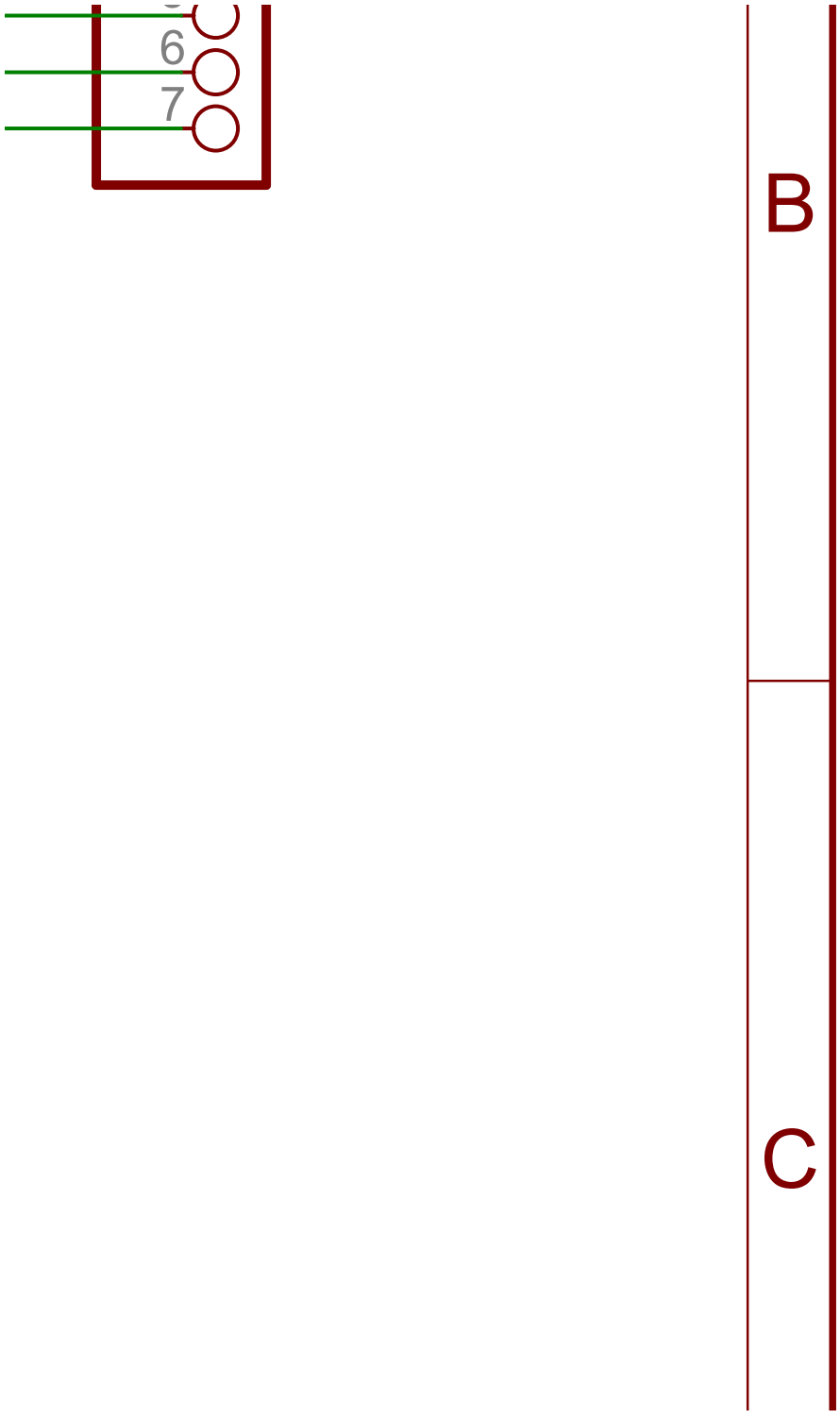
6

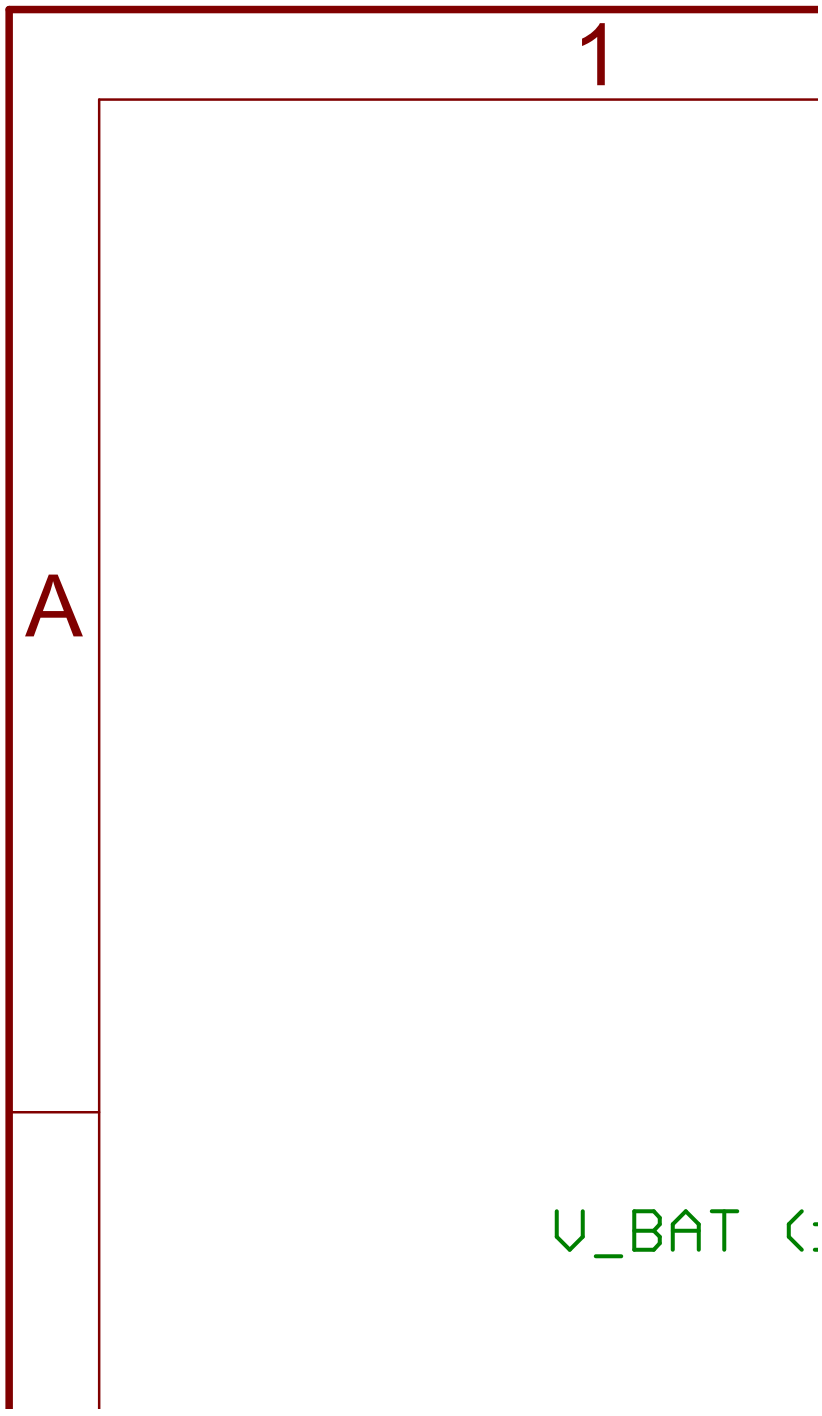

B
C

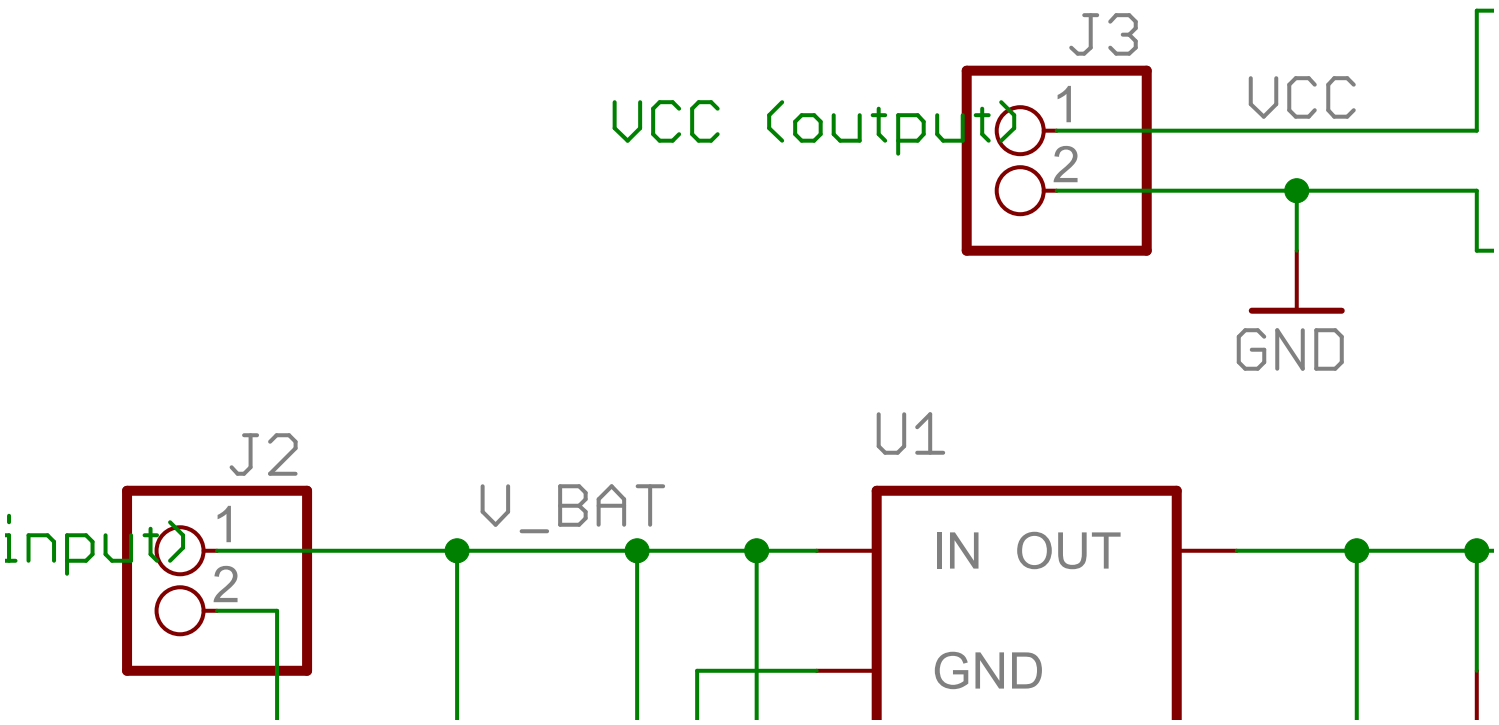

C1
1uf/25V
GND
EN BP
MIC5225
GND
USB
VCC
DM
DP
GND@1
GND@3*2
GND@2*2
GND
GND
GND
MICRO-USB_ALIEXPRESS
GND
VCC
DM
DP
1117
GND
OUT IN

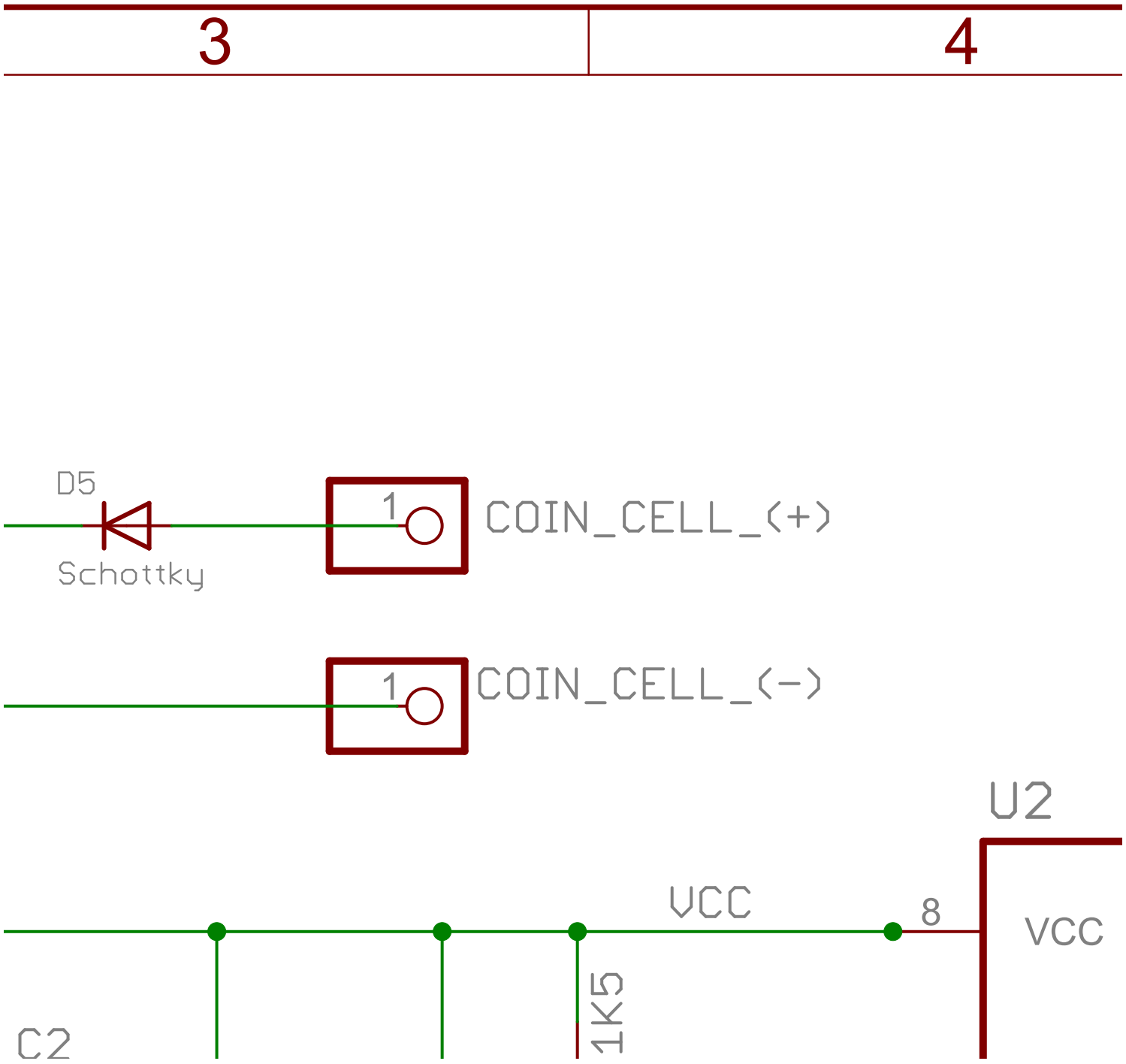

2.2uf/16V
D3
Schottky
R1
1K5
R2
D4
Red LED
4
GND
GND
2
1
GND
SJ1
R3 68R
R4 68R
D1
Blue LED
D2
Blue LED
GND

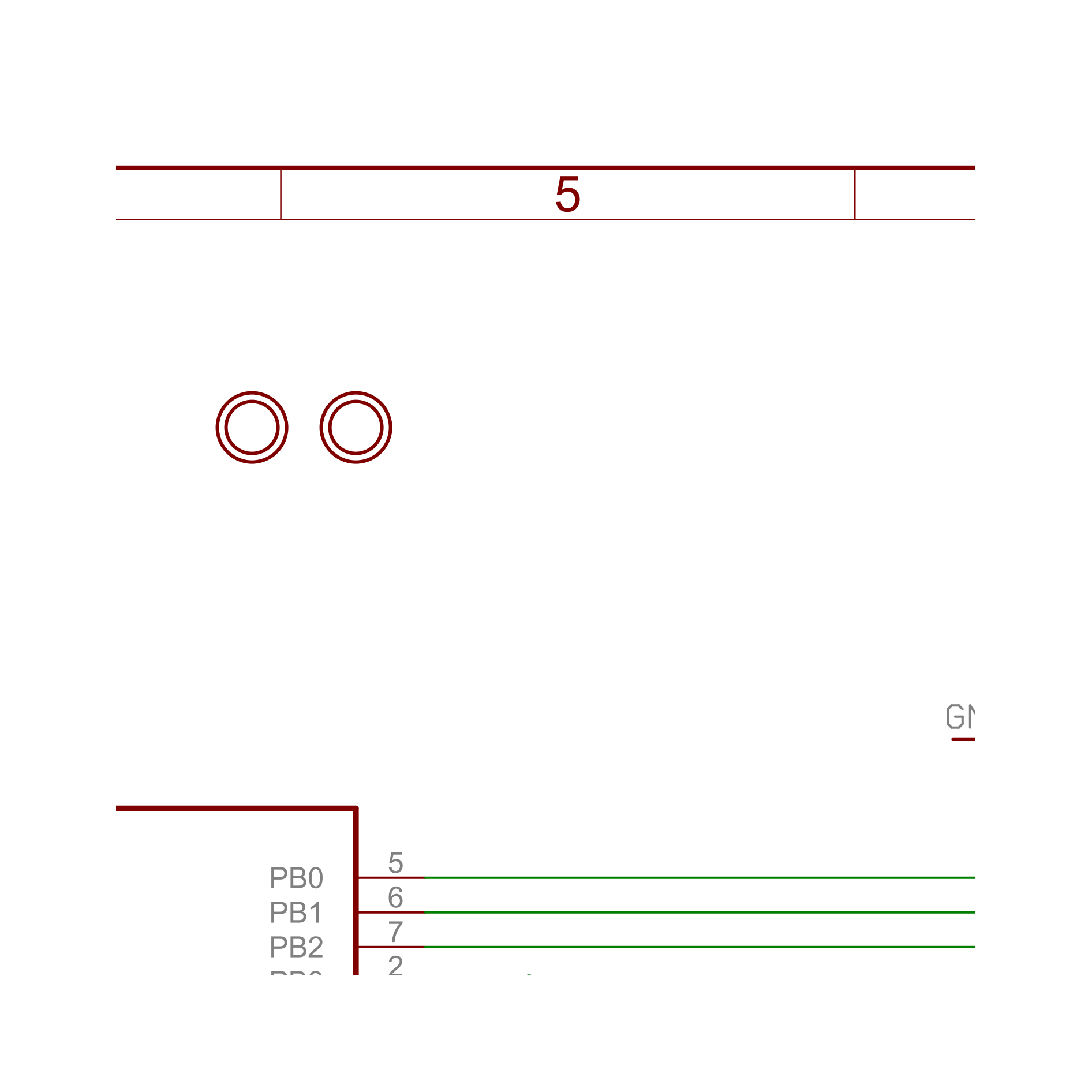

PB3
PB4
PB5
3
1
ATTINY85-20SU
S1
Reset
GND

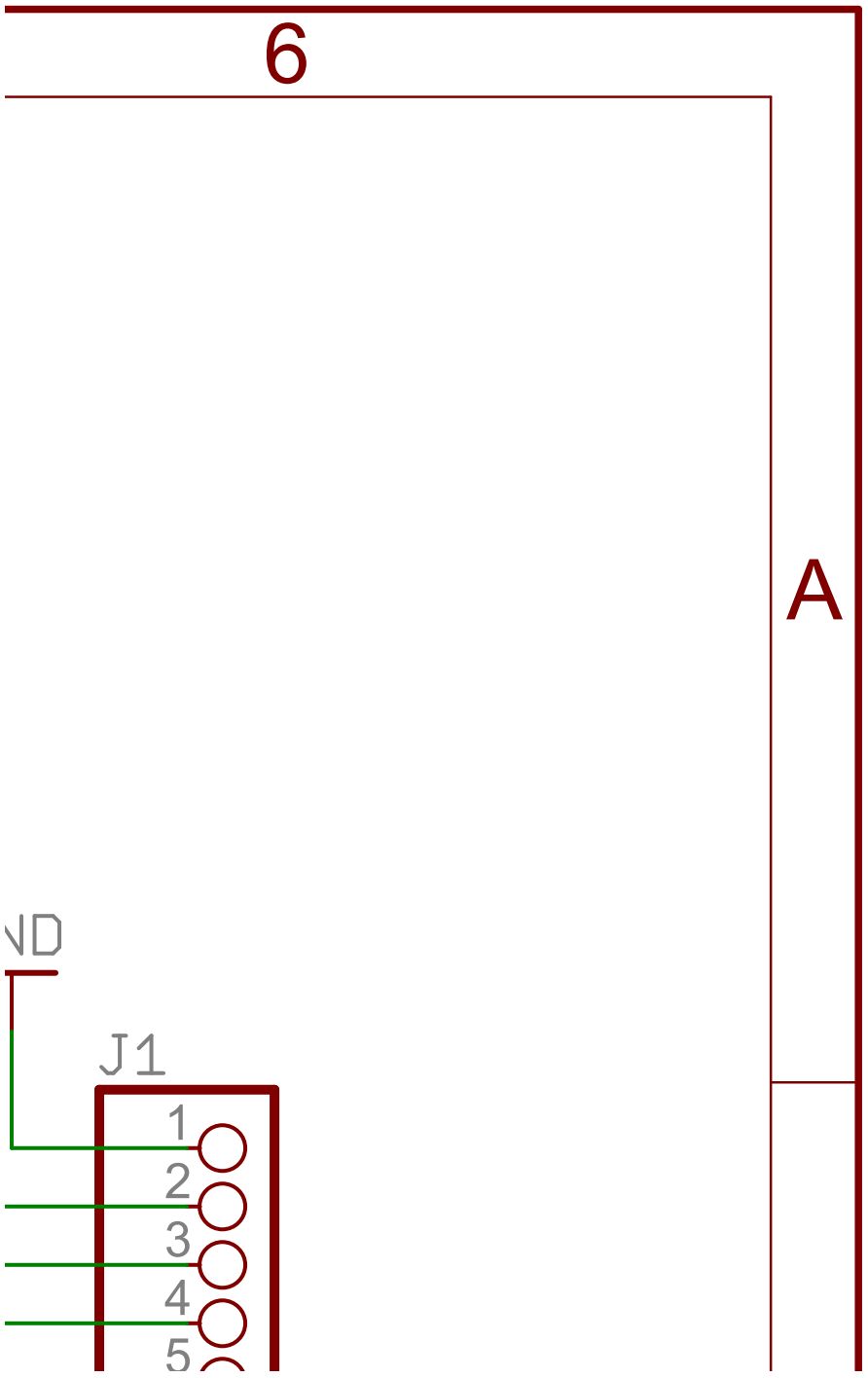

6
7
B
C


1
A
V_BAT (:


J3
VCC (output)
1
2
VCC
GND
J2
input)
1
2
V_BAT
U1
IN OUT
GND


3
4
D5
Schottky
1
COIN_CELL_(+)
1
COIN_CELL_(-)
U2
VCC
8
VCC
1K5
C2


5
GN
PB0
PB1
PB2
5
6
7
2


6
A
ND
J1
1
2
3
4
5