



MEGA65 off

Power on

FPGA loads bitstream
from Slot 0

Bitstream from
Slot 0 starts

Hypervisor boot
process

HYPPO:
Is this first boot since power-on?
(I.e., running on Slot 0?)

Yes
(Hypervisor starts
Flash Menu program)

MEGAFLASH:
Is NO SCROLL key held down?

No

Does Slot 1 contain
a valid core?

No
(Use Slot 0)

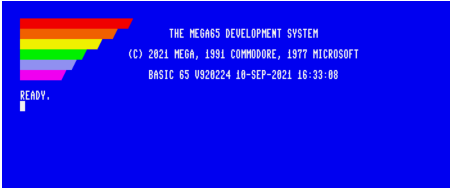
Yes
(Use Slot 1)

FPGA loads bitstream
from specified slot

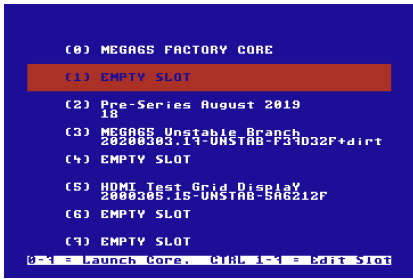
Does the bitstream
in Slot 1 work?

No

Show 'ambulance lights'
until powered off



HYPPO:
Continue to run and boot
up MEGA65.ROM



Prompt user to select core

Core selected

FPGA tries to load bitstream
from specified slot

Does the specified
bitstream slot work?

Yes
(Bitstream in slot is valid)

No
(Bitstream is not valid)

Keyboard showing
'ambulance lights',
increment slot number

Try next slot