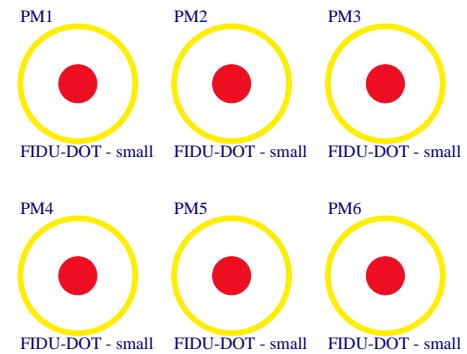
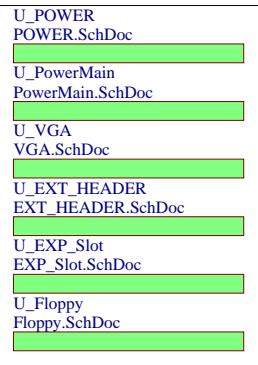
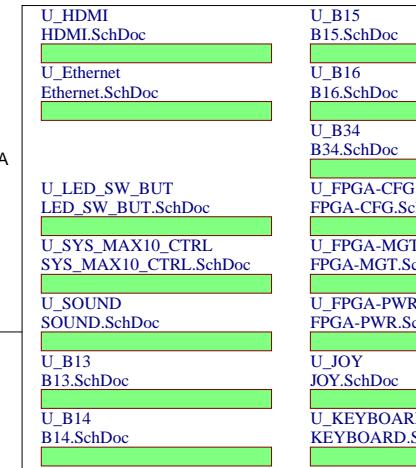


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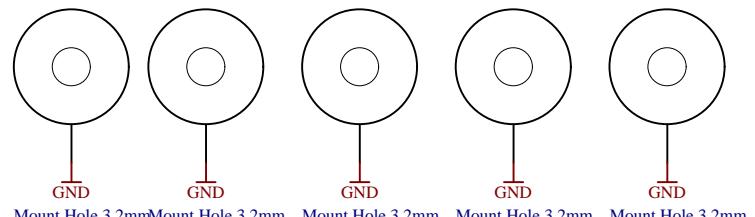
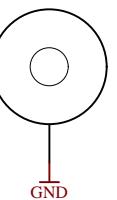
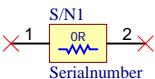
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**B**

Serial1  
Serial  
Serialnumber 6,3 x 6.3mm



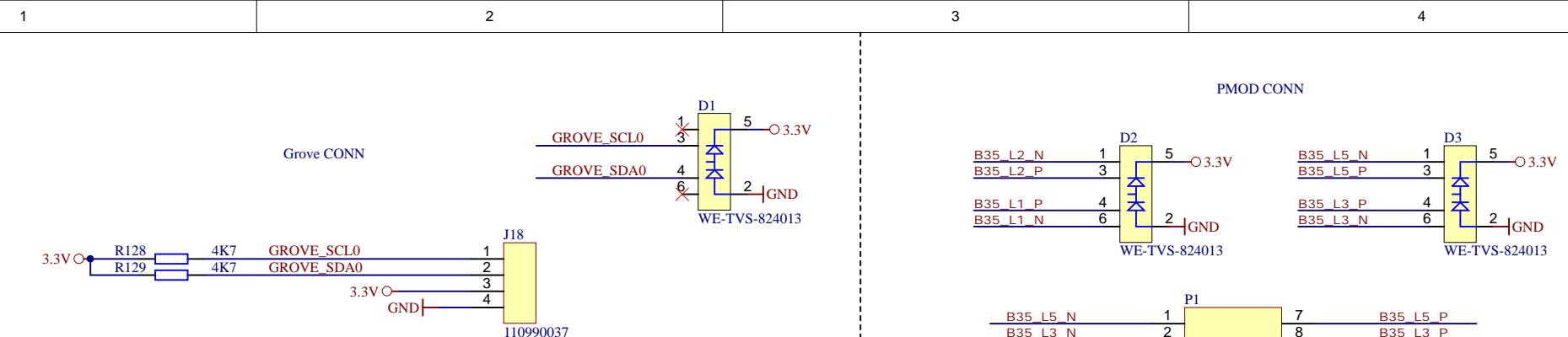
Title: MEGA65		Rev. 02
A4	Number: TE0765 Default	
Date: 2019-03-11	Copyright: Trenz Electronic GmbH	Page 1 of 23
Filename: TE0765.SchDoc		

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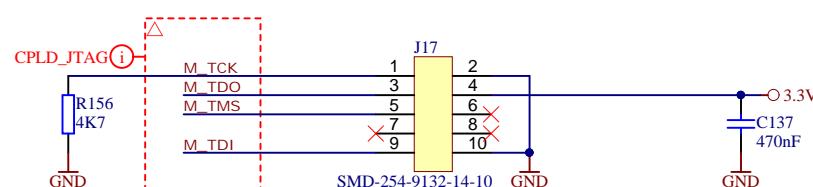
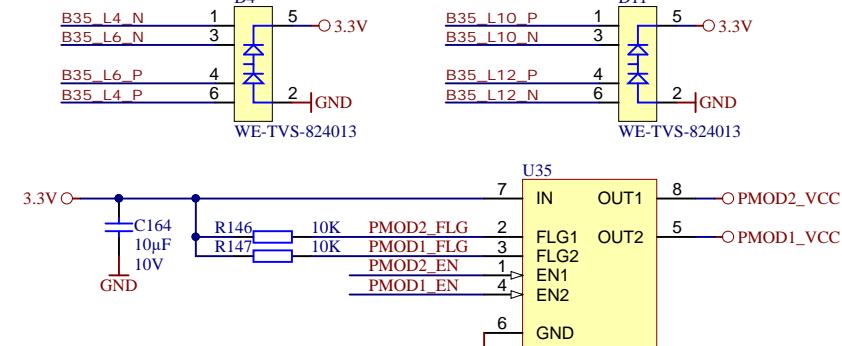
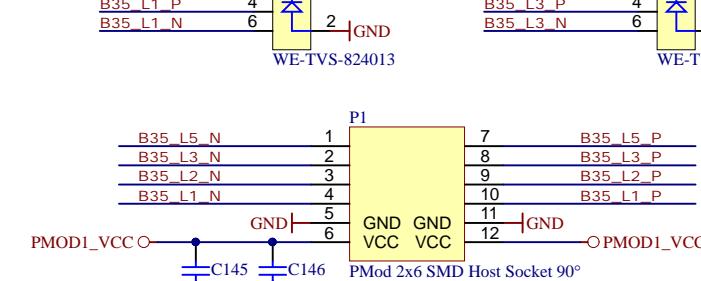
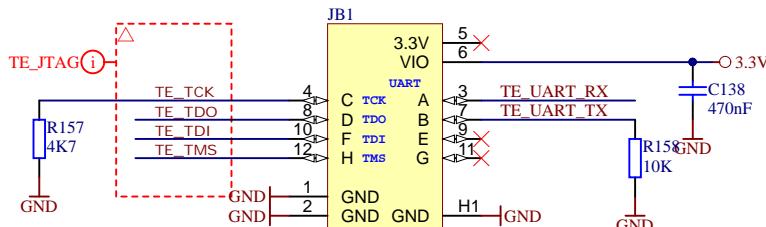
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**CPLD CONFIG****TE0790-Base SMT**

Title: MEGA65

A4 | Number: TE0765 Default

Rev. 02

Date: 2019-03-11 | Copyright: Trenz Electronic GmbH

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Filename: EXT\_HEADER.SchDoc

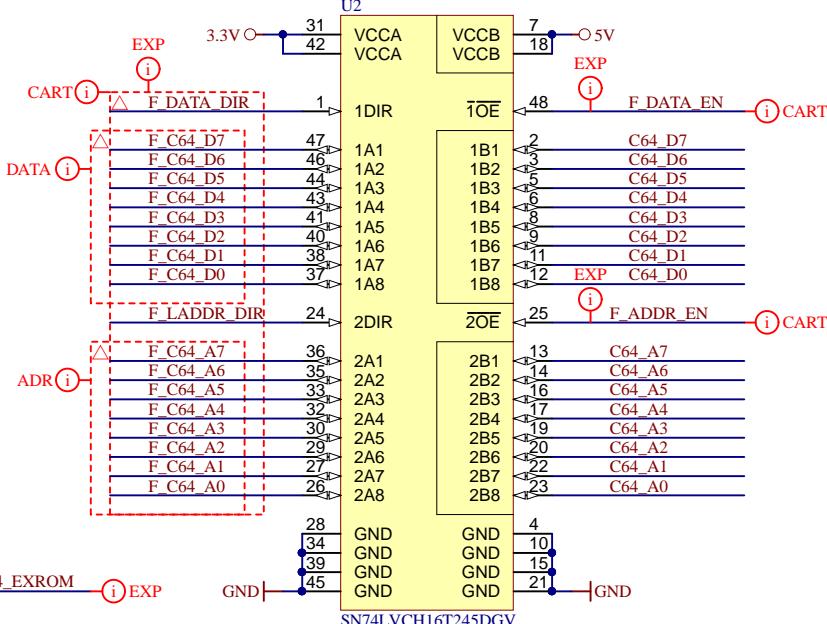
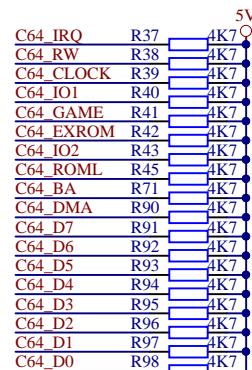
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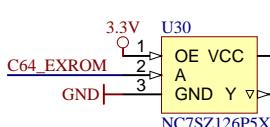
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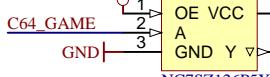
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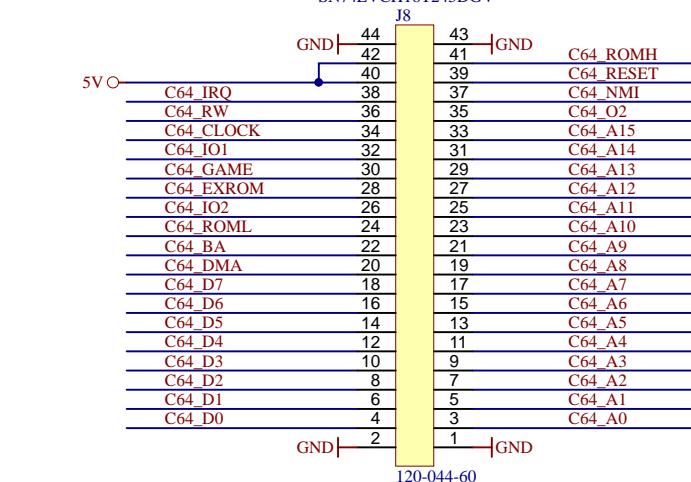
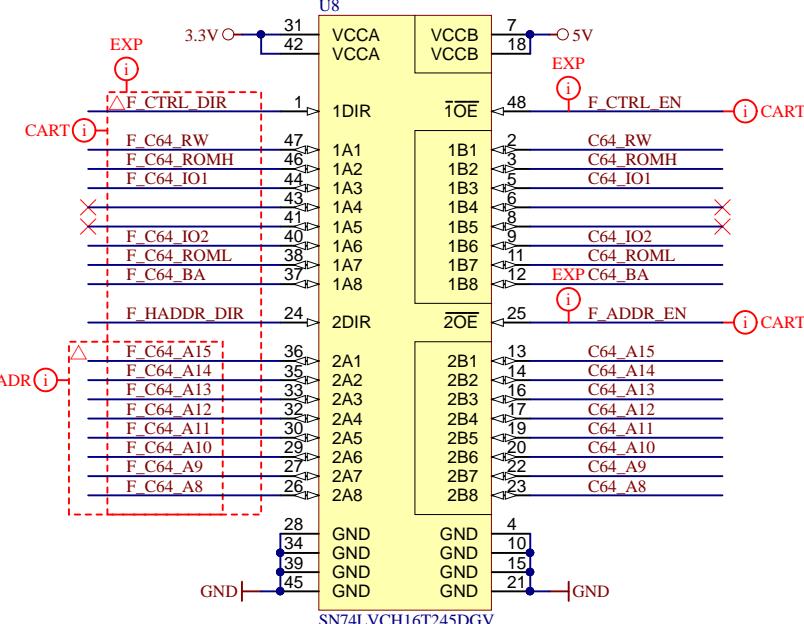
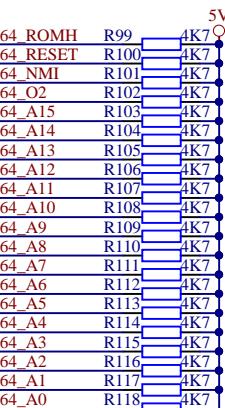
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Title: MEGA65		Rev. 02
A4	Number: TE0765 Default	
Date: 2019-03-11	Copyright: Trenz Electronic GmbH	Page 3 of 23
Filename: EXP_Slot.SchDoc		



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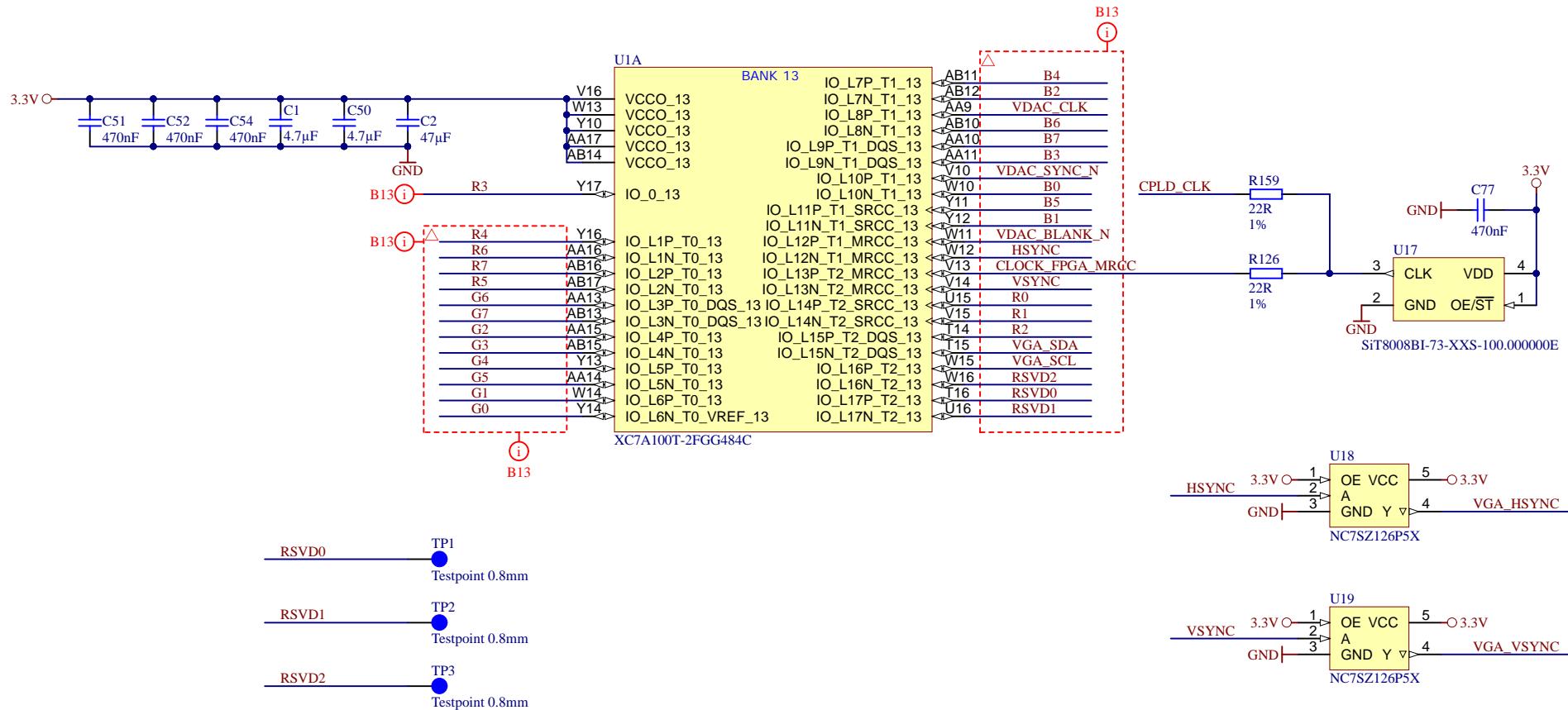
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Title: MEGA65

A4 | Number: TE0765  
Default

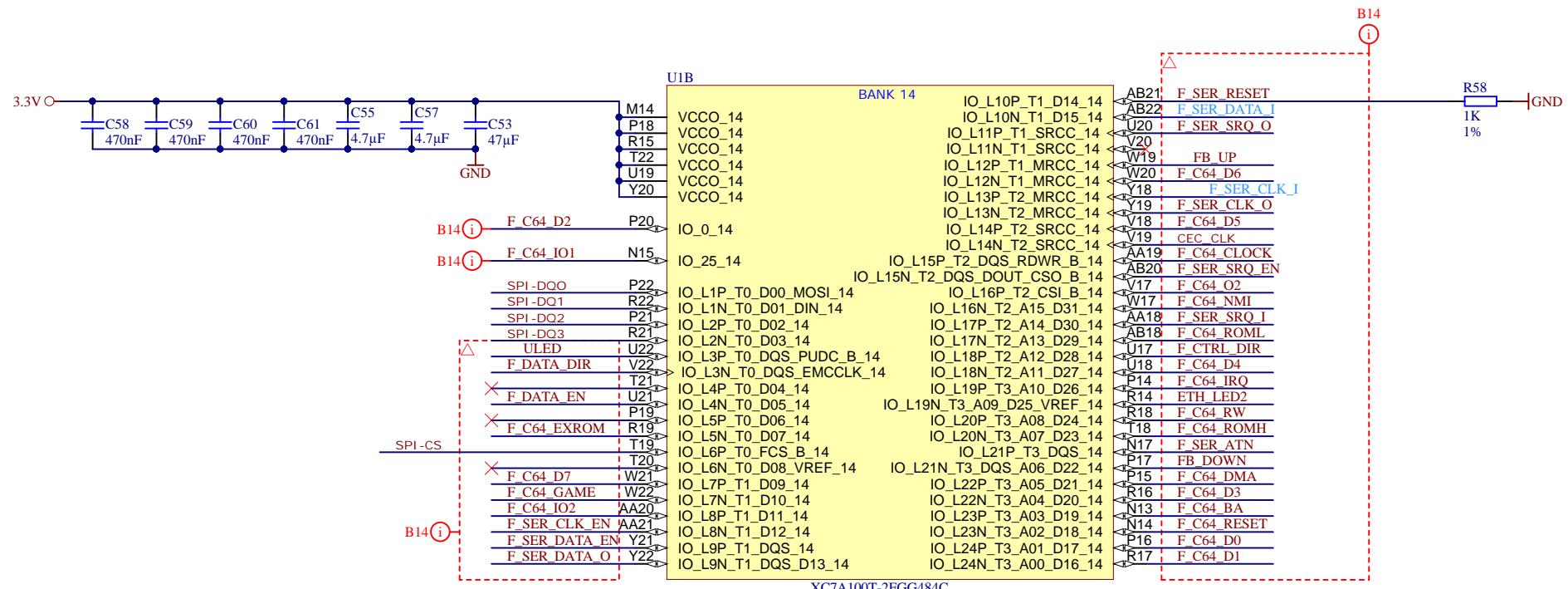
Rev. 02

Date: 2019-03-11 | Copyright: Trenz Electronic GmbH

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Filename: B13.SchDoc

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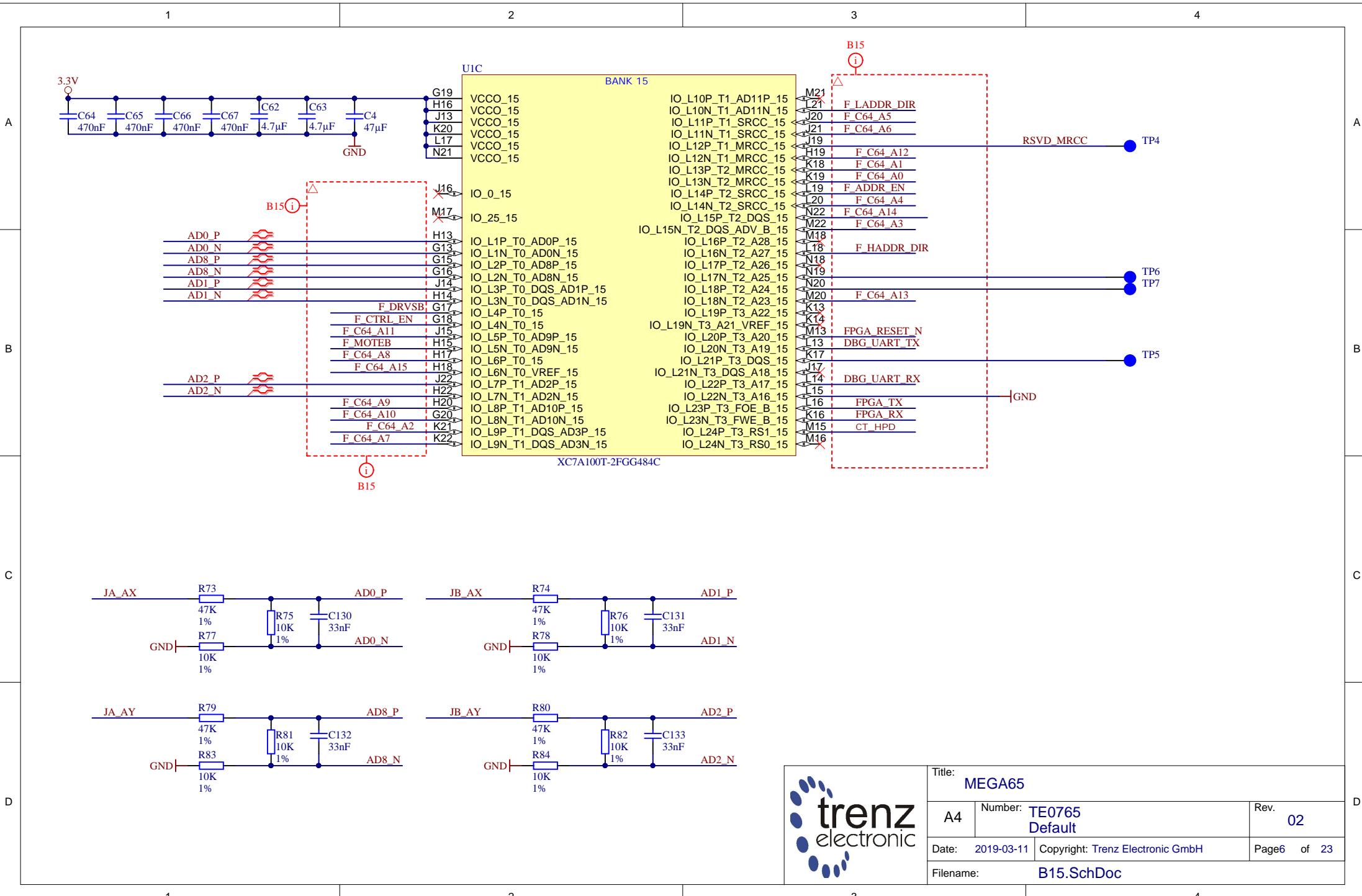


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Title: MEGA65		Rev. 02
A4	Number: TE0765 Default	
Date: 2019-03-11	Copyright: Trenz Electronic GmbH	Page 5 of 23
Filename: B14.SchDoc		

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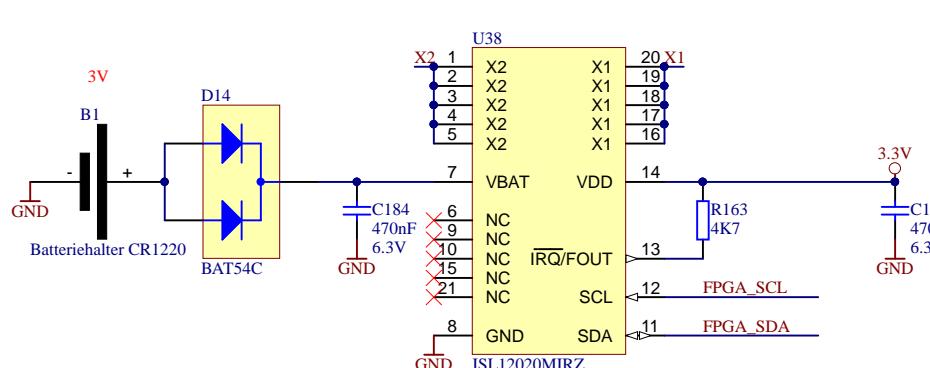
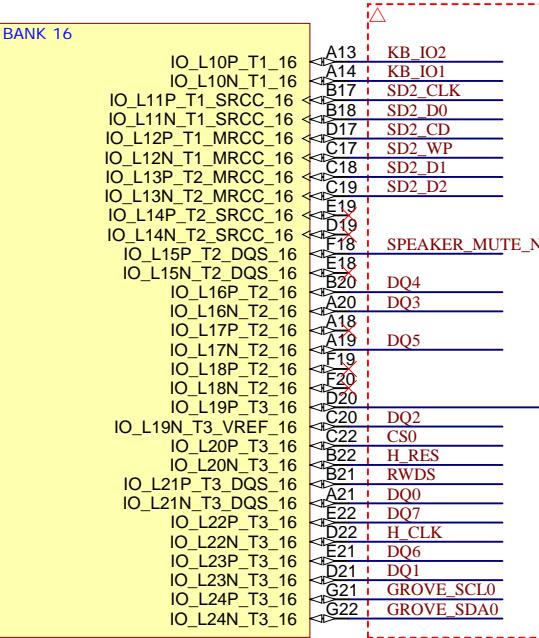
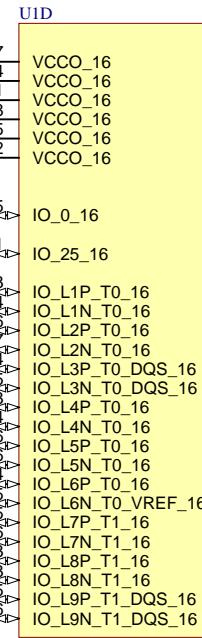
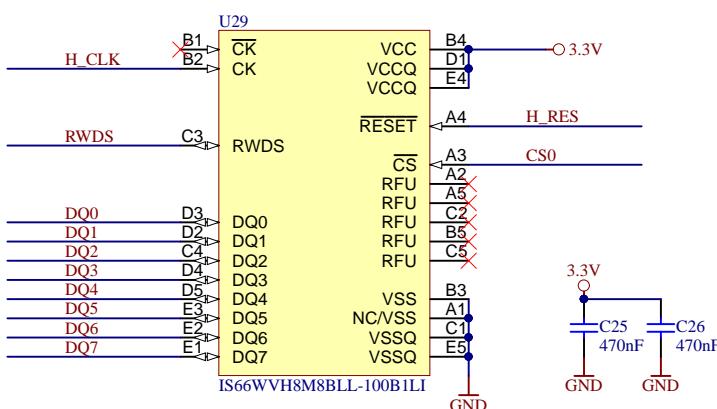
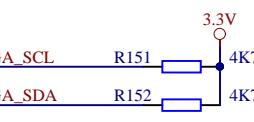
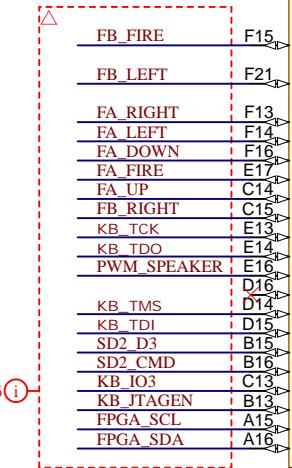
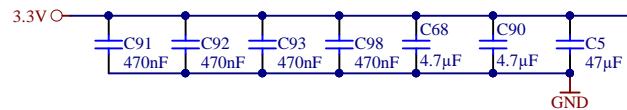
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Title:  
**MEGA65**

A4 | Number: **TE0765 Default** | Rev. **02**

Date: 2019-03-11 | Copyright: Trenz Electronic GmbH

Page 7 of 23

Filename: **B16.SchDoc**

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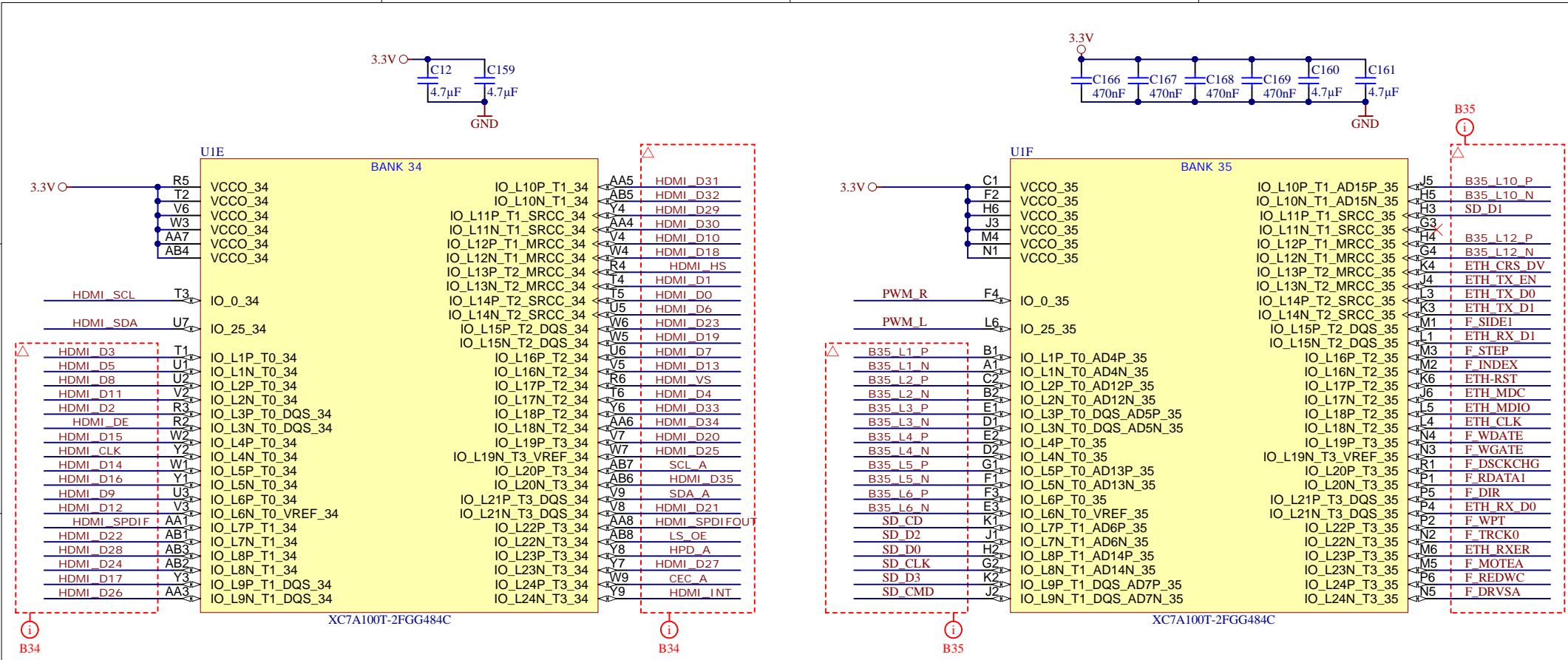
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Title: MEGA65

A4 | Number: TE0765  
Default

Rev. 02

Date: 2019-03-11 | Copyright: Trenz Electronic GmbH

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Filename: B34.SchDoc

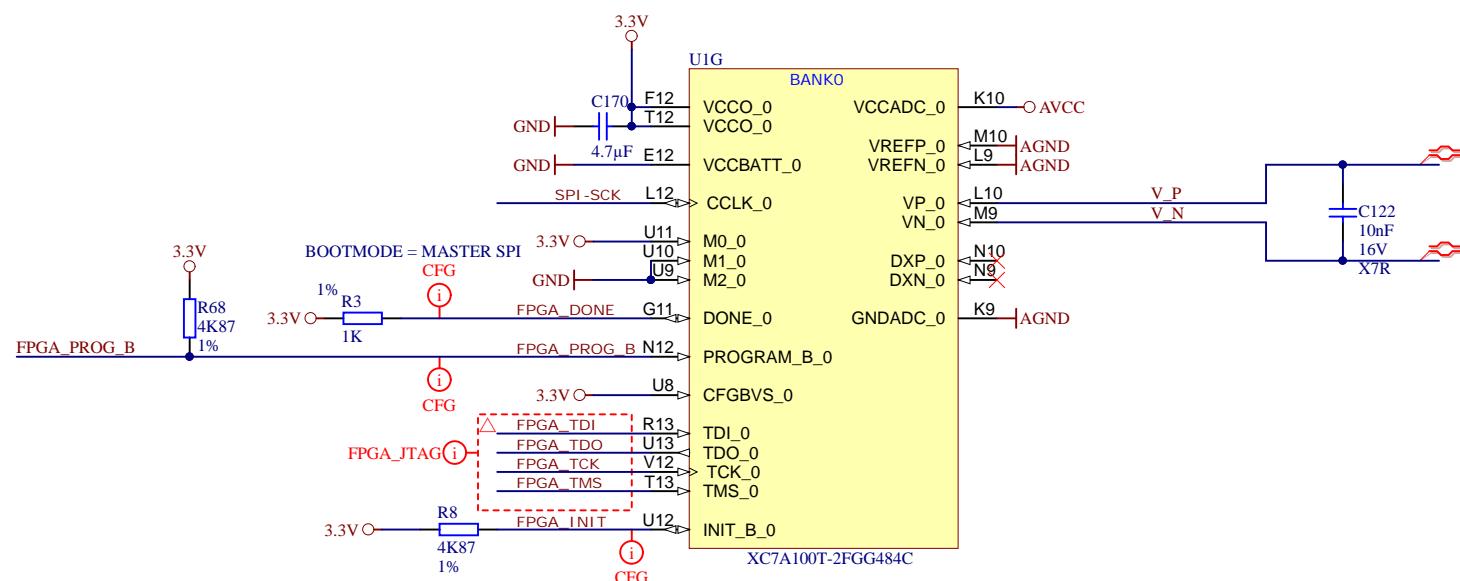
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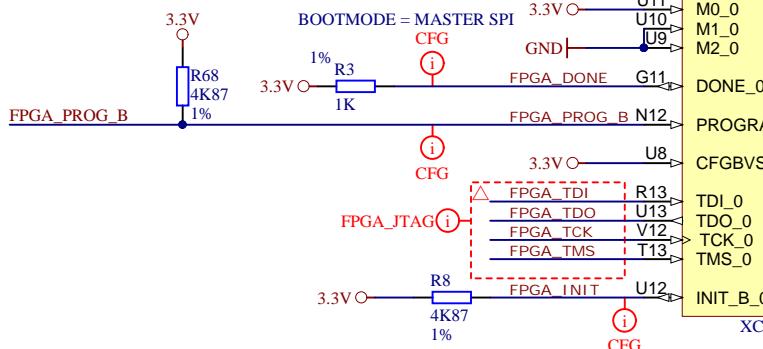
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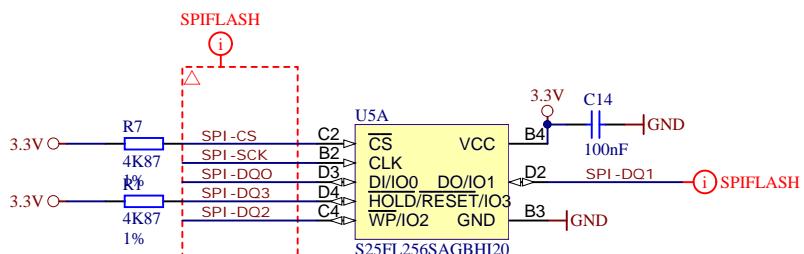
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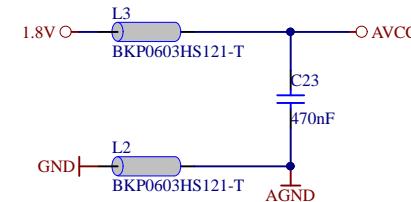
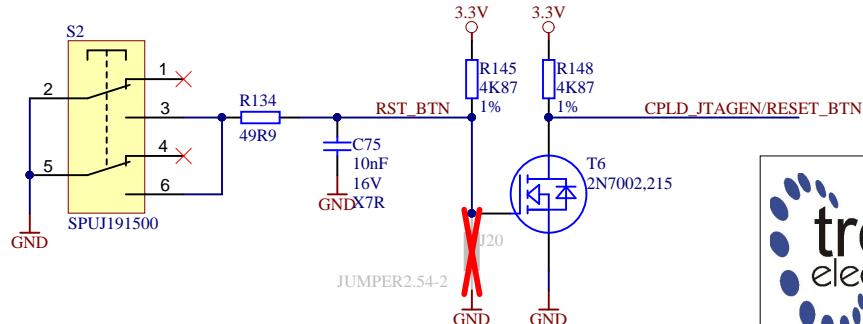


C



A2	NC
A3	NC
A4	NC
A5	NC
B1	NC
B5	NC
C1	NC
C3	NC
C5	NC
D1	NC
D5	NC
E1	NC
E2	NC
E3	NC
E4	NC
E5	NC

S25FL256SAGBHI20



Title: MEGA65

A4 | Number: TE0765 Default

Rev. 02

Date: 2019-03-11 | Copyright: Trenz Electronic GmbH

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Filename: FPGA-CFG.SchDoc

A

A

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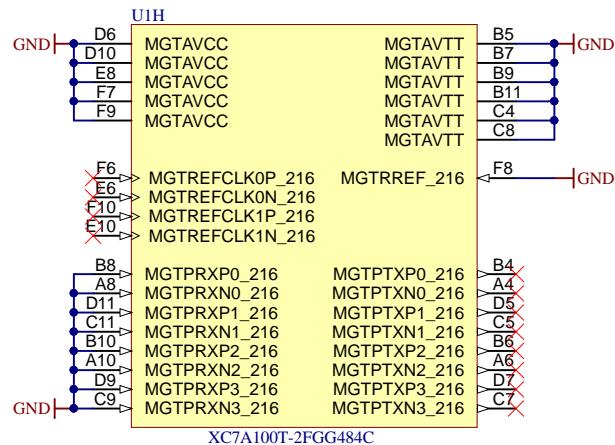
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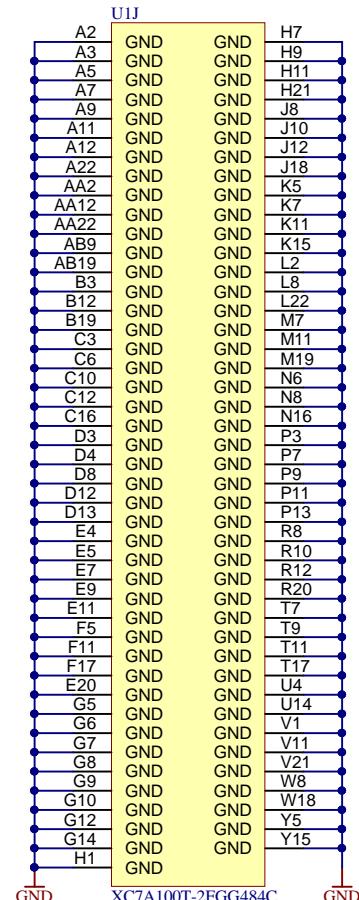
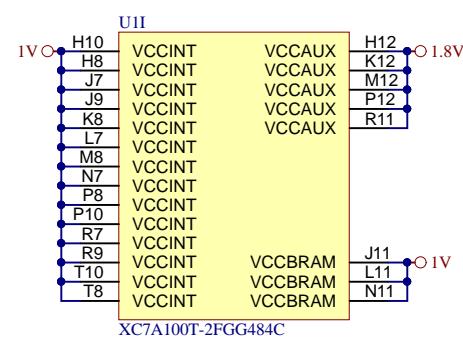
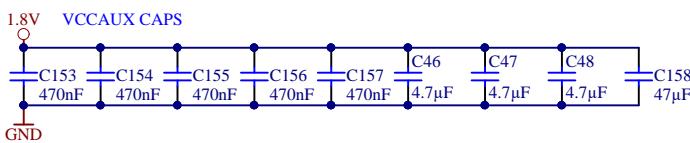
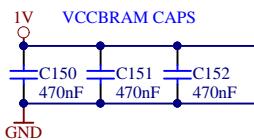
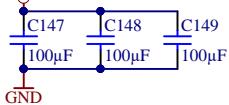
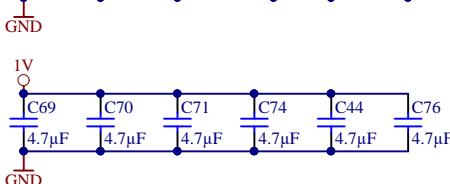
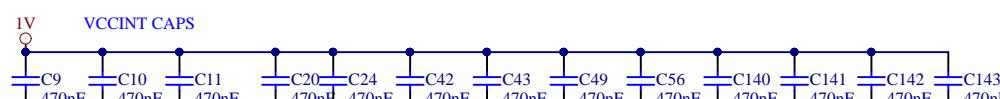
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Title: MEGA65		
A4	Number: TE0765 Default	Rev. 02
Date: 2019-03-11	Copyright: Trenz Electronic GmbH	Page 10 of 23
Filename: FPGA-MGT.SchDoc		

1 2 3 4



Title: MEGA65

A4 Number: TE0765  
Default

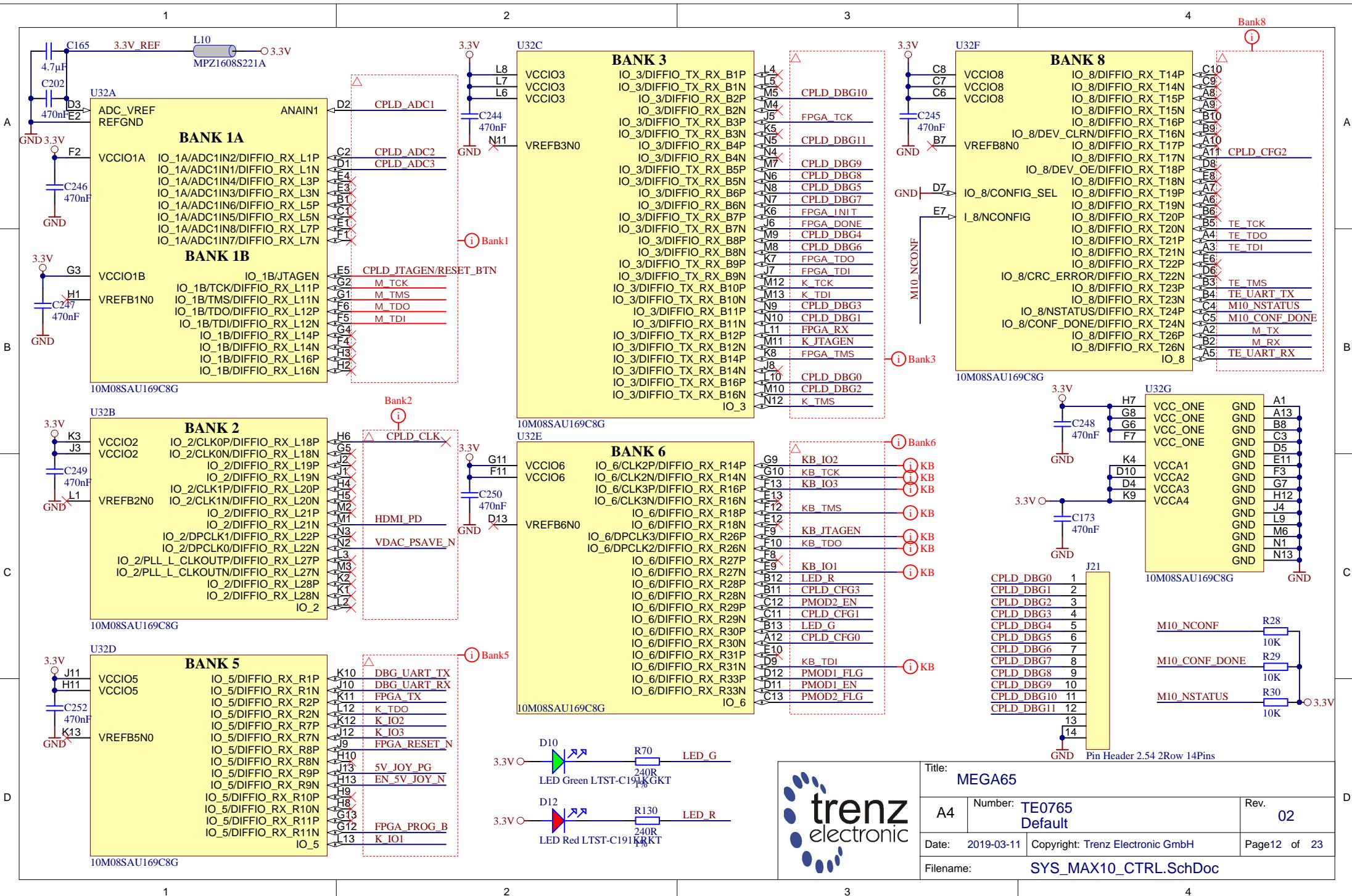
Rev. 02

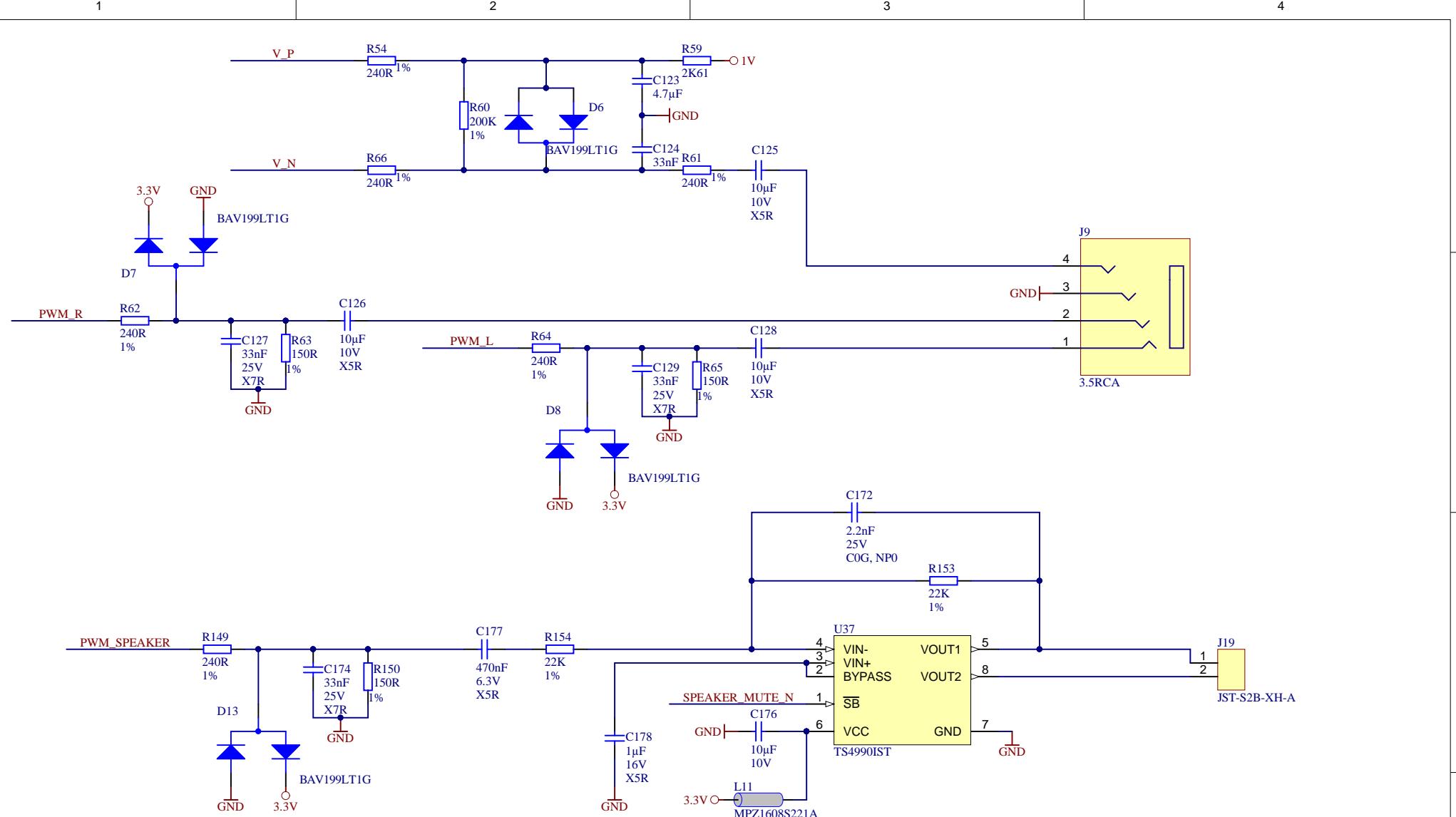
Date: 2019-03-11 Copyright: Trenz Electronic GmbH

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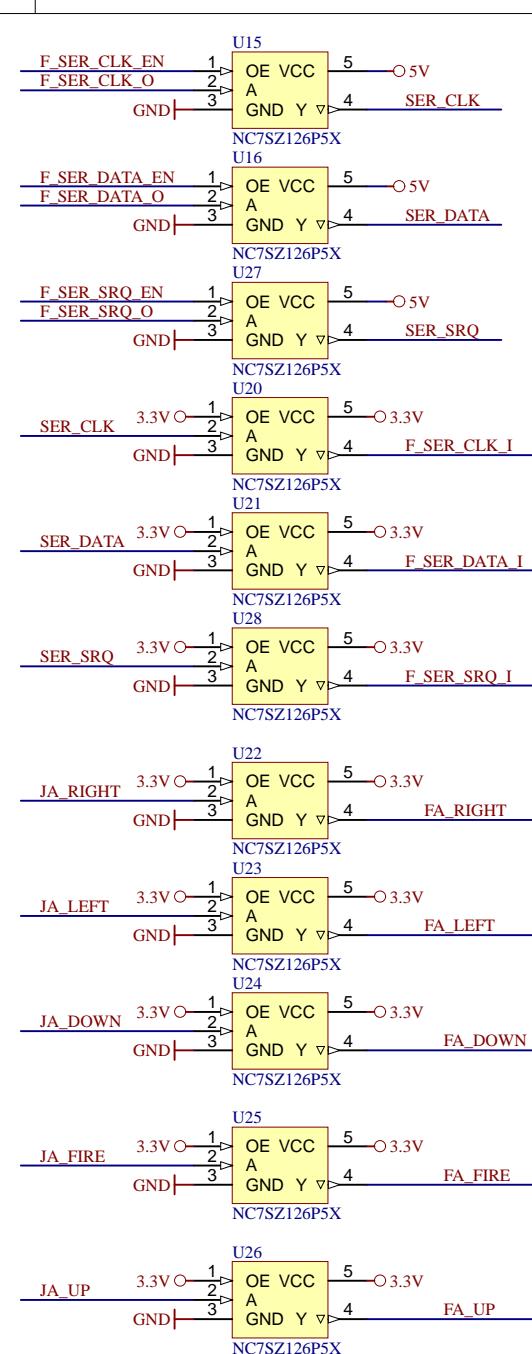
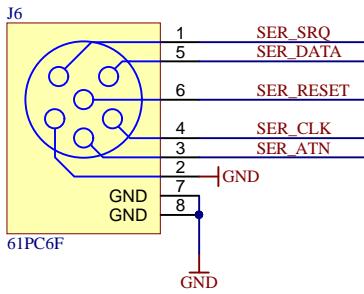
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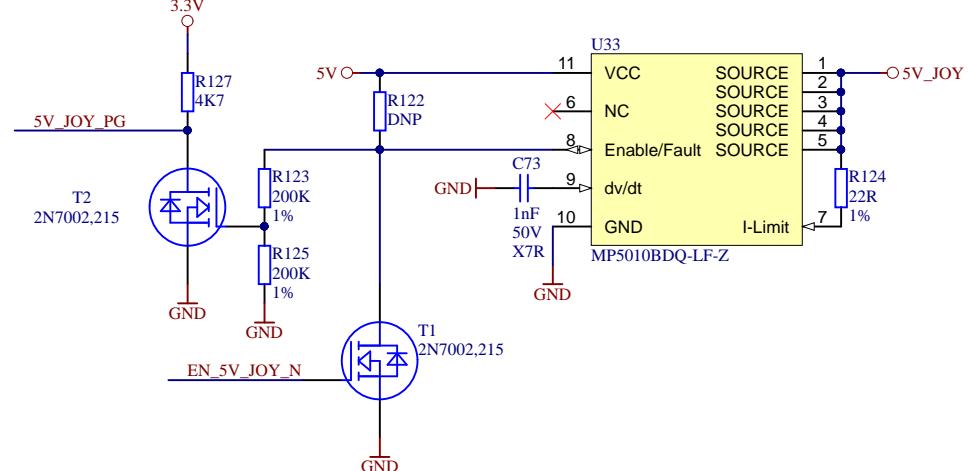
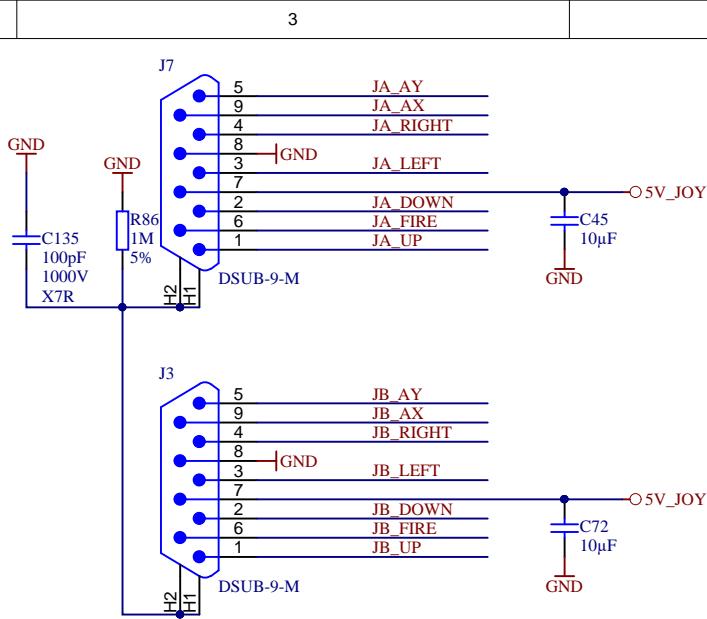




Title: MEGA65	
A4	Number: TE0765 Default
Date: 2019-03-11	Copyright: Trenz Electronic GmbH
Filename: SOUND.SchDoc	Rev. 02



3.3V → C179 10µF → GND  
5V → C180 10V → GND



Title: MEGA65

A4 | Number: TE0765 Default

Rev. 02

Date: 2019-03-11 | Copyright: Trenz Electronic GmbH

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Filename: JOY.SchDoc

A

A

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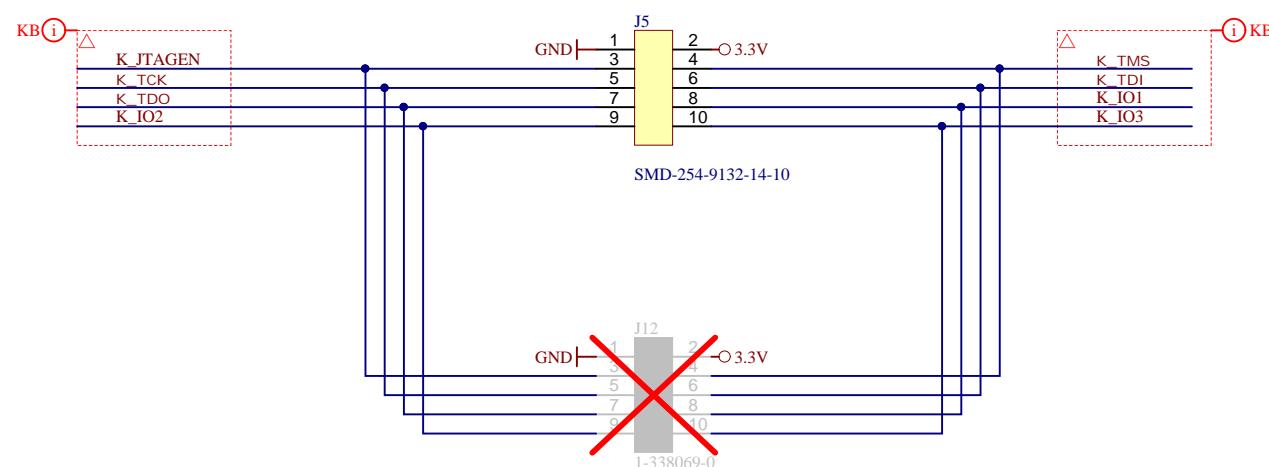
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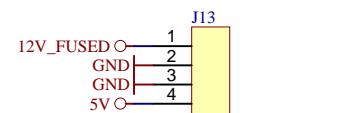
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Title: MEGA65		
A4	Number: TE0765 Default	Rev. 02
Date: 2019-03-11	Copyright: Trenz Electronic GmbH	Page 16 of 23
Filename: KEYBOARD.SchDoc		

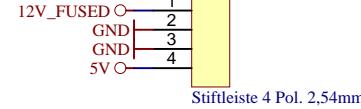
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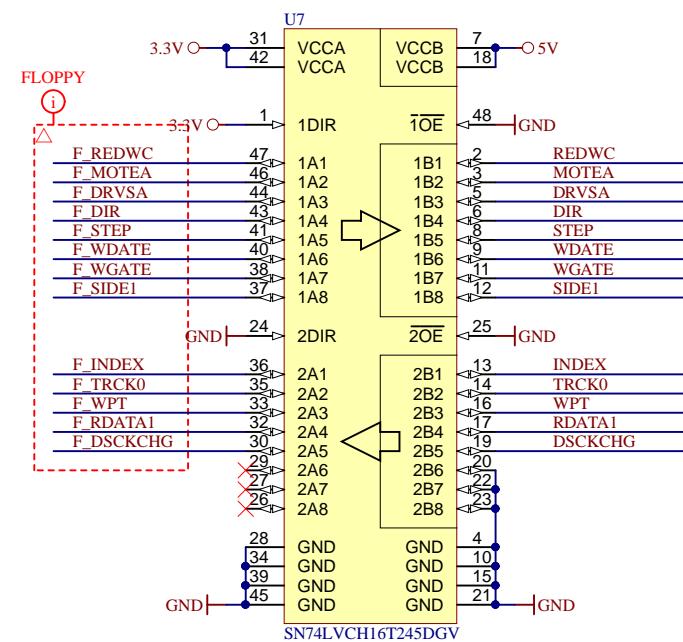
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C

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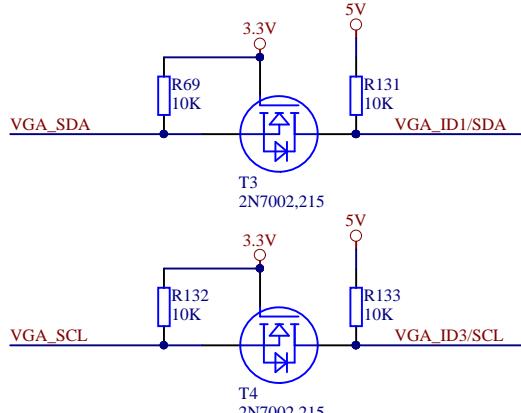
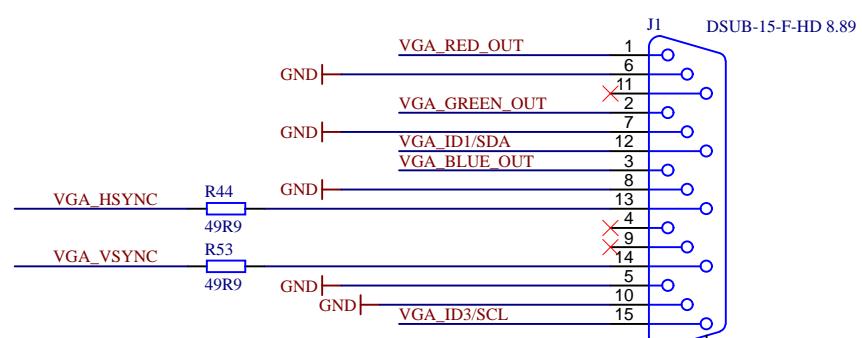
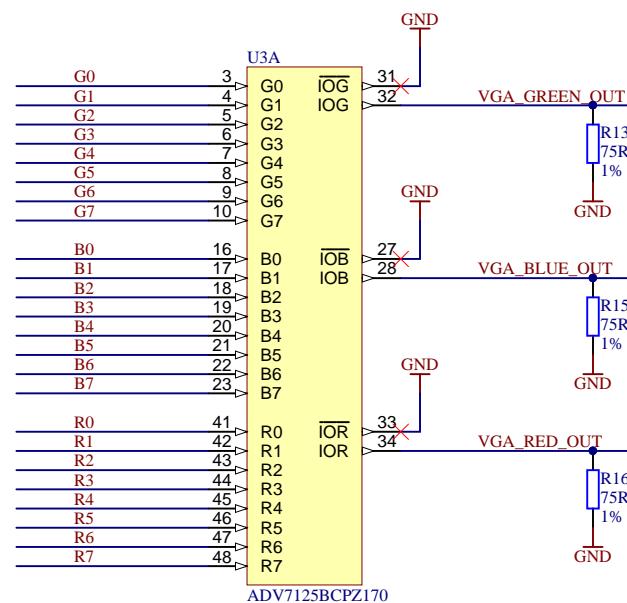
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Title: MEGA65		Rev. 02
A4	Number: TE0765 Default	
Date: 2019-03-11	Copyright: Trenz Electronic GmbH	Page 17 of 23
Filename: Floppy.SchDoc		

1 2 3 4



Title: MEGA65

A4 | Number: TE0765  
Default

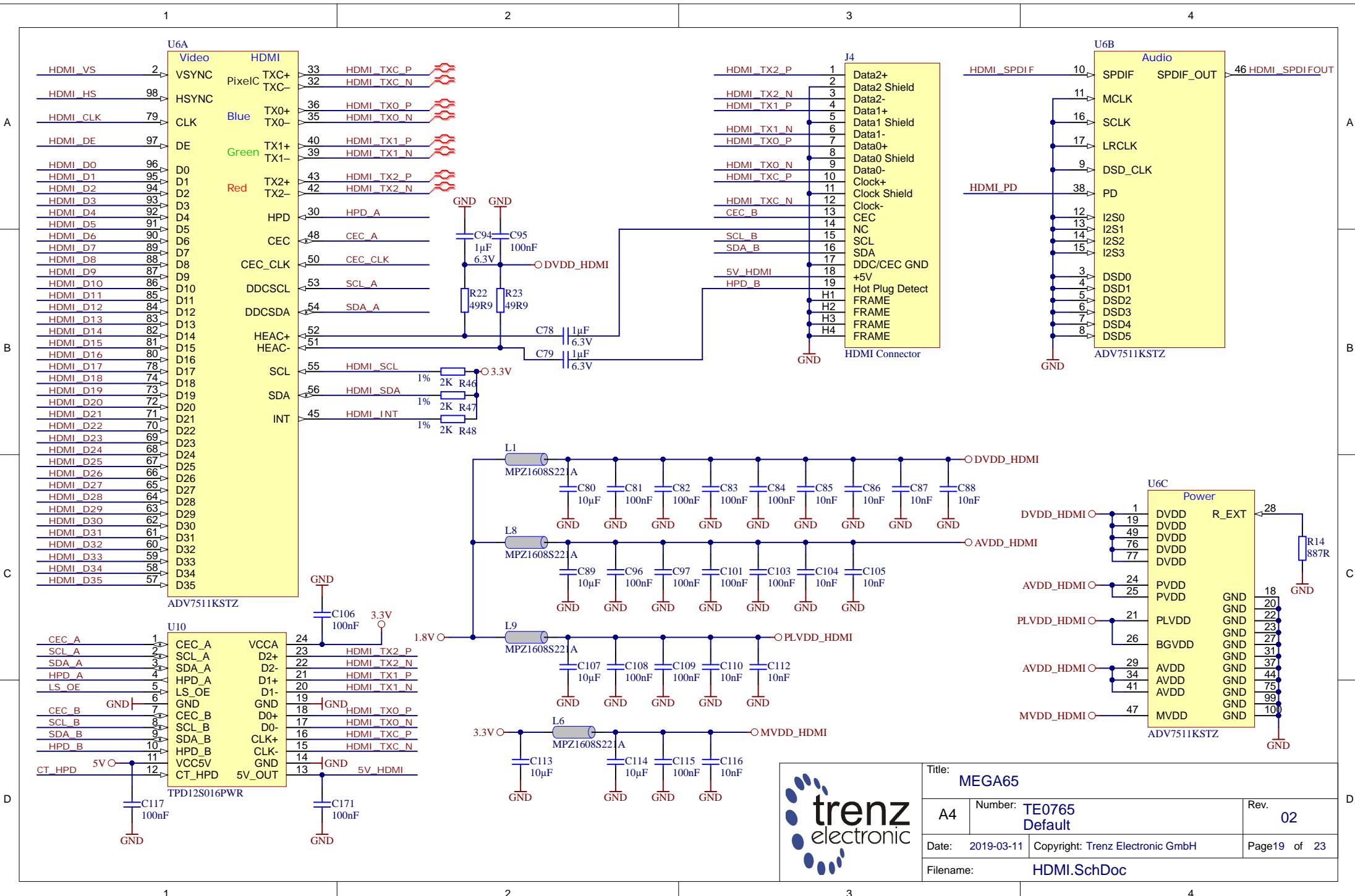
Rev. 02

Date: 2019-03-11 | Copyright: Trenz Electronic GmbH

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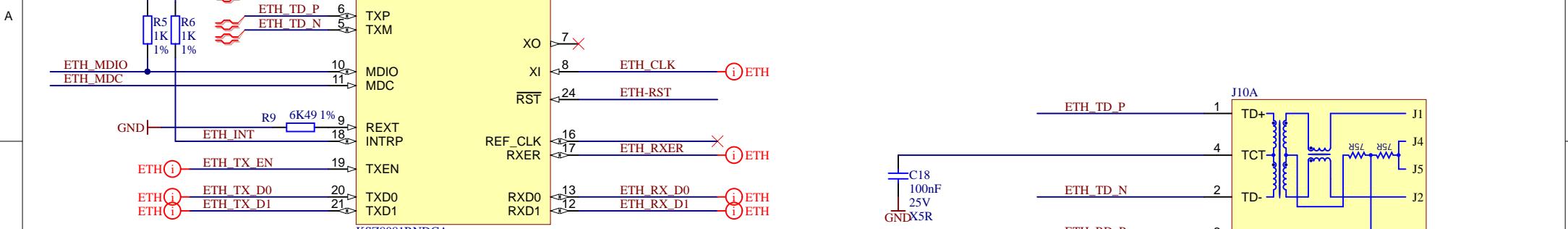
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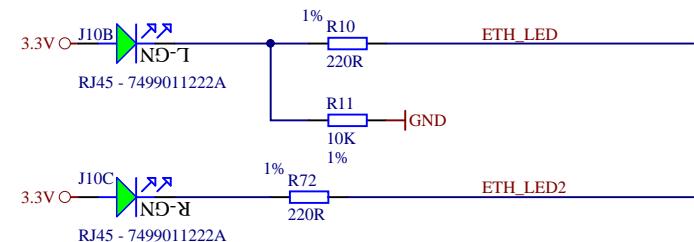


1 2 3 4

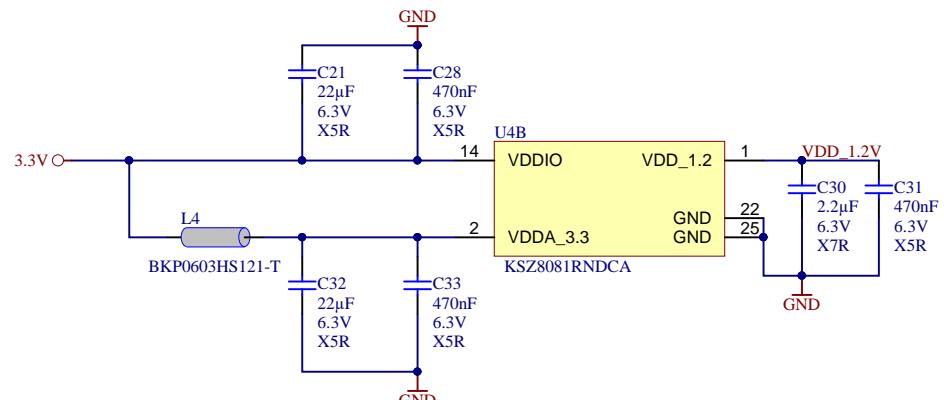
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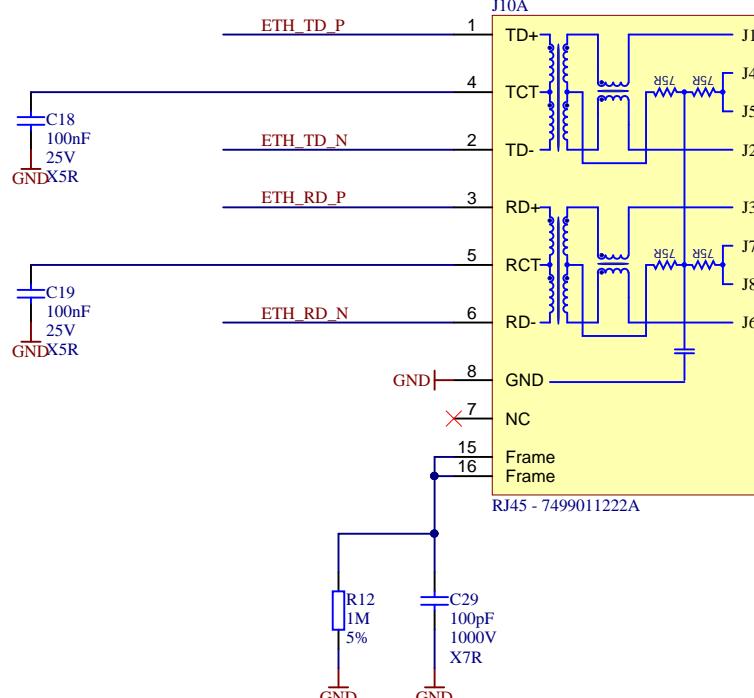
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Title: MEGA65	
A4	Nummer: TE0765 Default
Datum: 2019-03-11	Zeichner: Trenz Electronic GmbH
Blatt 20	von 23

Filename: Ethernet.SchDoc

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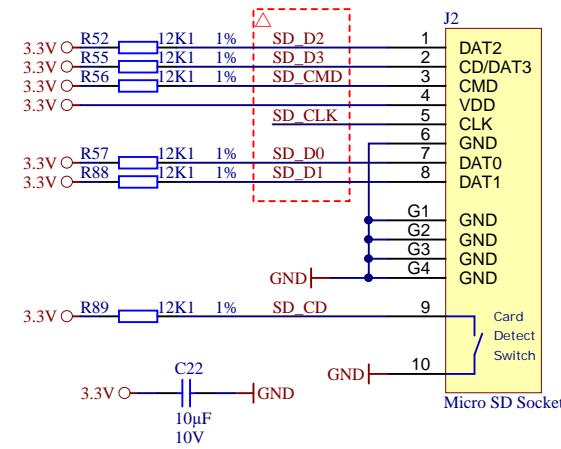
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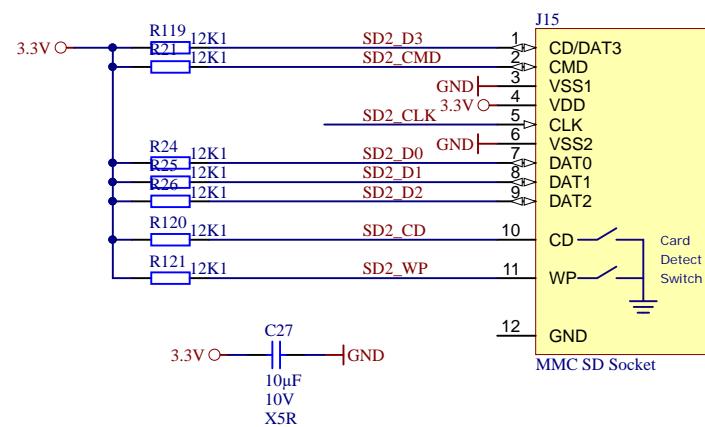
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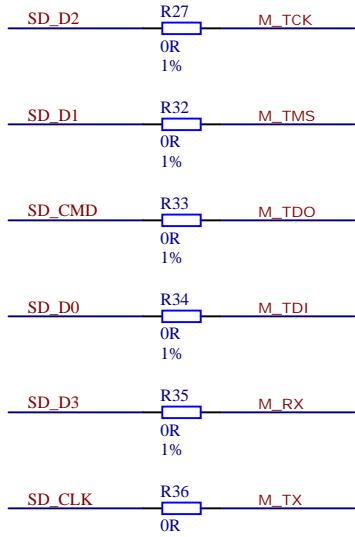
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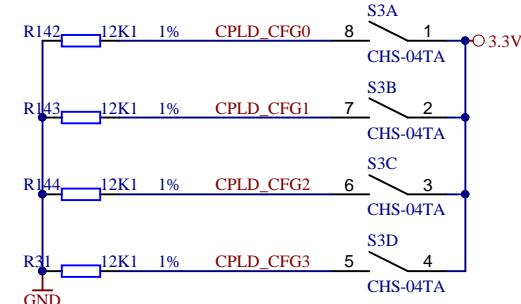
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[http://linux-sunxi.org/MicroSD\\_Breakout](http://linux-sunxi.org/MicroSD_Breakout)

D

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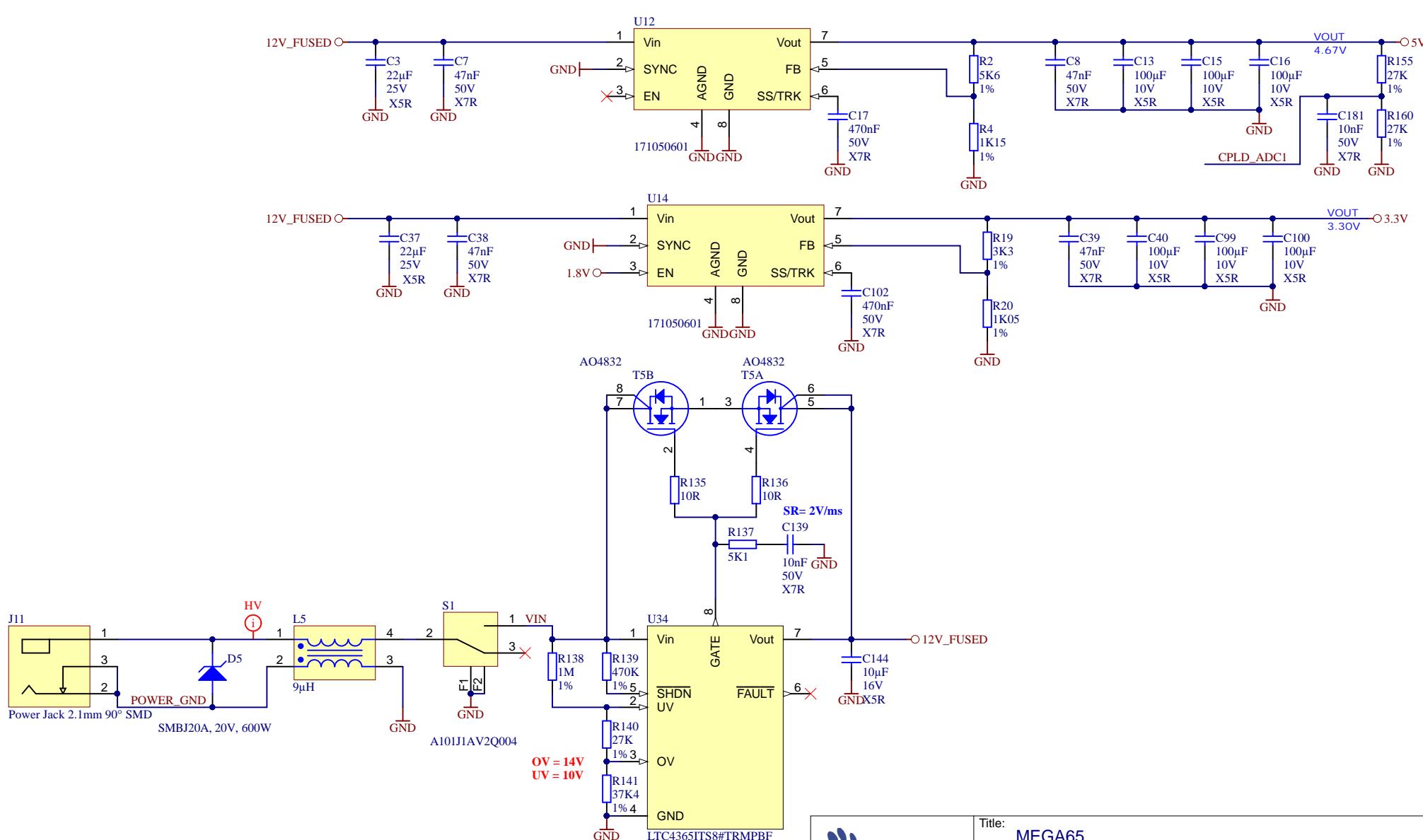
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A4	Number: TE0765 Default	
Date: 2019-03-11	Copyright: Trenz Electronic GmbH	Page 21 of 23
Filename: LED_SW_BUT.SchDoc		

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Title: MEGA65

A4 | Number: TE0765  
Default

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Date: 2019-03-11 | Copyright: Trenz Electronic GmbH

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Filename: PowerMain.SchDoc

1 2 3 4

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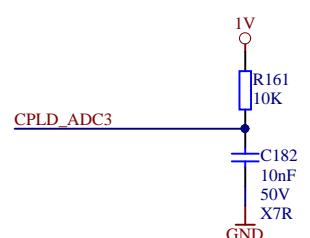
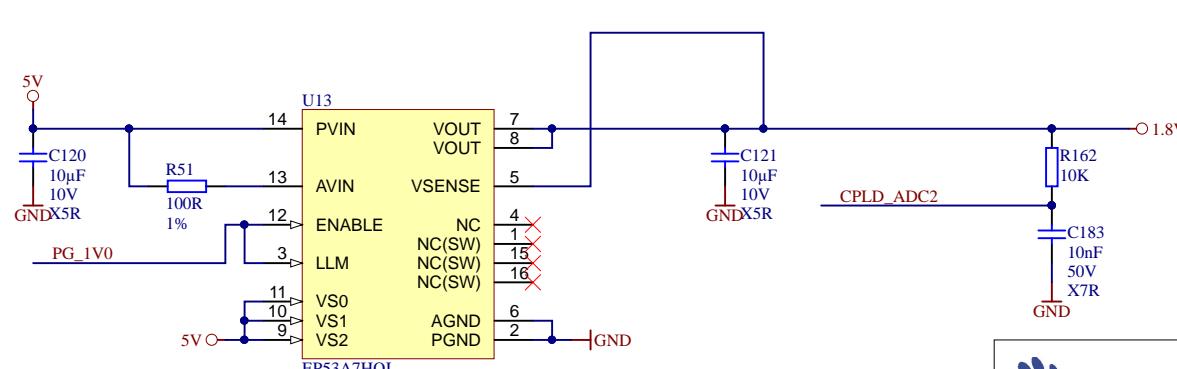
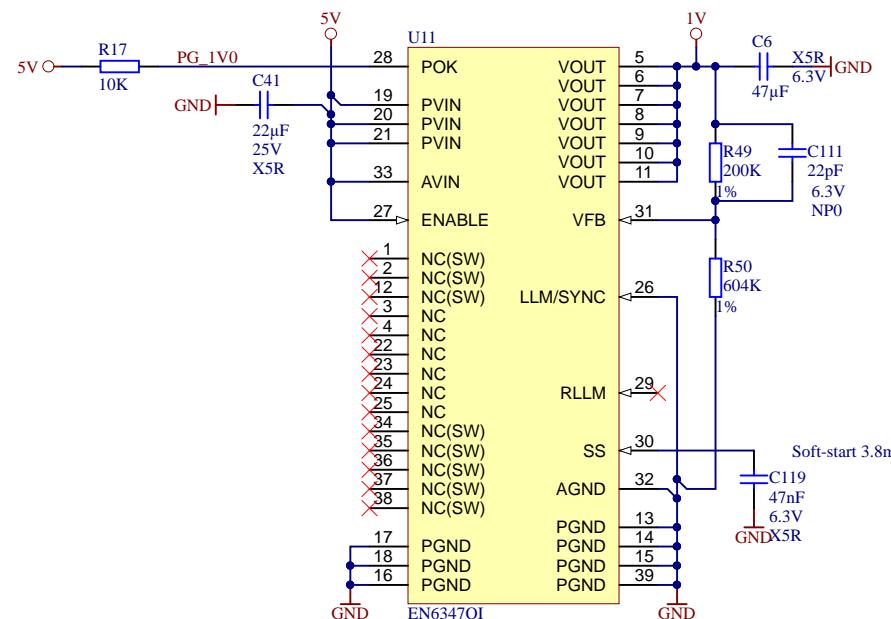
B

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Title: MEGA65

A4 Number: TE0765 Default

Rev. 02

Date: 2019-03-11 Copyright: Trenz Electronic GmbH / TT Page 23 of 23

Filename: POWER.SchDoc

1 2 3 4