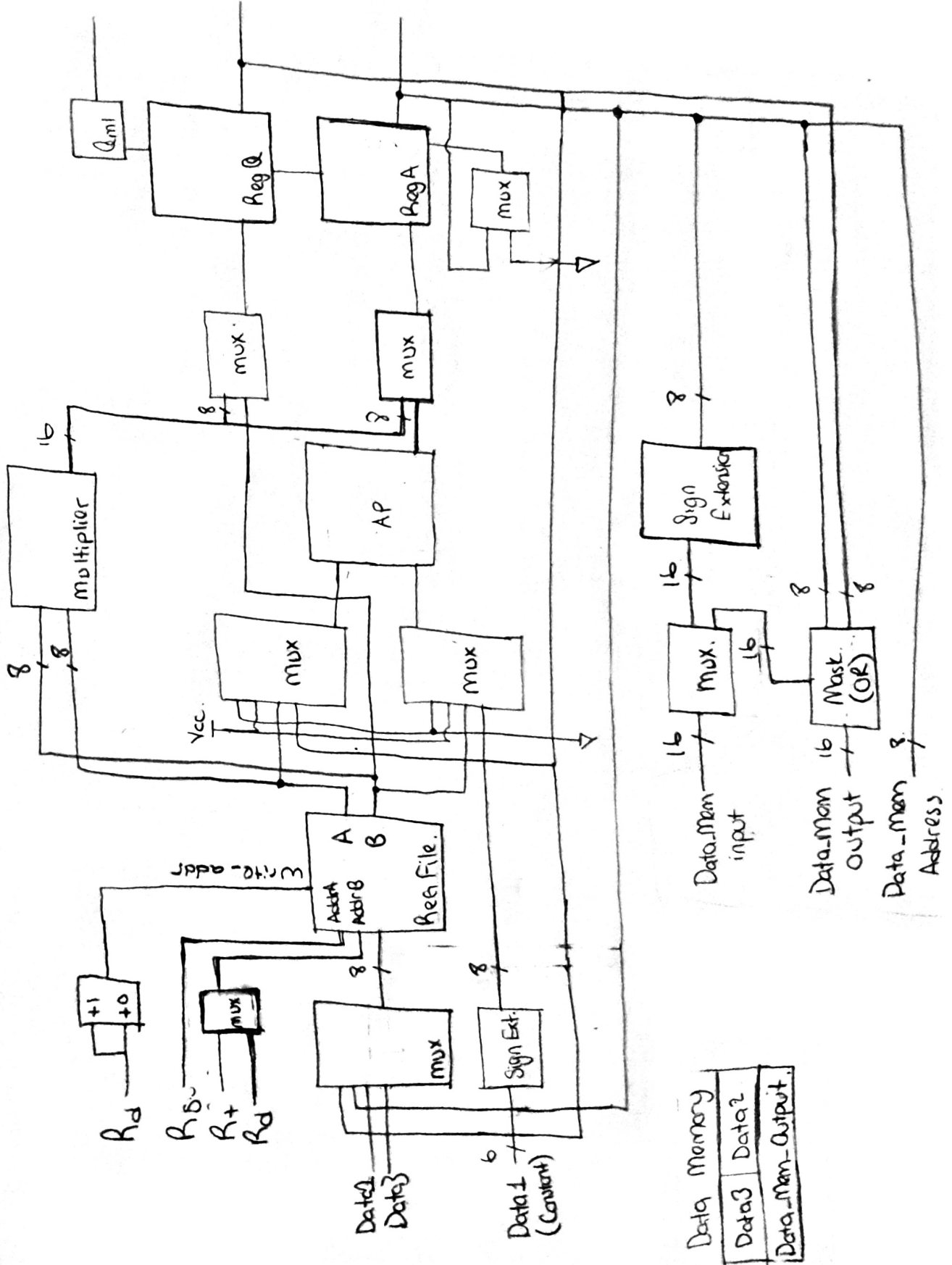


For Shift op, Data1 directly
go to control unit. to control
Shift registers

R type ✓
I type ✓
J type ✓

Addi R₁₁, R₀, 15
addi R₂, R₀, 16
ldl R₁, R₂(C)
R_d R_s

ld R₁, R₂(C)
swl R₁, R₂(C)



Data Memory
Data3
Data2
Data-Mem-Output