



Faculty of Engineering
Credit Hours Engineering Programs

**Communication Systems Engineering
Program**

Academic Year 2021/2022 – Fall 2021

**Telescopic Cascode Differential
Amplifier**

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1. Hand analysis gm, ro, Vov

First, we assumed maximum current used is $I_{SS} = 40 \text{ uA}$ since the power budget is 4mw so the current in half of differential amplifier circuit $I_d = 20\text{uA}$ we used small value of current with the given budget to achieve low power consumption by the telescopic cascode amplifier.

Second, we assumed ranges of the input voltage and output voltage so we can get equations of over drive voltages of each transistor

$$V_{out \text{ range}} = 0.65 - 0.75 = 0.7V$$

$$V_{in \text{ range}} = 0.75 - 0.8 = 0.775V$$

EQ1:

$$V_{in} > V_{th1} + V_{ov1} + V_{ovss}$$

$$V_{in(min)} = V_{th1} + V_{ov1} + V_{ovss}$$

EQ2:

$$V_{IN} - V_n < V_{th}$$

$$V_n = V_{b1} - V_{gs3} = V_{b1} - V_{th3} - V_{ov3}$$

$$V_{in} - V_{b1} + V_{th3} + V_{ov3} < V_{th1}$$

$$V_{b1} > V_{in(max)} - V_{th1} + V_{th3} + V_{ov3}$$

EQ3:

$$V_{b1} - V_{OUTmin} < V_{th3}$$

$$V_{b1} < V_{th3} + V_{OUT(min)}$$

EQ4:

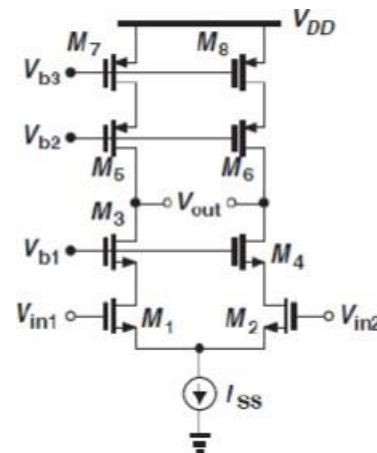
$$V_{outmax} - V_{b2} < -V_{th5}$$

$$V_{b2} > V_{out(max)} - V_{th5}$$

EQ5:

$$1.2 - V_{b2} > V_{th5} + V_{ov5} + V_{ov7}$$

$$V_{b2} < 1.2 - V_{th5} - V_{ov5} - V_{ov7} \text{ (all values is positive)}$$



Third, we assumed transconductances are equal to each other and output resistances are ratios from each other and calculated these values from gain equation of circuit.

$$G_m = -gm_1$$

$$R_{out} = (ro1 * gm3 * ro3 // ro5 * gm5 * ro7)$$

$$abs(Gain) = G_m * R_{out} = gm1 * (ro1 * gm3 * ro3 // ro5 * gm5 * ro7) = 600$$

$$\frac{gm1}{Id} = 15 \quad \text{assumption}$$

$$V_{ov1} = 133 \text{ mV}$$

$$gm1 = 300 \mu S$$

$$gm3 = gm5 = gm1 = gm7$$

$$ro1 = ro7 = ro3/2 = ro5/2$$

$$Gain = gm1^2 * \left(\frac{ro3^2}{4} \right) = 600$$

$$ro5 = ro3 = 163K$$

$$ro1 = ro7 = 81.5K$$

2. Transistor analysis

After we assumed values of transconductance, and we got the value of output resistance we ratio between transconductance and current gm/Id and we also got the ratio between transconductance and output conductance gm/gds .

To get the length of the channel we used gm/Id methodology by dc sweep on the gate voltage and parametric sweep over several values of channel length, using standard width, to achieve the required values for the ratios $\frac{gm}{gds}$ & $\frac{gm}{Id}$, as width don't affect these ratios.

$$gm * ro = \frac{2 * b}{V_{vo}} * \frac{1}{\lambda * b} = \frac{2}{V_{ov} * \lambda}$$

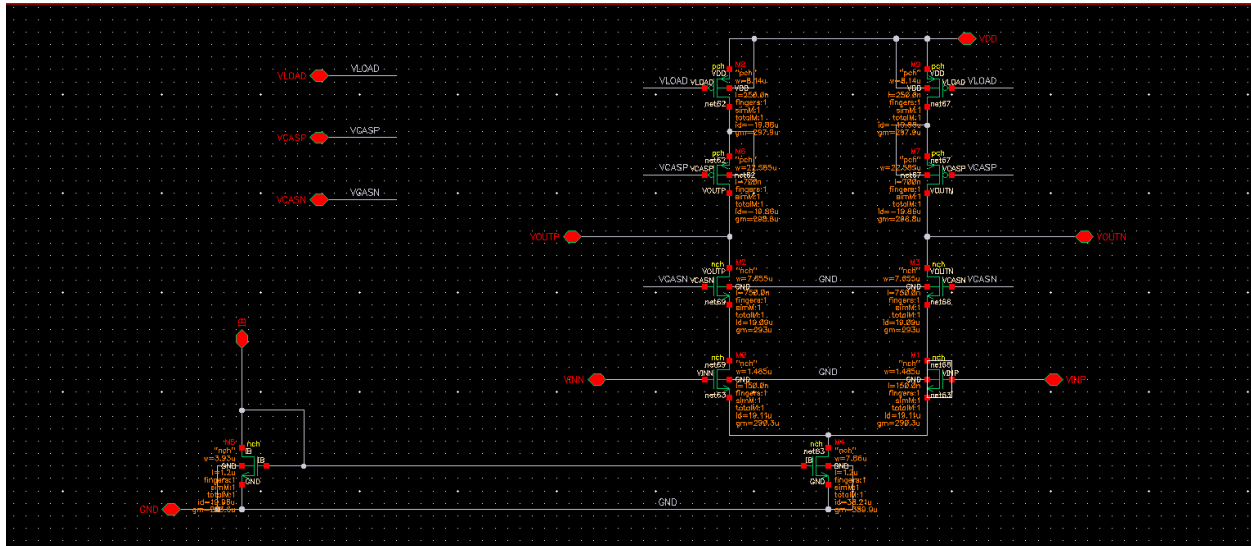
$$\frac{gm}{Id} = \frac{2 * b}{V_{vo}} * \frac{1}{Id} = \frac{2}{V_{ov}}$$

To get value of transistor width we said that width is direct proportional to the current (initial values of current and width are $20 \mu A$, $10 \mu m$) and from graph we got the value of the current.

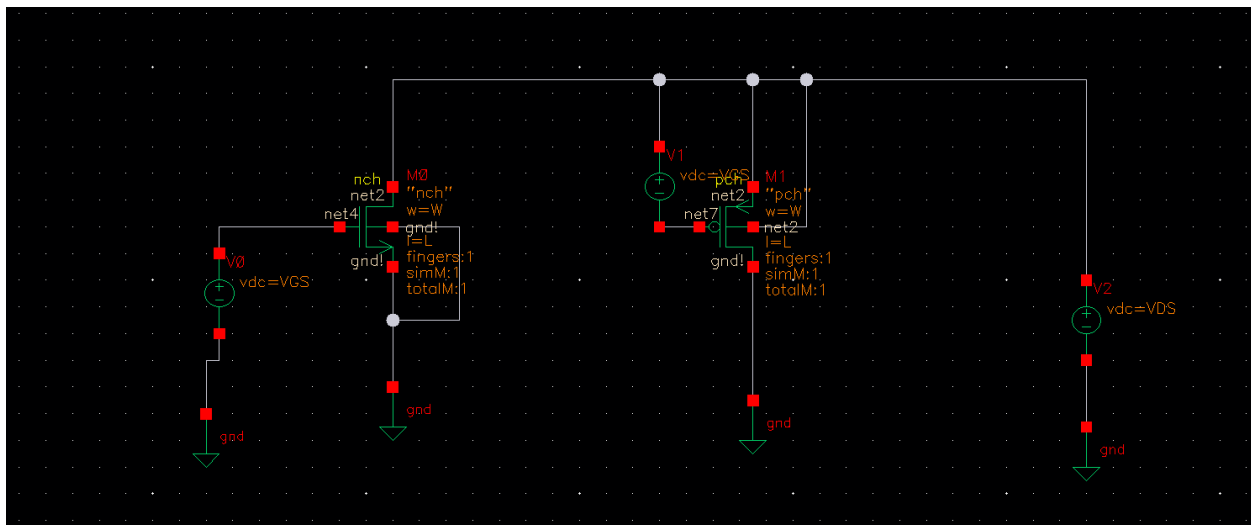
$$Id \propto w$$

$$w_{stan} = 10 \mu m$$

telescopic cascode differential amplifier:



Sweeping circuit:



Point	Test	Output	Nominal	Spec	Weight	Pass/Fail
Parameters: L= 100n						
1	mylib.N65.MOS_car:1	ID				
1	mylib.N65.MOS_car:1	IDP				
1	mylib.N65.MOS_car:1	gm/gds				
1	mylib.N65.MOS_car:1	gm/id				
1	mylib.N65.MOS_car:1	gm/gdsP				
1	mylib.N65.MOS_car:1	gm/idP				
Parameters: L= 150n						
2	mylib.N65.MOS_car:1	ID				
2	mylib.N65.MOS_car:1	IDP				
2	mylib.N65.MOS_car:1	gm/gds				
2	mylib.N65.MOS_car:1	gm/id				
2	mylib.N65.MOS_car:1	gm/gdsP				
2	mylib.N65.MOS_car:1	gm/idP				
Parameters: L= 200n						
3	mylib.N65.MOS_car:1	ID				
3	mylib.N65.MOS_car:1	IDP				
3	mylib.N65.MOS_car:1	gm/gds				
3	mylib.N65.MOS_car:1	gm/id				
3	mylib.N65.MOS_car:1	gm/gdsP				
3	mylib.N65.MOS_car:1	gm/idP				
Parameters: L= 250n						
4	mylib.N65.MOS_car:1	ID				
4	mylib.N65.MOS_car:1	IDP				
4	mylib.N65.MOS_car:1	gm/gds				
4	mylib.N65.MOS_car:1	gm/id				

M1

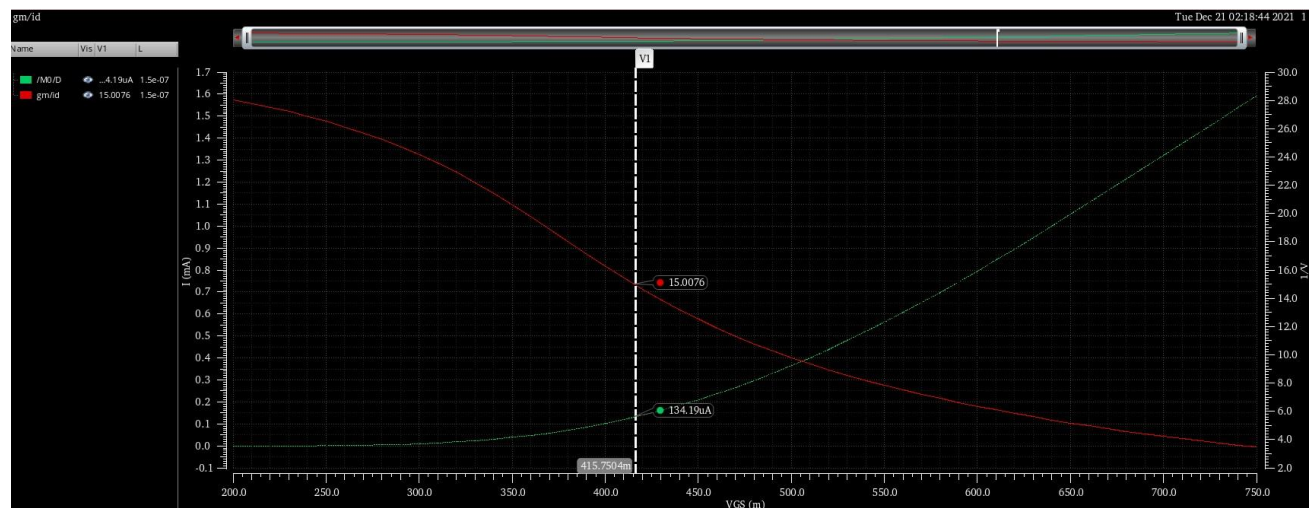
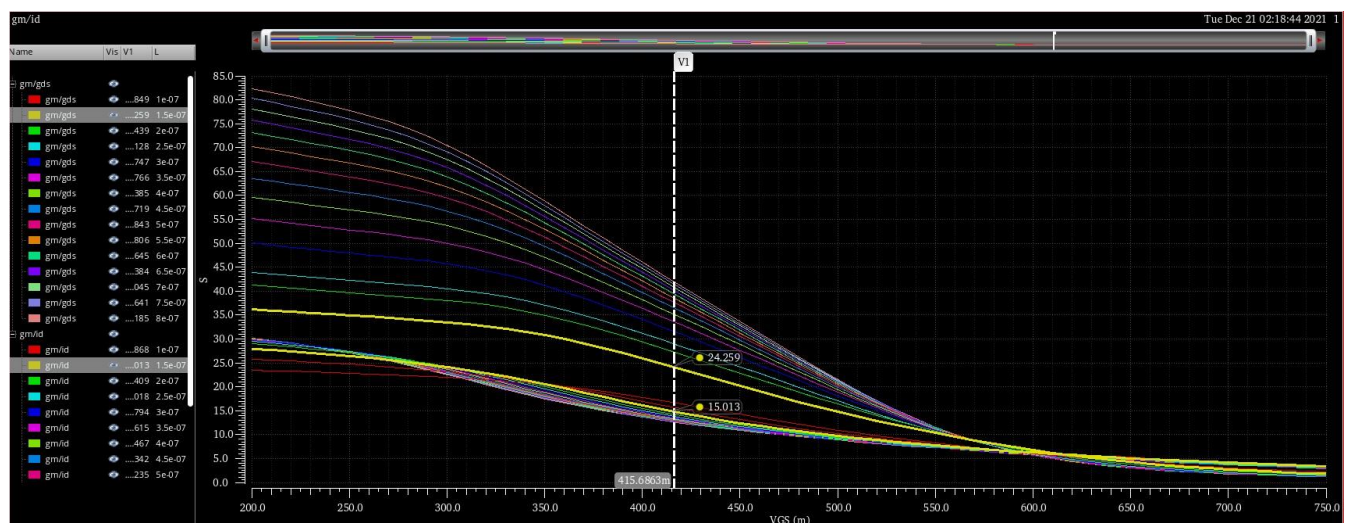
$$\frac{gm}{id} = 15$$

$$\frac{gm}{gds} = 24.45$$

$$l = 150 \text{ nm}$$

$$w = \frac{20}{134.6} * 10 \text{ } \mu\text{m} = 1.486 \text{ } \mu\text{m}$$

$$V_{gs1} = 416 \text{ mV}$$



M3

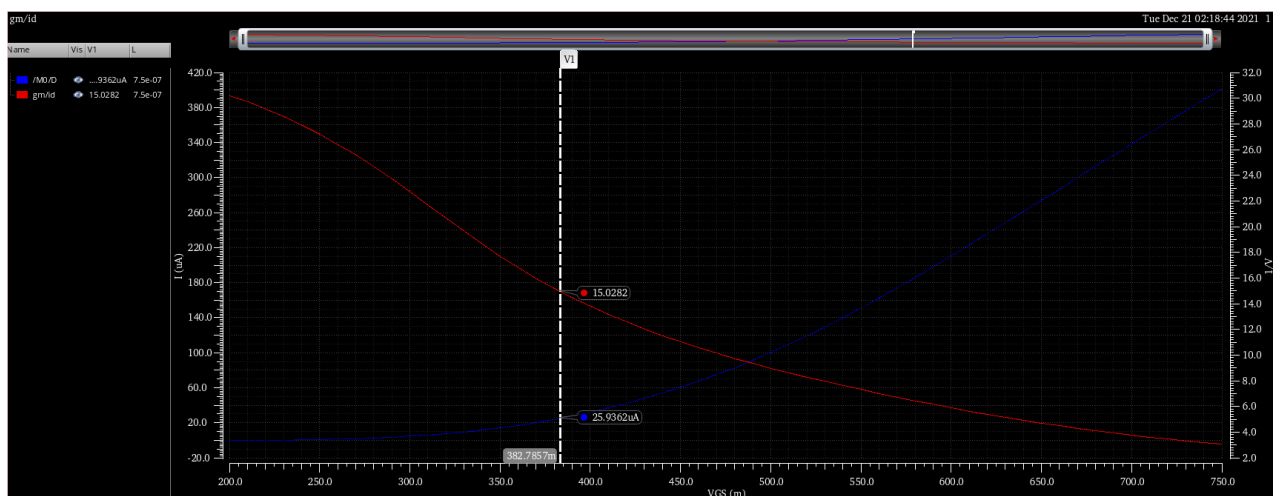
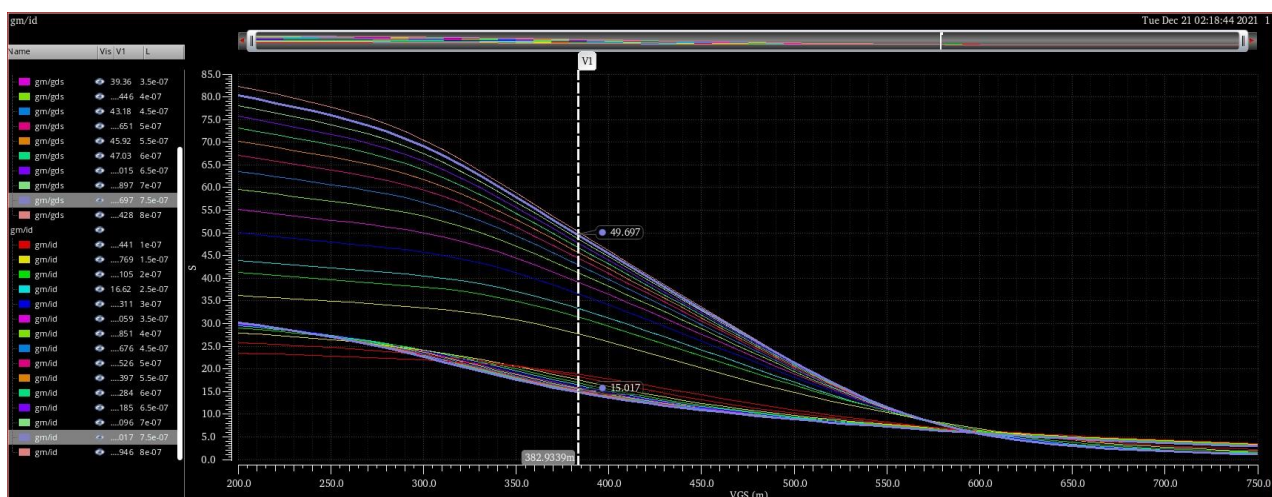
$$\frac{gm}{id} = 15$$

$$\frac{gm}{gds} = 48.9$$

$$l = 750 \text{ nm}$$

$$w = 7.656 \text{ um}$$

$$V_{gs3} = 382.9 \text{ mV}$$



M5

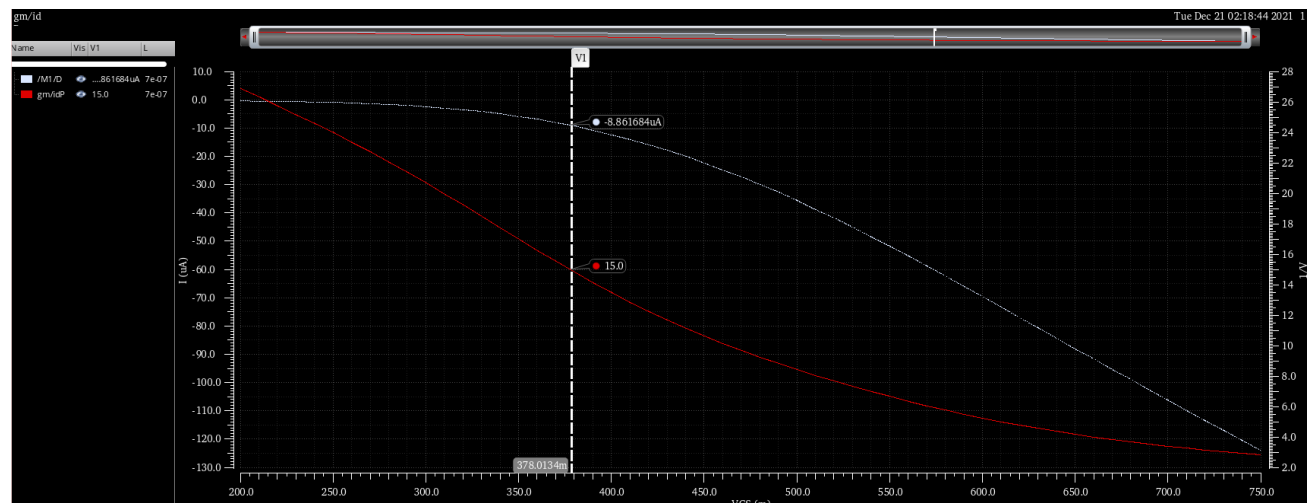
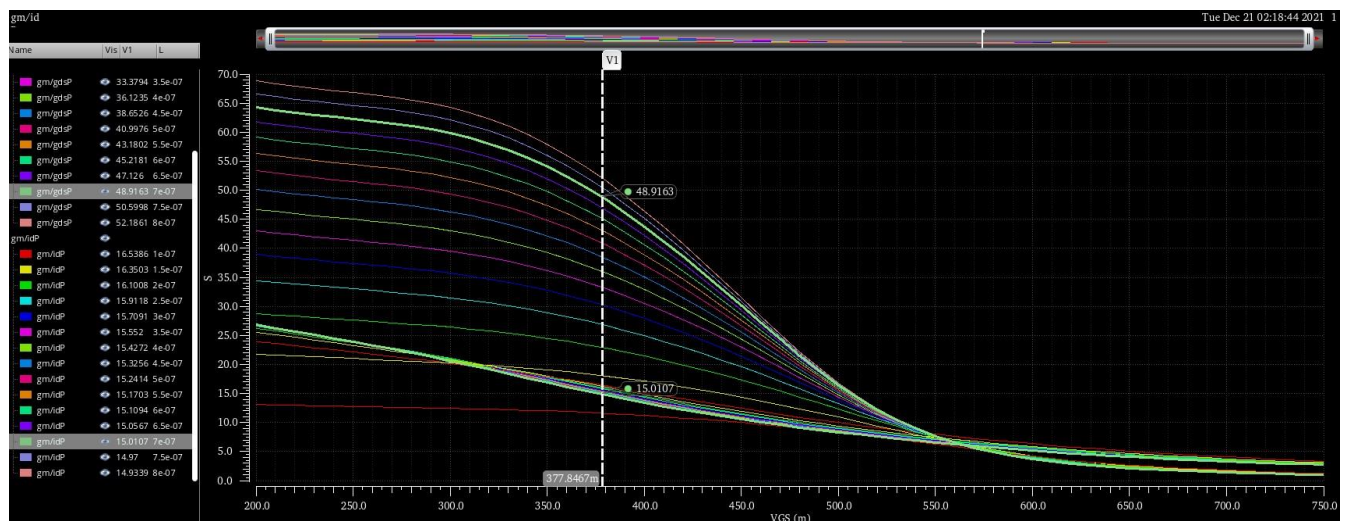
$$\frac{gm}{id} = 15$$

$$\frac{gm}{gds} = 48.9$$

$$l = 700 \text{ nm}$$

$$w = 22.584 \text{ um}$$

$$V_{gs5} = -378 \text{ mV}$$



M7

$$\frac{gm}{id} = 15$$

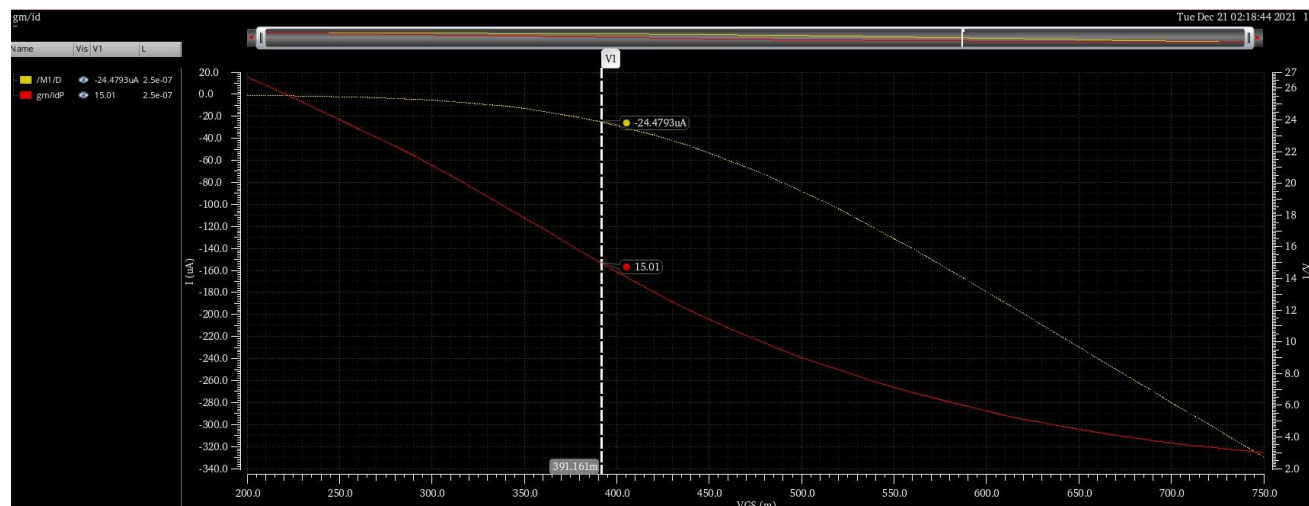
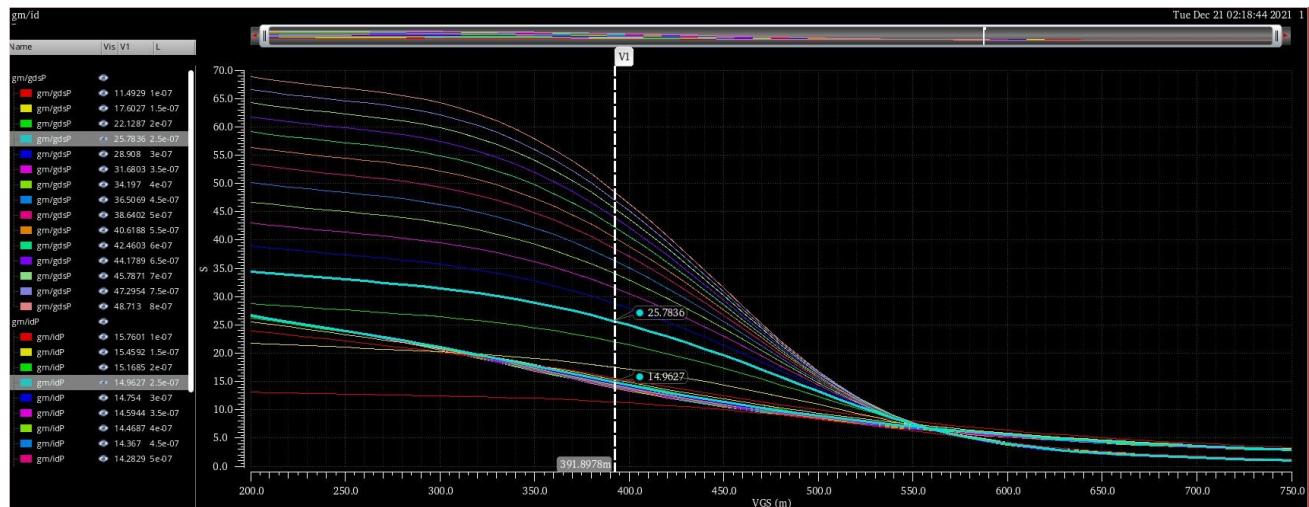
$$\frac{gm}{gds} = 24.45$$

$$l = 250 \text{ nm}$$

$$w = 8.139 \text{ um}$$

$$V_{gs7} = -391.5 \text{ mV}$$

$$V_{load} = 1.2 - 391.5 \text{ m} = 0.808 \text{ mV}$$



Mss

from V input common mode min and VGS1

$$V_{ov5} < 334 \text{ mV}$$

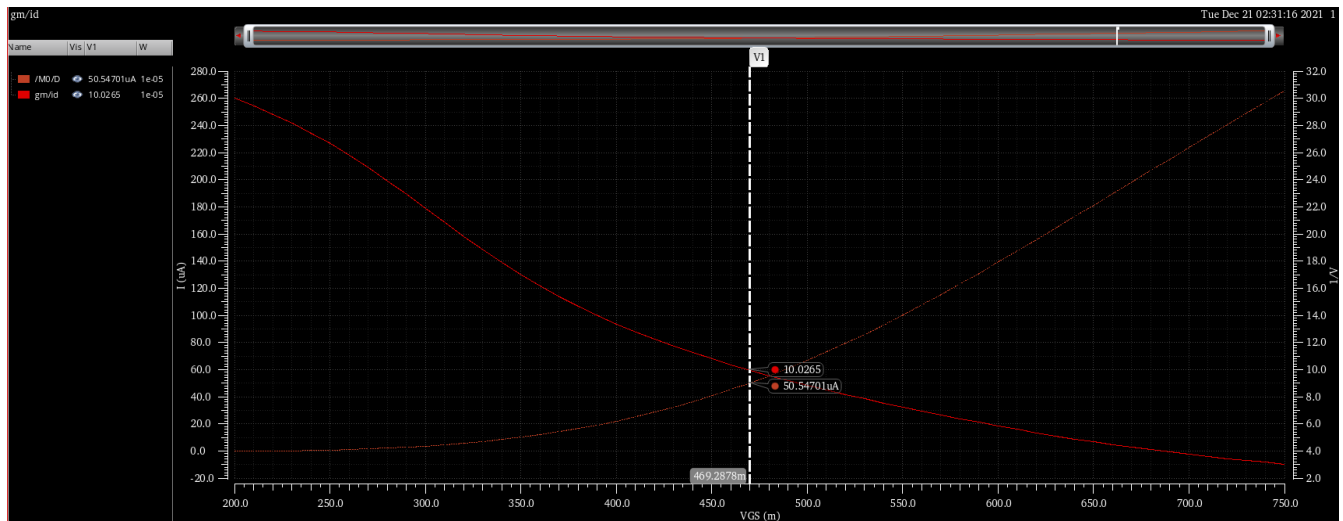
$$V_{ovss} = 200 \text{ mV} \quad \text{assumption}$$

$$\frac{g_{mss}}{i_d} = 10$$

$$L = 1.2 \text{ } \mu\text{m} \quad \text{assumption}$$

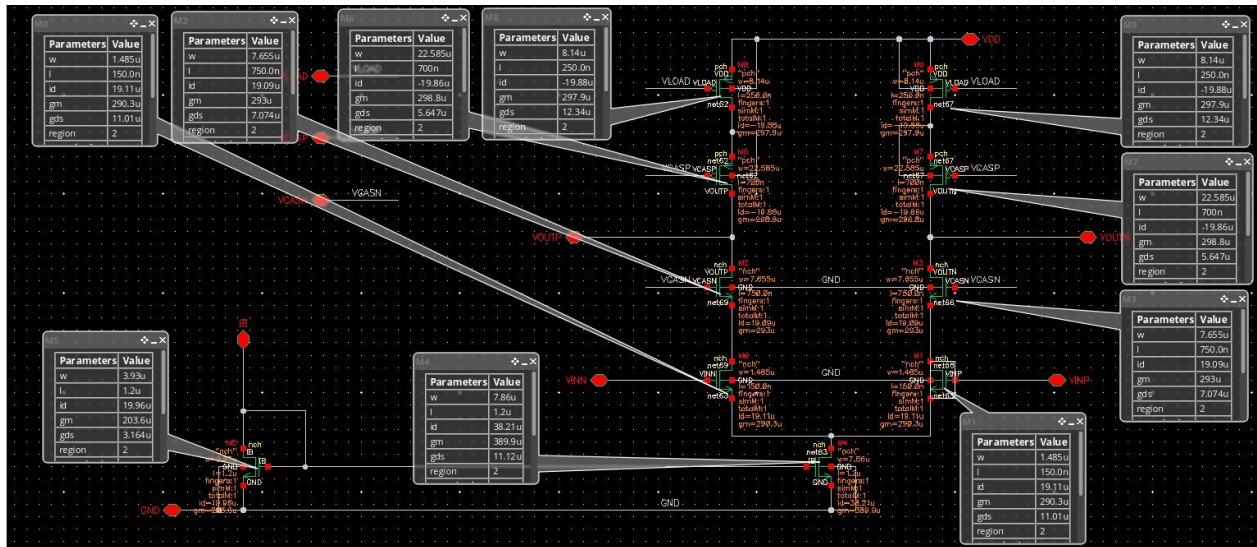
$$w = 7.86 \text{ } \mu\text{m}$$

$$V_{gss} = 474 \text{ mV}$$



3. Final Design

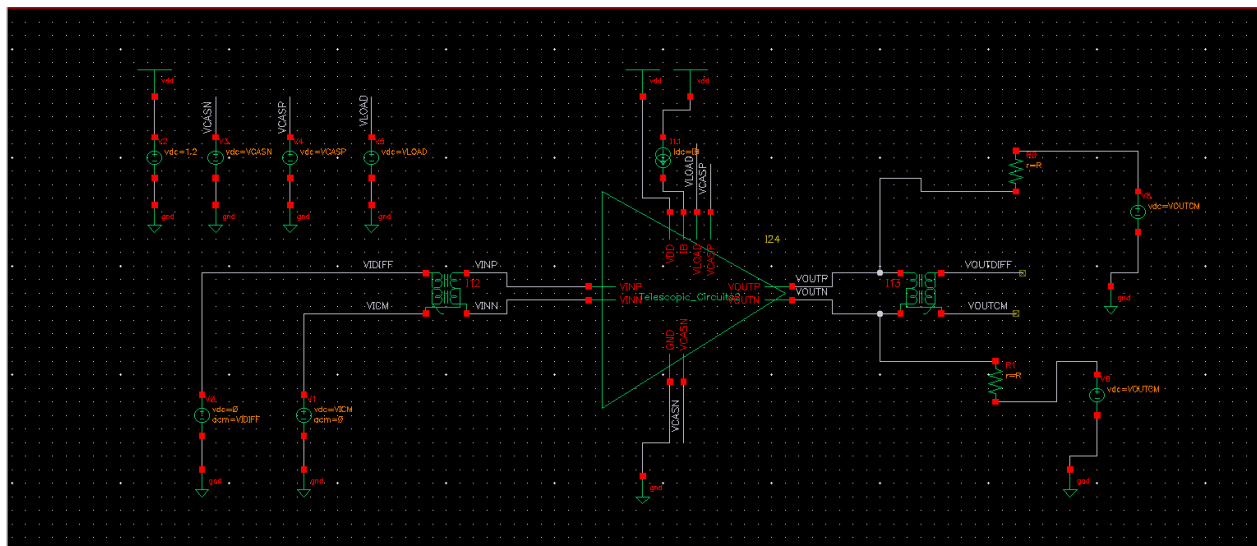
telescopic cascode differential amplifier:



Bias voltages:

Global Variables		
<input checked="" type="checkbox"/>	IB	20u
<input checked="" type="checkbox"/>	VCASN	0.95
<input checked="" type="checkbox"/>	VCASP	0.6
<input checked="" type="checkbox"/>	VICM	0.7
<input checked="" type="checkbox"/>	VIDIFF	1
<input checked="" type="checkbox"/>	VLOAD	808.5m
<input checked="" type="checkbox"/>	VOUTCN	0.72
<input checked="" type="checkbox"/>	R	1m
<input checked="" type="checkbox"/>	MOSCORNERS	"tt"

telescopic cascode differential amplifier testbench:

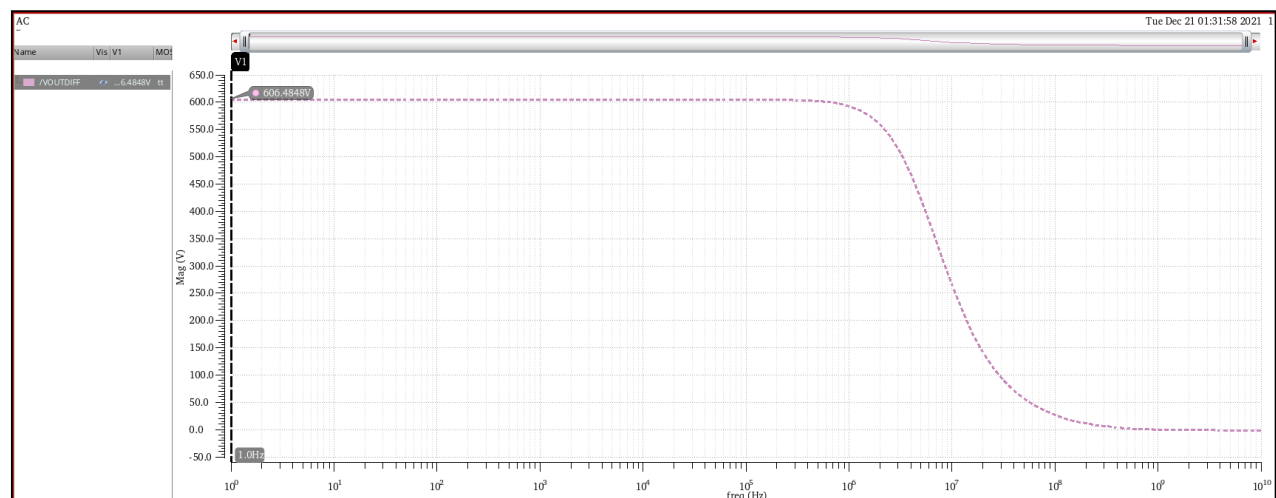


4. Gain and power

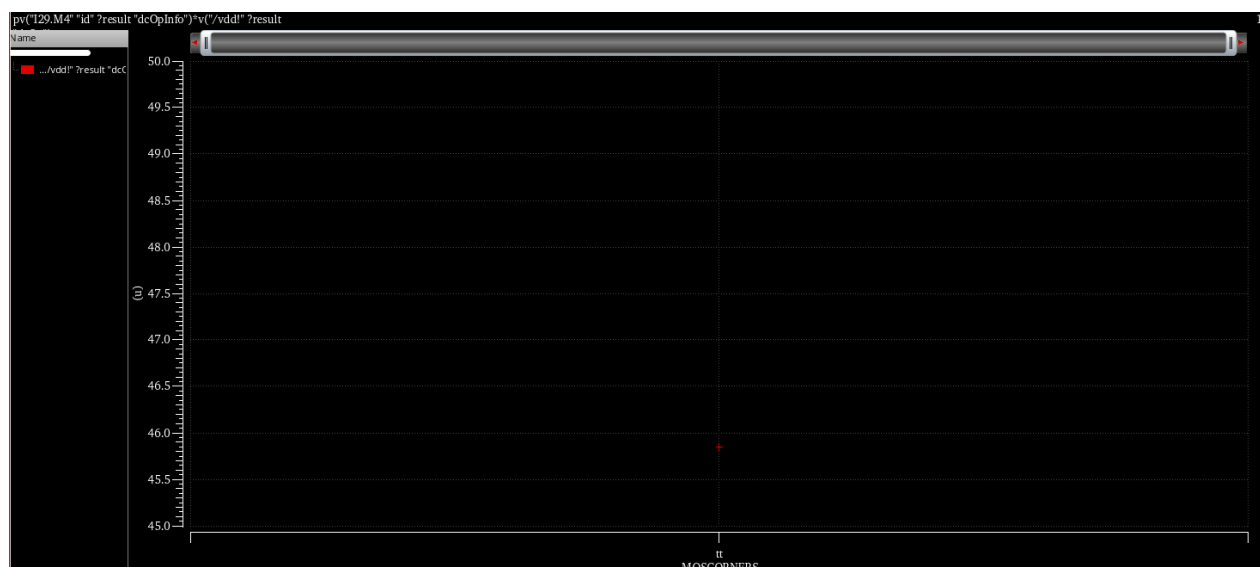
Power and gain:

Outputs Setup Run Preview Results Diagnostics					
Detail					
Test	Output	Nominal	Spec	Weight	Pass/Fail
mylib_N65:Telescopic_Circuits2_TestBench:1	VOUTDIFF				
mylib_N65:Telescopic_Circuits2_TestBench:1	Gain	606.5			
mylib_N65:Telescopic_Circuits2_TestBench:1	power	45.85u			

gain:



Power:





5. Comparison

	gm hand	gm sim	error	ro hand	ro sim	error
M1	300 uS	290.3 uS	3.3 %	81.5 Kohm	90.8 Kohm	10 %
M3	300 uS	293 uS	2.3 %	163 Kohm	141.4 Kohm	15 %
M5	300 uS	298.8 uS	0.4 %	163 Kohm	177.1 Kohm	7.9 %
M7	300 uS	297.9 uS	0.7 %	81.5 Kohm	81.03 Kohm	0.6 %
MSS	400 uS	389.9 uS	2.5 %	-	-	-

$$gain\ error = \frac{gain\ sim - gain\ hand}{gain\ sim} = 0.99\%$$

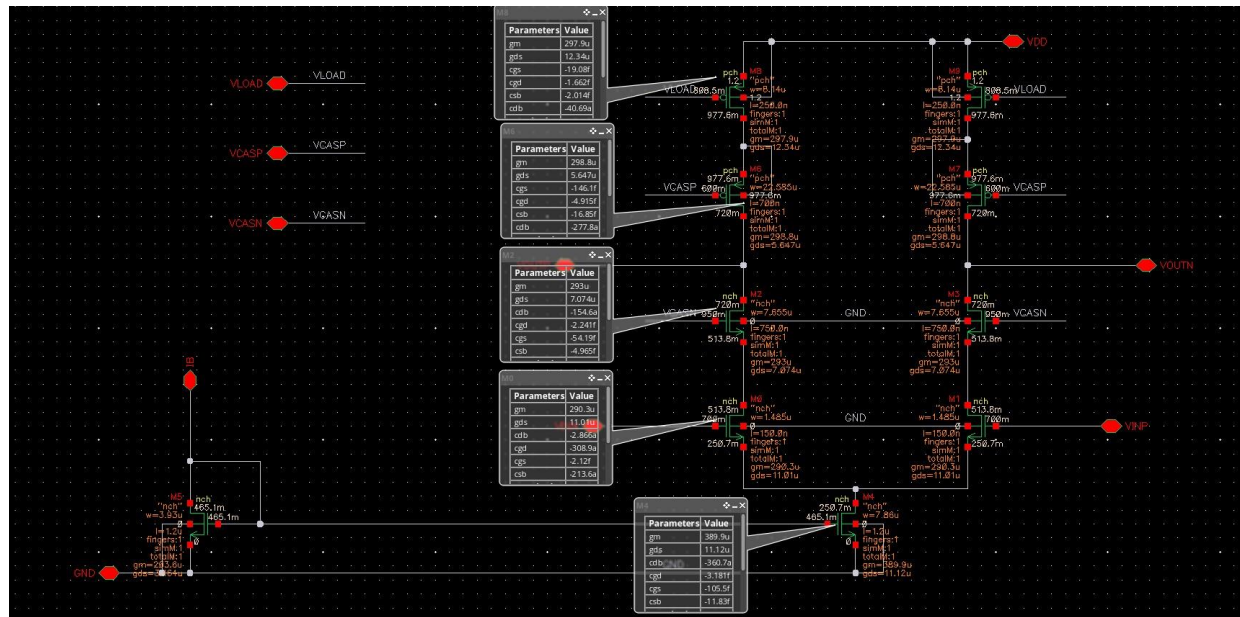
$$I_{SS}\ error = \frac{I_{SS}\ sim - I_{SS}\ hand}{I_{SS}\ sim} = 4.7\ %$$

6. Frequency response

6.1 Hand analysis

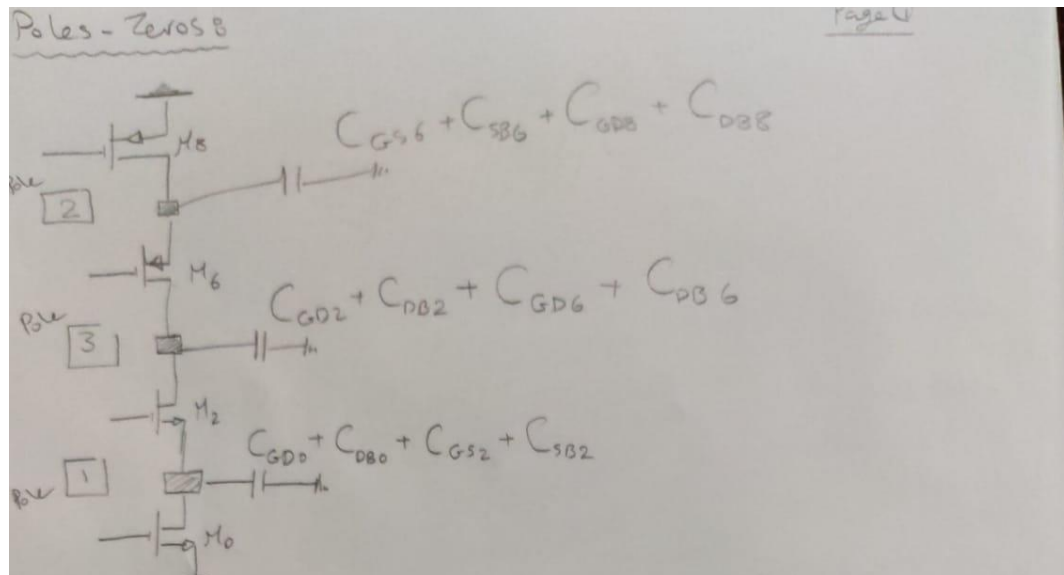
Since the poles are close to each other so they affected each other so they are different from simulations poles results.

Frequency response of transistors:



Poles and zeros calculations:

Poles - Zeros



1) Pole 1):

$$R = r_{o1} \parallel \frac{1}{g_{m2}} \left(1 + \frac{r_{o6} g_{m6} r_{o8}}{r_{o2}} \right)$$

$$= 90.8265 \text{ K}\Omega \parallel 106.9379 \text{ K}\Omega = 49.113 \text{ K}\Omega$$

$$C = C_{gs2} + C_{ds2} + C_{db0} + \left(1 + \frac{1}{A_{v2}} \right) C_{gd}$$

$$A_v = g_{m0} R = 14.258$$

$$C = 59.488 \text{ fF}$$

$$F = 54.475 \times 10^6 \text{ Hz}$$

[2] Pole (2)

$$81.037 \text{ K}\Omega // \frac{1}{g_m} (1 + \frac{R_o}{r_o}) // 74.444 \text{ K}\Omega$$

$$\therefore R = 38.8 \text{ K}\Omega$$

$$C = C_{gd8} + C_{db8} + C_{gs6} + C_{sb6}$$

$$= 164.65 \text{ fF}$$

$$f = 24.913 \times 10^6 \text{ Hz}$$

[3] Pole (3)

$$C_{GD2} = 2.241 \times 10^{-15} \text{ F}$$

$$C_{DB2} = 154.6 \times 10^{-18} \text{ F}$$

$$C_{GD6} = 4.915 \times 10^{-15} \text{ F}$$

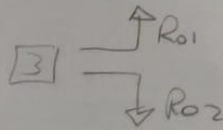
$$C_{DB6} = 2.778 \times 10^{-18} \text{ F}$$

$$R = R_{o1} // R_{o2}$$

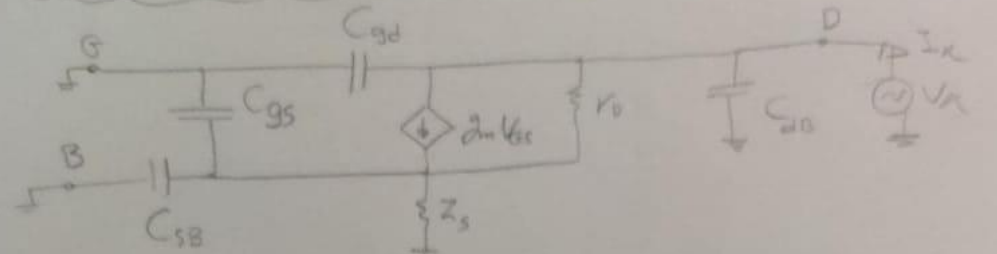
$$R_{o1} = g_{m6} r_{o6} r_{o8}$$

$$R_{o2} = g_{m2} r_{o2} r_{o4}$$

$$\therefore \omega = \frac{1}{RC} \rightarrow f = \frac{\omega}{2\pi} \rightarrow f = 10.466 \times 10^6 \text{ Hz}$$



Small circuit Model used to Calc Zeros



$V_{gs} = -V_s$

$X = \frac{1}{j\omega C_{db}} \parallel \frac{1}{j\omega C_{gd}}$

$Y = \frac{1}{j\omega C_{sb}} \parallel \frac{1}{j\omega C_{gs}}$

$Z_s = X \parallel r_o [1 + g_m (Z_s \parallel Y)]$

Calc First Zero

$X = \frac{1}{j\omega (C_{db8} + C_{gd8})} \parallel \frac{1}{g_{s8}} = \frac{1}{j\omega (C_{gd8} + C_{db8}) + g_{ds8}}$

$Z_{LFD} = \frac{1}{j\omega (C_{db6} + C_{gd6})} \parallel \frac{1}{g_{s6}} \left[1 + g_{m6} \left(\frac{1}{g_{ds8} + j\omega (C_{db8} + C_{gd8}) + C_{sb6} + C_{gs6}} \right) \right]$

$j\omega 5.193 \text{ F} + \frac{5.697 \times 10^{-6}}{1 + 298.8 \times 10^{-6} \left(\frac{1}{12.34 \times 10^{-6} + j\omega 164.65 \text{ F}} \right)} \rightarrow \text{Zero}$

$\omega_0 \text{ F} = 276.9 \text{ MHz}$

$$Z_{SD} = \frac{1}{j\omega(C_{dg2} + C_{gs2})} \parallel \frac{1}{g_{ds2}} \left(1 + (g_{m2} + g_{mb2}) \left(\frac{1}{g_{ds1} + j\omega C_1} \right) \right)$$

$$C_1 = C_{db0} + C_{gd0} + C_{sb2} + C_{gs2} = 59.47 \text{ fF}$$

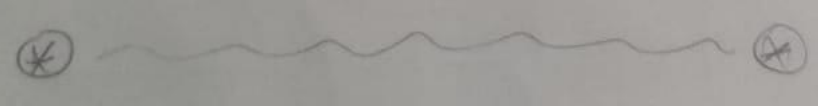
$$\frac{g_{ds2}}{1 + (g_{m2} + g_{mb2}) \left(\frac{1}{g_{ds2} + j\omega C_1} \right)} \rightarrow \infty$$

$$(g_{m2} + g_{mb2}) \left(\frac{1}{g_{ds2} + j\omega C_1} \right) = 1$$

$$321.46 \times 10^{-6} \left(\frac{1}{7.074 \times 10^{-6} + j\omega 59.478} \right) = 1$$

$$\underline{f = 841.36 \text{ MHz}}$$

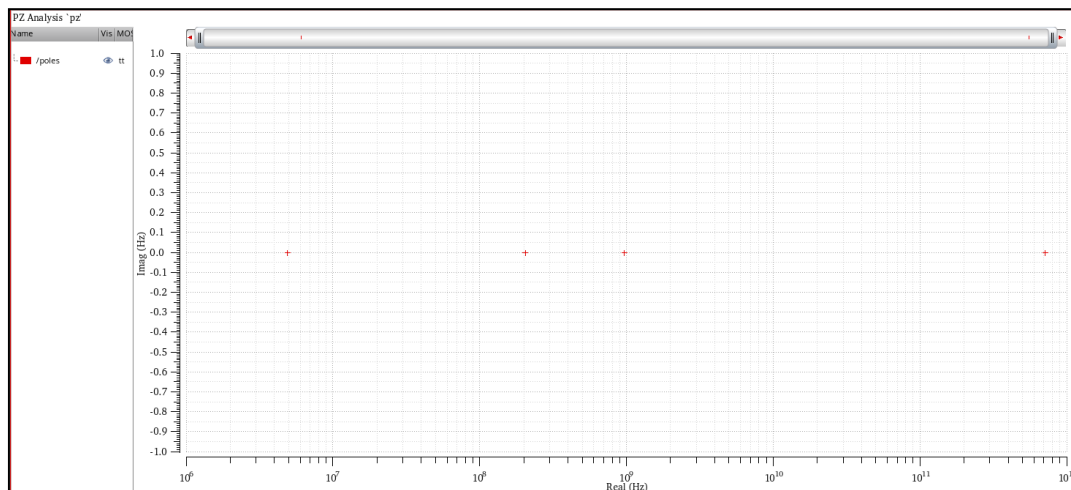
Third zero $\rightarrow \infty$



6.2 Simulation results

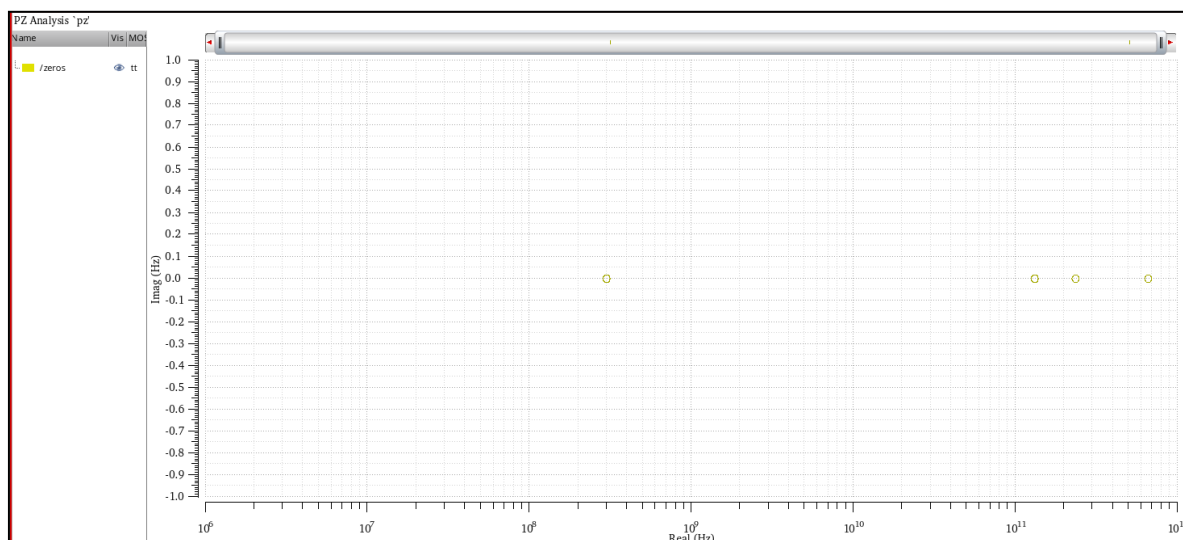
Poles:

	qfactor	/poles M...tt (Hz)
1	500.0E-3	4.852E6
2	500.0E-3	204.1E6
3	500.0E-3	965.6E6
4	500.0E-3	716.7E9

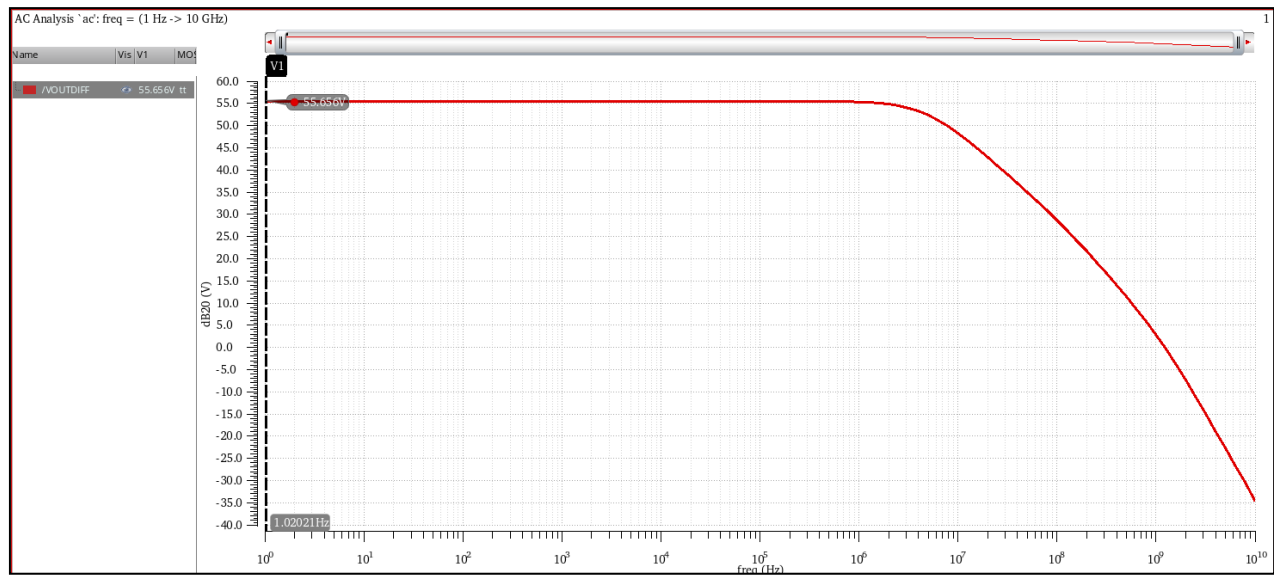


Zeros:

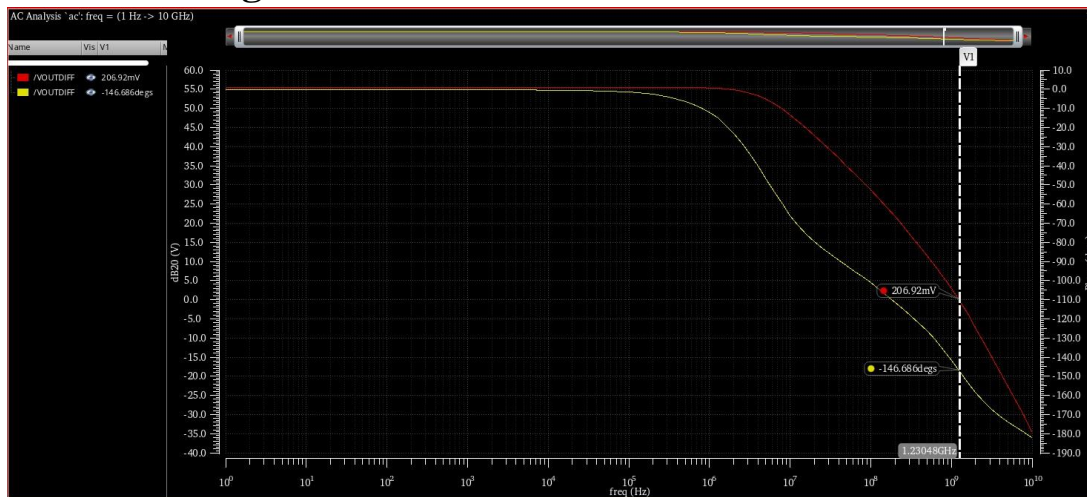
	qfactor	/poles M...tt (Hz)	qfactor	/zeros M...tt (Hz)
1	500.0E-3	4.852E6	500.0E-3	299.6E6
2	500.0E-3	204.1E6	-500.0E-3	132.6E9
3	500.0E-3	965.6E6	-500.0E-3	236.4E9
4	500.0E-3	716.7E9	500.0E-3	665.0E9



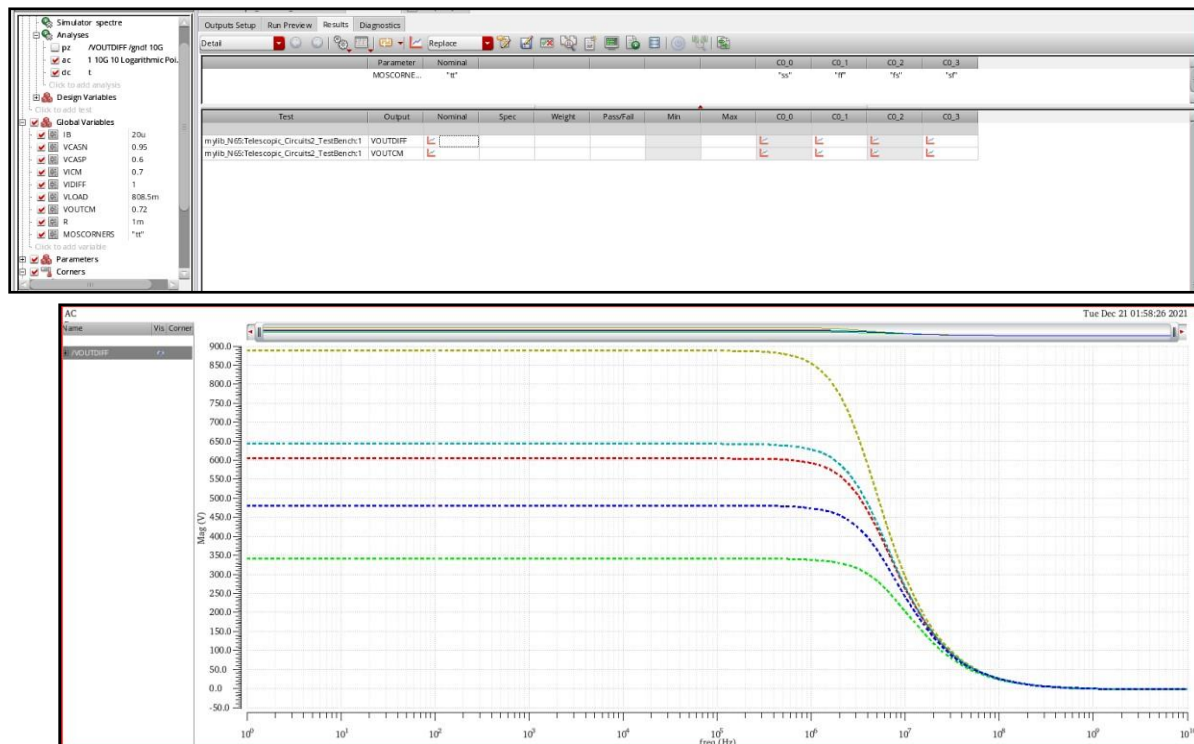
Bode plot:



7. Phase margin

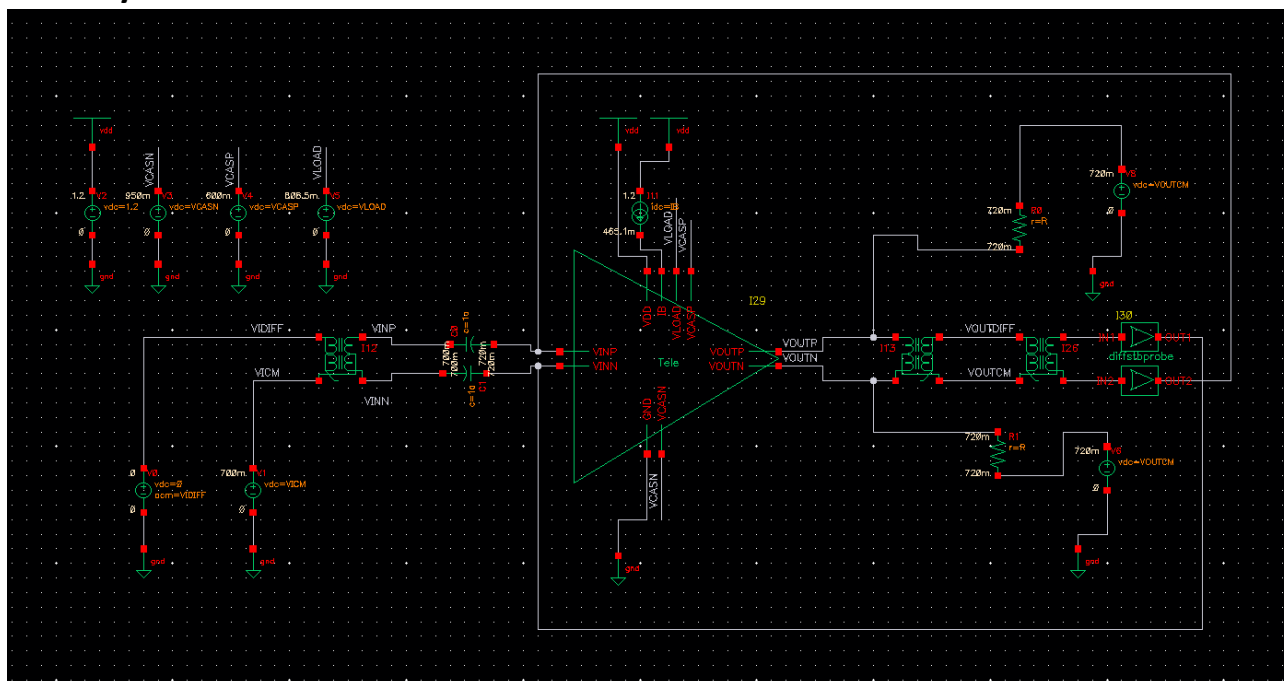


8. Corner analysis

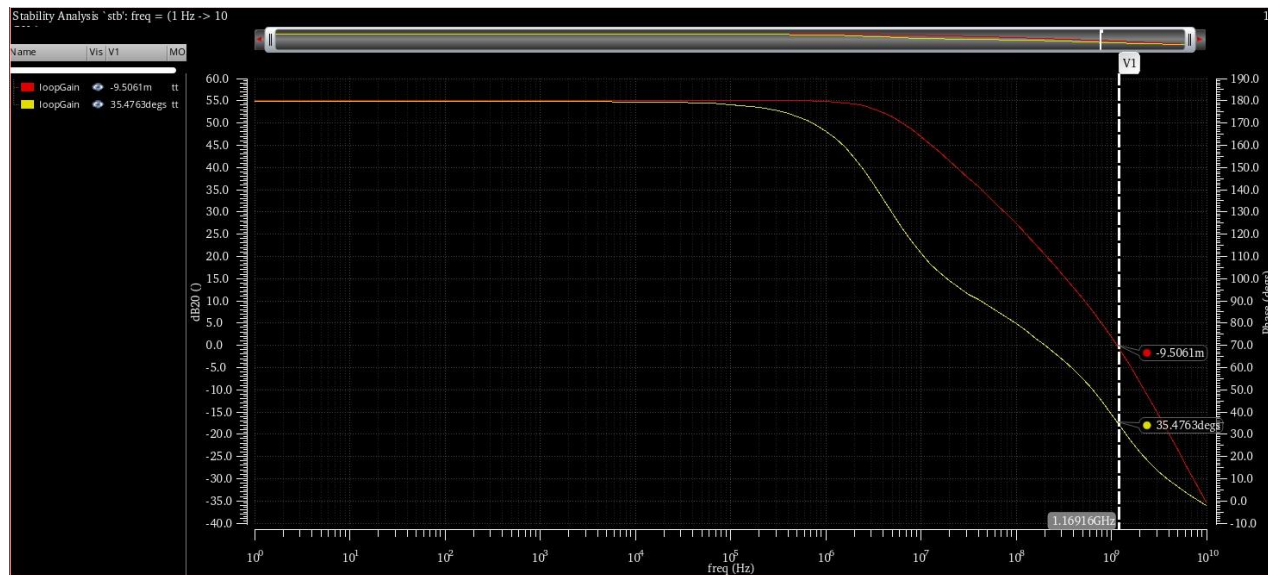


9. Stability analysis

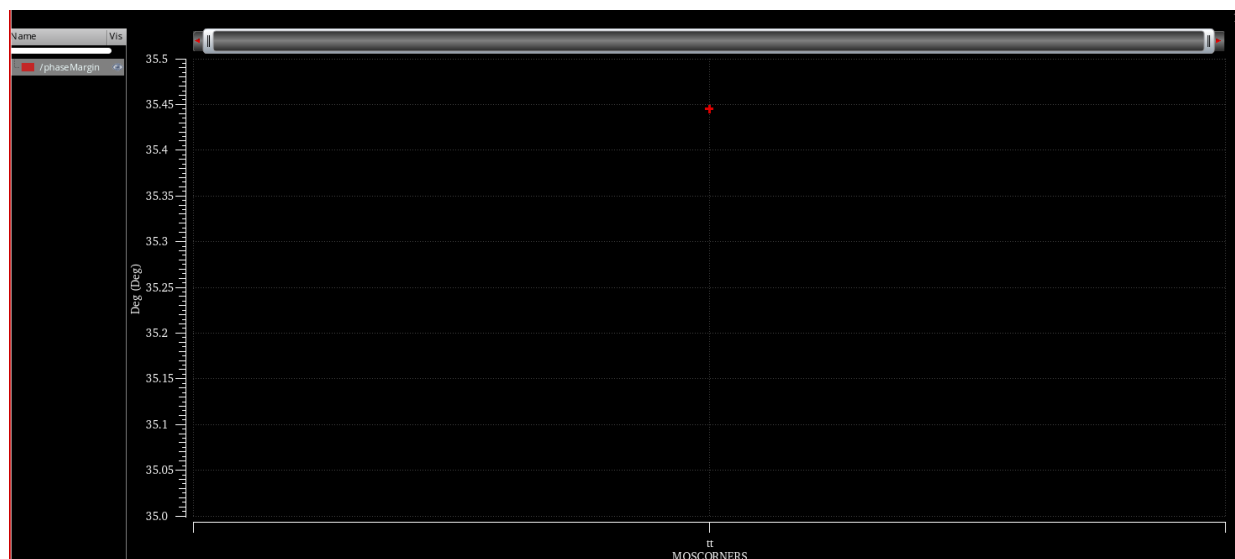
Stability test bench:



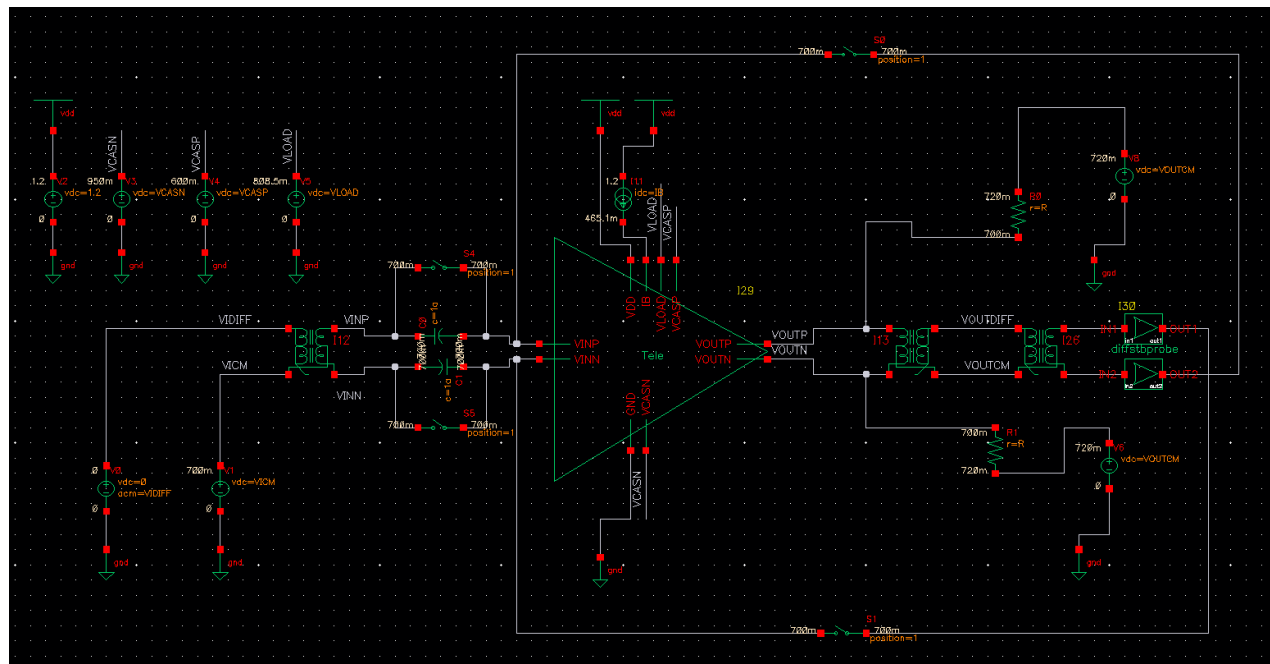
Stability loop gain:



Stability phase margin:



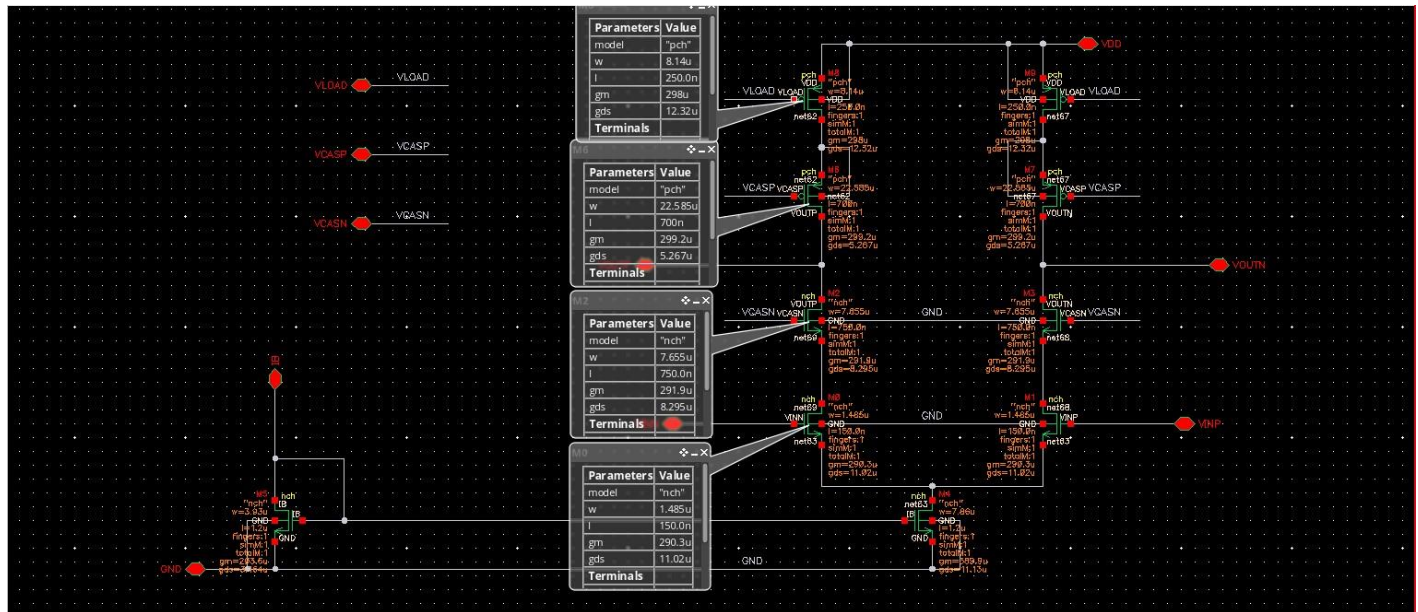
Final testbench




By changing switches conditions we can get open loop simulation and closed loop simulation as well.

10. Mistakes made then resolved

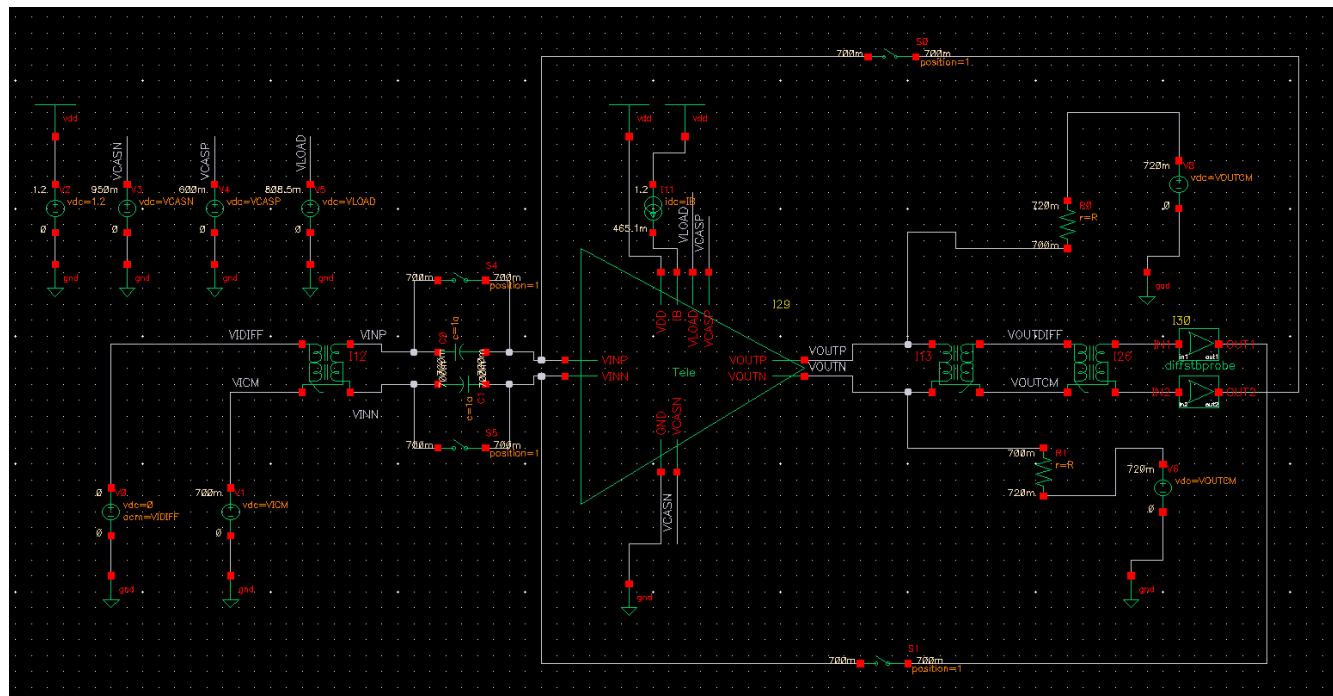
Old design:



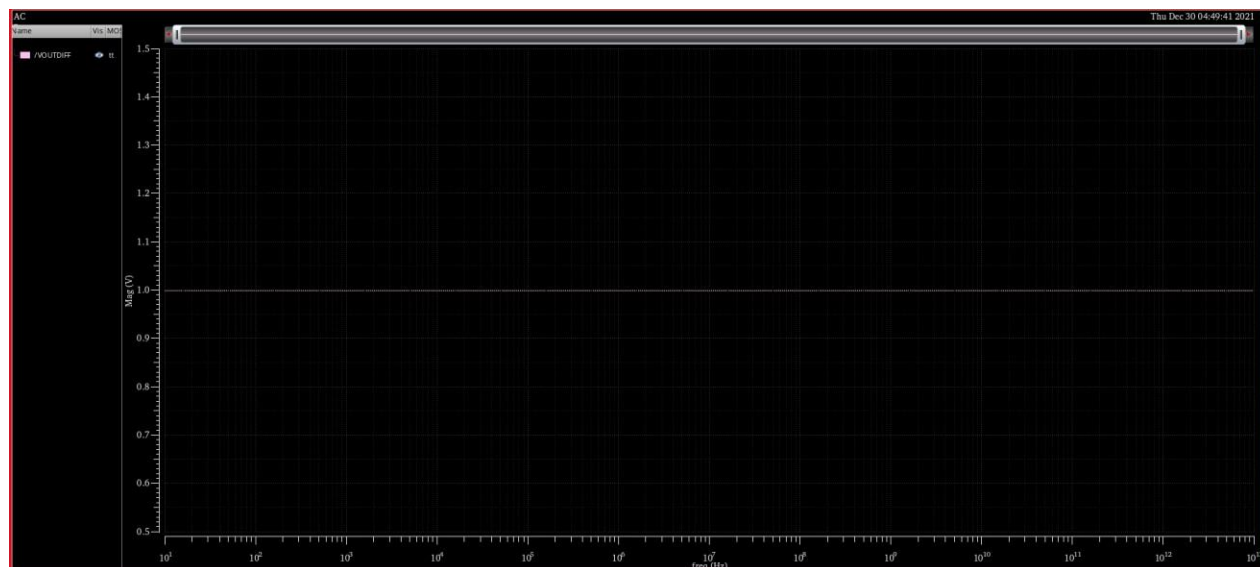
Old gain:

Test	Output	Nominal	Spec	Weight	Pass/Fail
mylib_N65:Telescopic_Circuits2_TestBench:1	VOUDDIFF				
mylib_N65:Telescopic_Circuits2_TestBench:1	Gain	575.5			
mylib_N65:Telescopic_Circuits2_TestBench:1	power	45.85u			

Stability test with wrong switch conditions:



Stability loop gain:





11. Contribution to the project

This was a big project in a short notice, so we teamed up to finish it. my contribution to the project was

- hand analysis
- transistor analysis
- designing circuits
- finding gain and power
- getting the poles and zeros theoretical