

# STM32系列

# 32位微控制器(MCU)



产品选型手册

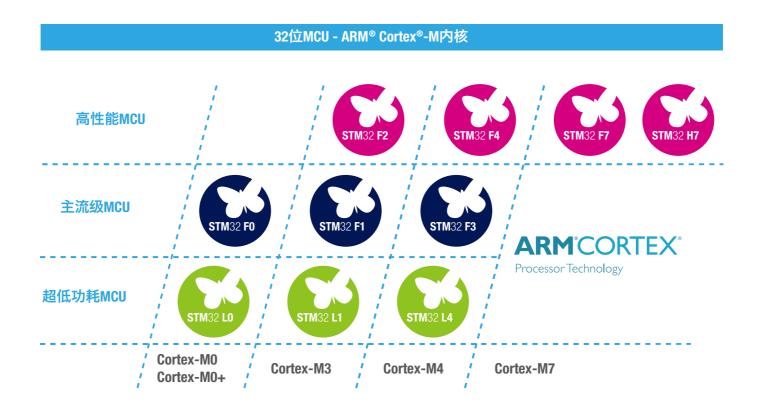




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产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	低功耗定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12:过DAC	比较器	放大器	QUADSPI	S. I.S.	r°C	高速I°C	U(S)ART	低功耗UART	CAN	SDIO	FMC	从USB	全速USB OTG	高速USB OTG	以太网	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	AES	SHA DES/TDES	НМАС
								·										ST	M32I	-0x0走		<u>4</u> – 48	8 MHz		<u> </u>	<u> </u>																			
STM32F030F4P6	48	ARM Cortex-M0	16	4	0	TSS0P20	15	2.4	3.6	5	0 1	0	0	1	11	0	0	0	0	0 1	0	0	1	0	1	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F030K6T6	48	ARM Cortex-M0	32	4	0	LQFP32	26	2.4	3.6	5	0 1	0	0	1	12	0	0	0	0	0 1	0	0	1	0	1	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F030C6T6	48	ARM Cortex-M0	32	4	0	LQFP48	39	2.4	3.6	5	0 1	0	0	1	12	0	0	0	0	0 1	0	0	1	0	1	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F030C8T6	48	ARM Cortex-M0	64	8	0	LQFP48	39	2.4	3.6	7	0 1	0	0	1	12	0	0	0	0	0 2	2 0	0	2	0	2	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F030CCT6	48	ARM Cortex-M0	256	32	0	LQFP48	37	2.4	3.6	8	0 1	0	0	1	12	0	0	0	0	0 2	2 0	0	2	0	6	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F030R8T6	48	ARM Cortex-M0	64	8	0	LQFP64	55	2.4	3.6	7	0 1	0	0	1	18	0	0	0	0	0 2	2 0	0	2	0	2	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F030RCT6	48	ARM Cortex-M0	256	32	0	LQFP64	51	2.4	3.6	8	0 1	0	0	1	18	0	0	0	0	0 2	2 0	0	2	0	6	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F070F6P6	48	ARM Cortex-M0	32	6	0	TSS0P20	15	2.4	3.6	5	0 1	0	0	1	9	0	0	0	0	0 -	0	0	1	0	2	0	0 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F070C6T6	48	ARM Cortex-M0	32	6	0	LQFP48	37	2.4	3.6	5	0 1	0	0	1	10	0	0	0	0	0 1	0	0	1	0	2	0	0 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F070CBT6	48	ARM Cortex-M0	128	16	0	LQFP48	37	2.4	3.6	8	0 1	0	0	1	10	0	0	0	0	0 2	2 0	0	2	0	4	0	0 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F070RBT6	48	ARM Cortex-M0	128	16	0	LQFP64	51	2.4	3.6	8	0 1	0	0	1	16	0	0	0	0	0 2	2 0	0	2	0	4	0	0 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
																		ST	M32	F0x1	でして	<u>u</u> - 48	8 MHz																						
STM32F031F4P6	48	ARM Cortex-M0	16	4	0	TSS0P20	15	2	3.6	5	1 1	0	0	1	9	0	0	0	0	0 1	0	1	1	0	1	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F031G4U6	48	ARM Cortex-M0	16	4	0	UFQFPN28	23	2	3.6	5	1 1	0	0	1	10	0	0	0	0	0 1	0	1	1	0	1	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F031K4U6	48	ARM Cortex-M0	16	4	0	UFQFPN32	27	2	3.6	5	1 1	0	0	1	10	0	0	0	0	0 1	0	1	1	0	1	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F031K4T6	48	ARM Cortex-M0	16	4	0	LQFP32	25	2	3.6	5	1 1	0	0	1	10	0	0	0	0	0 -	0	1	1	0	1	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F031C4T6	48	ARM Cortex-M0	16	4	0	LQFP48	39	2	3.6	5	1 1	0	0	1	10	0	0	0	0	0 1	0	1	1	0	1	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F031F6P6	48	ARM Cortex-M0	32	4	0	TSS0P20	15	2	3.6	5	1 1	0	0	1	9	0	0	0	0	0 1	0	1	1	0	1	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F031E6Y6	48	ARM Cortex-M0	32	4	0	WLCSP25	20	2	3.6	5	1 1	0	0	1	10	0	0	0	0	0 1	0	1	1	0	1	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F031G6U6	48	ARM Cortex-M0	32	4	0	UFQFPN28	23	2	3.6	5	1 1	0	0	1	10	0	0	0	0	0 -	0	1	1	0	1	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F031K6T6	48	ARM Cortex-M0	32	4	0	LQFP32	25	2	3.6	5	1 1	0	0	1	10	0	0	0	0	0 -	0	1	1	0	1	0	0 (	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0

产品型号	主频 (MHz)	内核	FLASH (KB)	<b>ВАМ (КВ)</b>	EEPROM (B)	封装	通用IO	最低工作	最高工作	16位定时器	32位定	电机控制	高分辨率	12位ADC转	12位AD0	16.过ADC转	16立ADC預道	比较器	放大器	SPI	QUADSPI	l²S	l°C	高速l°C	U(S)ART	低功耗	SDIO	FSMC	FMC	从USB	全速USB OTG	高速USBO	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES AES	SHA	HMAC
号	/IHZ)	12	(KB)	KB)	/ (B)	24	0	低工作电压	工作电压	器	定时器	制定时器 器	半 定 时 器	ADC转换单元	ADC通道	立ADC转换单元	· 直 道	器	器		SPI			<sup>2</sup> C	콥	功耗UART		C	Ü	ä	3 OTG	速USB OTG	S   E	G	G	)ST		RX	Š		≦ !	T I	DES		C
STM32F031K6U6	48	ARM Cortex-M0	32	4	0	UFQFPN32	27	2	3.6	5	1	1 0	0	1	10	0	0 0	0	0	1	0	1	1	0	1	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0	0
STM32F031C6T6	48	ARM Cortex-M0	32	4	0	LQFP48	39	2	3.6	5	1	1 0	0	1	10	0	0 0	0	0	1	0	1	1	0	1	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051K4T6	48	ARM Cortex-M0	16	8	0	LQFP32	25	2	3.6	7	1	1 0	0	1	10	0	0 1	2	0	1	0	1	1	0	1	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051K4U6	48	ARM Cortex-M0	16	8	0	UFQFPN32	27	2	3.6	7	1	1 0	0	1	10	0	0 1	2	0	1	0	1	1	0	1	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051K6T6	48	ARM Cortex-M0	32	8	0	LQFP32	25	2	3.6	7	1	1 0	0	1	10	0	0 1	2	0	1	0	1	1	0	2	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051K6U6	48	ARM Cortex-M0	32	8	0	UFQFPN32	27	2	3.6	7	1	1 0	0	1	10	0	0 1	2	0	1	0	1	1	0	2	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051K8T6	48	ARM Cortex-M0	64	8	0	LQFP32	25	2	3.6	7	1	1 0	0	1	10	0	0 1	2	0	1	0	1	1	0	2	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051K8U6	48	ARM Cortex-M0	64	8	0	UFQFPN32	27	2	3.6	7	1	1 0	0	1	10	0	0 1	2	0	1	0	1	1	0	2	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051T8Y6	48	ARM Cortex-M0	64	8	0	WLCSP36	29	2	3.6	7	1	1 0	0	1	10	0	0 1	2	0	1	0	1	1	0	2	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051C4T6	48	ARM Cortex-M0	16	8	0	LQFP48	39	2	3.6	7	1	1 0	0	1	10	0	0 1	2	0	1	0	1	1	0	1	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051C4U6	48	ARM Cortex-M0	16	8	0	UFQFPN48	39	2	3.6	7	1	1 0	0	1	10	0	0 1	2	0	1	0	1	1	0	1	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051C6T6	48	ARM Cortex-M0	32	8	0	LQFP48	39	2	3.6	7	1	1 0	0	1	10	0	0 1	2	0	1	0	1	1	0	2	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051C6U6	48	ARM Cortex-M0	32	8	0	UFQFPN48	39	2	3.6	7	1	1 0	0	1	10	0	0 1	2	0	1	0	1	1	0	2	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051C8T6	48	ARM Cortex-M0	64	8	0	LQFP48	39	2	3.6	7	1	1 0	0	1	10	0	0 1	2	0	2	0	1	2	0	2	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051C8U6	48	ARM Cortex-M0	64	8	0	UFQFPN48	39	2	3.6	7	1	1 0	0	1	10	0	0 1	2	0	2	0	1	2	0	2	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051R4T6	48	ARM Cortex-M0	16	8	0	LQFP64	55	2	3.6	7	1	1 0	0	1	16	0	0 1	2	0	2	0	1	1	0	1	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051R6T6	48	ARM Cortex-M0	32	8	0	LQFP64	55	2	3.6	7	1	1 0	0	1	16	0	0 1	2	0	2	0	1	1	0	2	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051R8T6	48	ARM Cortex-M0	64	8	0	LQFP64	55	2	3.6	7	1	1 0	0	1	16	0	0 1	2	0	2	0	1	2	0	1	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051R4H6	48	ARM Cortex-M0	16	8	0	UFBGA64	55	2	3.6	7	1	1 0	0	1	16	0	0 1	2	0	2	0	1	1	0	1	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051R6H6	48	ARM Cortex-M0	32	8	0	UFBGA64	55	2	3.6	7	1	1 0	0	1	16	0	0 1	2	0	2	0	1	1	0	2	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F051R8H6	48	ARM Cortex-M0	64	8	0	UFBGA64	55	2	3.6	7	1	1 0	0	1	16	0	0 1	2	0	2	0	1	2	0	2	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0

产品型号	主频 (MHz)	<b>内</b> 核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16岁ADC转换单元	16立ADC預首	12位700	放大器	SPI	QUADSPI	l²S	l²C	高速l°C	U(S)ART	低功耗UART	SDIO	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	以 MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES	AHS	HMAC
					<b>3</b>			压	压	皕	皕	器器	器	单元	道	単に	首									7					ត	G													
STM32F071CBY6	48	ARM Cortex-M0	128	16	0	WLCSP49	37	2	3.6	8	1	1 0	0	1	10	0	0 2	2 2	0	2	0	2	2	0	4	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F071CBT6	48	ARM Cortex-M0	128	16	0	LQFP48	37	2	3.6	8	1	1 0	0	1	10	0	0 2	2 2	0	2	0	2	2	0	4	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F071CBU6	48	ARM Cortex-M0	128	16	0	UFQFPN48	37	2	3.6	8	1	1 0	0	1	10	0	0 2	2 2	0	2	0	2	2	0	4	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F071RBT6	48	ARM Cortex-M0	128	16	0	LQFP64	51	2	3.6	8	1	1 0	0	1	16	0	0 2	2 2	0	2	0	2	2	0	4	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F071V8H6	48	ARM Cortex-M0	64	16	0	UFBGA100	87	2	3.6	8	1	1 0	0	1	16	0	0 2	2 2	0	2	0	2	2	0	4	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F071V8T6	48	ARM Cortex-M0	64	16	0	LQFP100	87	2	3.6	8	1	1 0	0	1	16	0	0 2	2 2	0	2	0	2	2	0	4	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F071VBH6	48	ARM Cortex-M0	128	16	0	UFBGA100	87	2	3.6	8	1	1 0	0	1	16	0	0 2	2 2	0	2	0	2	2	0	4	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F071VBT6	48	ARM Cortex-M0	128	16	0	LQFP100	87	2	3.6	8	1	1 0	0	1	16	0	0 2	2 2	0	2	0	2	2	0	4	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F091CBT6	48	ARM Cortex-M0	128	32	0	LQFP48	38	2	3.6	8	1	1 0	0	1	10	0	0 2	2 2	0	2	0	2	2	0	6	0	1 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F091CBU6	48	ARM Cortex-M0	128	32	0	UFQFPN48	38	2	3.6	8	1	1 0	0	1	10	0	0 2	2 2	0	2	0	2	2	0	6	0	1 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F091CCT6	48	ARM Cortex-M0	256	32	0	LQFP48	38	2	3.6	8	1	1 0	0	1	10	0	0 2	2 2	0	2	0	2	2	0	6	0	1 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F091CCU6	48	ARM Cortex-M0	256	32	0	UFQFPN48	38	2	3.6	8	1	1 0	0	1	10	0	0 2	2 2	0	2	0	2	2	0	6	0	1 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F091RBT6	48	ARM Cortex-M0	128	32	0	LQFP64	52	2	3.6	8	1	1 0	0	1	16	0	0 2	2 2	0	2	0	2	2	0	8	0	1 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F091RBH6	48	ARM Cortex-M0	128	32	0	UFBGA64	52	2	3.6	8	1	1 0	0	1	16	0	0 2	2 2	0	2	0	2	2	0	8	0	1 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F091RBY6	48	ARM Cortex-M0	128	32	0	WLCSP64	52	2	3.6	8	1	1 0	0	1	16	0	0 2	2 2	0	2	0	2	2	0	8	0	1 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F091RCT6	48	ARM Cortex-M0	256	32	0	LQFP64	52	2	3.6	8	1	1 0	0	1	16	0	0 2	2 2	0	2	0	2	2	0	8	0	1 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F091RCH6	48	ARM Cortex-M0	256	32	0	UFBGA64	52	2	3.6	8	1	1 0	0	1	16	0	0 2	2 2	0	2	0	2	2	0	8	0	1 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F091RCY6	48	ARM Cortex-M0	256	32	0	WLCSP64	52	2	3.6	8	1	1 0	0	1	16	0	0 2	2 2	0	2	0	2	2	0	8	0	1 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F091VBT6	48	ARM Cortex-M0	128	32	0	LQFP100	88	2	3.6	8	1	1 0	0	1	16	0	0 2	2 2	0	2	0	2	2	0	8	0	1 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F091VBH6	48	ARM Cortex-M0	128	32	0	UFBGA100	88	2	3.6	8	1	1 0	0	1	16	0	0 2	2 2	0	2	0	2	2	0	8	0	1 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F091VCT6	48	ARM Cortex-M0	256	32	0	LQFP100	88	2	3.6	8	1	1 0	0	1	16	0	0 2	2 2	0	2	0	2	2	0	8	0	1 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12位DAC	比交番	女 SPI	QUADSPI	I'S	l²C	高速I <sup>®</sup> C	U(S)ART	低功耗UART	CAN	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	以太网 MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	AES	DES/TDES	HMAC
STM32F091VCH6	48	ARM Cortex-M0	256	32	0	UFBGA100	88	2	3.6	8	1	1 0	0	1	16	0	0	2 2	2 0	) 2	0	2	2	0	8	0	1 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
																		STI	//32F	0x2 U	SB型	- 48	MHz																						
STM32F042F4P6	48	ARM Cortex-M0	16	6	0	TSS0P20	16	2	3.6	5	1	1 0	0	1	9	0	0	0 (	) (	) 1	0	1	1	0	2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F042F6P6	48	ARM Cortex-M0	32	6	0	TSS0P20	16	2	3.6	5	1	1 0	0	1	9	0	0	0 (	) (	) 1	0	1	1	0	2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F042G4U6	48	ARM Cortex-M0	16	6	0	UFQFPN28	24	2	3.6	5	1	1 0	0	1	10	0	0	0 (	) (	) 1	0	1	1	0	2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F042G6U6	48	ARM Cortex-M0	32	6	0	UFQFPN28	24	2	3.6	5	1	1 0	0	1	10	0	0	0 (	) (	) 1	0	1	1	0	2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F042K4T6	48	ARM Cortex-M0	16	6	0	LQFP32	26	2	3.6	5	1	1 0	0	1	10	0	0	0 (	) (	) 1	0	1	1	0	2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F042K4U6	48	ARM Cortex-M0	16	6	0	UFQFPN32	28	2	3.6	5	1	1 0	0	1	10	0	0	0 (	) (	1	0	1	1	0	2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F042K6T6	48	ARM Cortex-M0	32	6	0	LQFP32	26	2	3.6	5	1	1 0	0	1	10	0	0	0 (	) (	1	0	1	1	0	2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F042K6U6	48	ARM Cortex-M0	32	6	0	UFQFPN32	28	2	3.6	5	1	1 0	0	1	10	0	0	0 (	) (	1	0	1	1	0	2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F042T4Y6	48	ARM Cortex-M0	16	6	0	WLCSP36	30	2	3.6	5	1	1 0	0	1	10	0	0	0 (	) (	) 1	0	1	1	0	2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F042T6Y6	48	ARM Cortex-M0	32	6	0	WLCSP36	30	2	3.6	5	1	1 0	0	1	10	0	0	0 (	) (	1	0	1	1	0	2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F042C4T6	48	ARM Cortex-M0	16	6	0	LQFP48	38	2	3.6	5	1	1 0	0	1	10	0	0	0 (	) (	) 2	0	1	1	0	2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F042C4U6	48	ARM Cortex-M0	16	6	0	UFQFPN48	38	2	3.6	5	1	1 0	0	1	10	0	0	0 (	) (	) 2	0	1	1	0	2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F042C6T6	48	ARM Cortex-M0	32	6	0	LQFP48	38	2	3.6	5	1	1 0	0	1	10	0	0	0 (	) (	) 2	0	1	1	0	2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F042C6U6	48	ARM Cortex-M0	32	6	0	UFQFPN48	38	2	3.6	5	1	1 0	0	1	10	0	0	0 (	) (	) 2	0	1	1	0	2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F072C8T6	48	ARM Cortex-M0	64	16	0	LQFP48	37	2	3.6	8	1	1 0	0	1	10	0	0	2 2	2 0	) 2	0	2	2	0	4	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F072C8U6	48	ARM Cortex-M0	64	16	0	UFQFPN48	37	2	3.6	8	1	1 0	0	1	10	0	0	2 2	2 0	) 2	0	2	2	0	4	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F072CBY6	48	ARM Cortex-M0	128	16	0	WLCSP49	37	2	3.6	8	1	1 0	0	1	10	0	0	2 2	2 0	) 2	0	2	2	0	4	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F072CBT6	48	ARM Cortex-M0	128	16	0	LQFP48	37	2	3.6	8	1	1 0	0	1	10	0	0	2 2	2 (	) 2	0	2	2	0	4	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F072CBU6	48	ARM Cortex-M0	128	16	0	UFQFPN48	37	2	3.6	8	1	1 0	0	1	10	0	0	2 2	2 0	) 2	0	2	2	0	4	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F072R8T6	48	ARM Cortex-M0	64	16	0	LQFP64	51	2	3.6	8	1	1 0	0	1	16	0	0	2 2	2 (	) 2	0	2	2	0	4	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0

THE TRANSPORTER SHEET SH																																														
SMSCONSIGNATION SINGER SINGER SINGER SINGER SINCE SINC	产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电机空制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12立DAC	放大器	SPI	QUADSPI	I²S	I <sup>2</sup> C	高速l²C	U(S)ART	低功耗UART	CAN	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DGMI	SWPMI	TRNG	DES/TDES AFS	SHA	НМАС
SINGESTICATION OF SINGESTICATI	STM32F072RBH6	48		128	16	0	UFBGA64	51	2	3.6	8	1	1 0	0	1	16	0	0	2 2	0	2	0	2	2	0	4	0	1 0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
SMSSTONS 16 Substone 16 Substone 17 Substo	STM32F072RBT6	48		128	16	0	LQFP64	51	2	3.6	8	1	1 0	0	1	16	0	0	2 2	0	2	0	2	2	0	4	0	1 0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STMSSPTORYNNIN 64 Contention of the content of the	STM32F072V8H6	48		64	16	0	UFBGA100	87	2	3.6	8	1	1 0	0	1	16	0	0	2 2	0	2	0	2	2	0	4	0	1 0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
SINSPORTINGE 48 CORRES-MO 128 16 0 URSPORT 18	STM32F072V8T6	48		64	16	0	LQFP100	87	2	3.6	8	1	1 0	0	1	16	0	0	2 2	0	2	0	2	2	0	4	0	1 0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STMSZFGSSFGS 48 CARDA 02 4 0 URGFRGAS 22 4 0 URGFRGAS 22 165 135 5 1 1 0 0 1 1 0 0 0 0 0 0 0 0 0 0 0 0	STM32F072VBH6	48		128	16	0	UFBGA100	87	2	3.6	8	1	1 (	0	1	16	0	0	2 2	0	2	0	2	2	0	4	0	1 0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STMS2F088F6F8 48 CAFMA 03 2 4 0 TSSOV20 14 155 155 5 1 1 0 0 1 8 0 0 1 8 0 0 0 0 0 1 0 0 0 0	STM32F072VBT6	48		128	16	0	LQFP100	87	2	3.6	8	1	1 (	0	1	16	0	0	2 2	0	2	0	2	2	0	4	0	1 0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STMS2F088F8F8 68 CAFMA 02 2 4 0 TSSOV20 14 155 155 5 1 1 0 0 1 8 0 0 1 8 0 0 0 0 0 0 0 0 0 0																			STM	32F0	(8低)	]压型	<u>u</u> - 4	3 MHz																						
STMS2F03866816 49 CAREM 32 4 0 UFGFFM28 22 1.68 1.85 1.85 1.8 1 1 0 0 1 1 0 0 0 0 0 0 1 0 0 0 0 0 0	STM32F038F6P6	48		32	4	0	TSS0P20	14	1.65	1.95	5	1	1 0	0	1	8	0	0								1	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F0380800 48 Cortex-Mo 32 4 0 UF0FPN32 26 1.85 1.95 5 1 1 0 0 1 10 0 0 0 0 0 1 0 1 1 0 0 0 0	STM32F038E6Y6	48		32	4	0	WLCSP25	19	1.65	1.95	5	1	1 0	0	1	9	0	0	0 0	0	1	0	1	1	0	1	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F036N30F0 48 Cortice-MO 32 4 0 UFOFPN28 24 1.65 1.95 5 1 1 0 0 1 10 0 0 0 0 0 0 1 0 1 0 1 0	STM32F038G6U6	48		32	4	0	UFQFPN28	22	1.65	1.95	5	1	1 0	0	1	9	0	0	0 0	0	1	0	1	1	0	1	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F048G80F6 48 CARIM OFFINAL PROPERTIES AND STM34F048G80F6 48 CARIM O	STM32F038K6U6	48		32	4	0	UFQFPN32	26	1.65	1.95	5	1	1 0	0	1	10	0	0	0 0	0	1	0	1	1	0	1	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F048C80L6 48 Cortex-MO 32 6 0 UFOFPN48 37 1.65 1.95 5 1 1 1 0 0 1 1 0 0 0 0 0 0 0 1 0 0 1 0	STM32F038C6T6	48		32	4	0	LQFP48	38	1.65	1.95	5	1	1 0	0	1	10	0	0	0 0	0	1	0	1	1	0	1	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F048C806 48 Cortex-MO 32 6 0 UFGFPN48 37 1.65 1.95 5 1 1 1 0 0 1 1 10 0 0 0 1 2 0 1 1 0 0 0 0	STM32F048G6U6	48	ARM	32	6	0	UFQFPN28	24	1.65	1.95	5	1	1 0	0	1	10	0	0	0 0	0	1	0	1	1	0	2	0	0 0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F058R8H6 48 Cortex-MO 64 8 0 UFGFPN48 36 1.65 1.95 7 1 1 0 0 1 10 0 0 1 10 0 0 1 2 0 1 1 0 0 0 0	STM32F048T6Y6	48		32	6	0	WLCSP36	29	1.65	1.95	5	1	1 0	0	1	10	0	0	0 0	0	1	0	1	1	0	2	0	0 0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F058C8U6 48 Cortex-M0 64 8 0 UFQFPN48 38 1.65 1.95 7 1 1 1 0 0 1 10 0 0 1 2 0 1 0 1 2 0 2 0	STM32F048C6U6	48	ARM	32	6	0	UFQFPN48	37	1.65	1.95	5	1	1 (	0	1	10	0	0	0 0	0	2	0	1	1	0	2	0	0 0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F058R8H6 48 Cortex-M0 64 8 0 UFBGA64 54 1.65 1.95 7 1 1 1 0 0 1 16 0 0 1 2 0 2 0 1 2 0 2 0 0 0 0 0 0 0 0 0	STM32F058T8Y6	48		64	8	0	WLCSP36	28	1.65	1.95	7	1	1 (	0	1	10	0	0	1 2	0	1	0	1	2	0	2	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F078CBT6 48 Cortex-M0 128 16 0 UFQFPN48 36 1.65 1.95 8 1 1 0 0 1 10 0 0 1 10 0 0 2 2 0 2 0 2 0	STM32F058C8U6	48		64	8	0	UFQFPN48	38	1.65	1.95	7	1	1 (	0	1	10	0	0	1 2	0	2	0	1	2	0	2	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F078CBU6 48 ARM Cortex-M0 128 16 0 UFQFPN48 36 1.65 1.95 8 1 1 0 0 1 10 0 0 2 2 0 2 0 2 0 2 0 2 0	STM32F058R8H6	48		64	8	0	UFBGA64	54	1.65	1.95	7	1	1 0	0	1	16	0	0	1 2	0	2	0	1	2	0	2	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F078CBU6 48 ARM 128 16 0 UFQFPN48 36 1.65 1.95 8 1 1 0 0 1 10 0 0 2 2 0 2 0 2 0 2 0 2 0	STM32F058R8T6	48		64	8	0	LQFP64	54	1.65	1.95	7	1	1 0	0	1	16	0	0	1 2	0	2	0	1	2	0	2	0	0 0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F078CRV6 48 ARM 128 16 0 W/CSP49 36 165 195 8 1 1 0 0 1 10 0 0 2 2 0 2 0 2 0 2 0 2 0	STM32F078CBT6	48		128	16	0	LQFP48	36	1.65	1.95	8	1	1 0	0	1	10	0	0	2 2	0	2	0	2	2	0	4	0	0 0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
	STM32F078CBU6	48		128	16	0	UFQFPN48	36	1.65	1.95	8	1	1 (	0	1	10	0	0	2 2	0	2	0	2	2	0	4	0	0 0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
	STM32F078CBY6	48		128	16	0	WLCSP49	36	1.65	1.95	8	1	1 0	0	1	10	0	0	2 2	0	2	0	2	2	0	4	0	0 0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0

产品型号	主频 (MHz)	<b>内</b> 核	FLASH (KB)	<b>RAM (КВ)</b>	EEPROM (B)	封装	通 用 IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机空制定付器	こう また こうきょう こうかい こうしょう しょう かいき こうしょう しょう しょう しょう しょう しょう しょう しょう しょう しょう	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	SPI	QUADSPI	l <sup>2</sup> S	J <sup>2</sup> C	高速l°C	U(S)ART	低功耗UART	CAN	SDIO	ECMO	NUSB	全速USB OTG	高速USB OTG	以太网	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	AES	DES/TDES	HMAC
STM32F078RBT6	48	ARM Cortex-M0	128	16	0	LQFP64	50	1.65	1.95	8	1	1 (	) 0	1	16	0	0	2	2	0	2	0	2	2	0	4	0	0	0 (	) (	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F078VBH6	48	ARM Cortex-M0	128	16	0	UFBGA100	86	1.65	1.95	8	1	1 (	) (	1	16	0	0	2	2	0	2	0	2	2	0	4	0	0	0 (	) (	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F078VBT6	48	ARM Cortex-M0	128	16	0	LQFP100	86	1.65	1.95	8	1	1 (	) (	1	16	0	0	2	2	0	2	0	2	2	0	4	0	0	0 (	) (	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F098CCT6	48	ARM Cortex-M0	256	32	0	LQFP48	37	1.65	1.95	8	1	1 (	) (	1	10	0	0	2	2	0	2	0	2	2	0	6	0	1	0 (	) (	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F098CCU6	48	ARM Cortex-M0	256	32	0	UFQFPN48	37	1.65	1.95	8	1	1 (	) (	1	10	0	0	2	2	0	2	0	2	2	0	6	0	1	0 (	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F098RCH6	48	ARM Cortex-M0	256	32	0	UFBGA64	51	1.65	1.95	8	1	1 (	) (	1	16	0	0	2	2	0	2	0	2	2	0	8	0	1	0 (	) (	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F098RCT6	48	ARM Cortex-M0	256	32	0	LQFP64	51	1.65	1.95	8	1	1 (	) (	1	16	0	0	2	2	0	2	0	2	2	0	8	0	1	0 (	) (	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F098RCY6	48	ARM Cortex-M0	256	32	0	WLCSP64	51	1.65	1.95	8	1	1 (	) (	) 1	16	0	0	2	2	0	2	0	2	2	0	8	0	1	0 (	) (	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 (	0 0	0
STM32F098VCT6	48	ARM Cortex-M0	256	32	0	LQFP100	87	1.65	1.95	8	1	1 (	) 0	1	16	0	0	2	2	0	2	0	2	2	0	8	0	1	0 (	) (	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F098VCH6	48	ARM Cortex-M0	256	32	0	UFBGA100	87	1.65	1.95	8	1	1 (	) (	1	16	0	0	2	2	0	2	0	2	2	0	8	0	1	0 (	0 0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0

# STM32 F1系列 - ARM® Cortex®-M3基础型MCU

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12坦DAC	比较器	放大器	SPI	QUADSPI	I²S	120	画来IPC	II/S\ART	氏为眊IIART	SDIO	FSMC	FMC	从USB	全速USB OTG	高速IISB OTG	MDIOS	段式LCD	TET LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES AES	SHA	HMAC
																		SI	TM32	F100	超值	型 -	24 MI	z																						
STM32F100C4T6	24	ARM Cortex-M3	16	4	0	LQFP48	37	2	3.6	5	0	1 0	0	1	10	0	0	2	0	0	1	0	0 1	(	0 2	2	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F100C6T6	24	ARM Cortex-M3	32	4	0	LQFP48	37	2	3.6	5	0	1 0	0	1	10	0	0	2	0	0	1	0	0 1	1 (	0 2	2	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F100C8T6	24	ARM Cortex-M3	64	8	0	LQFP48	37	2	3.6	6	0	1 0	0	1	10	0	0	2	0	0	2	0	0 2	2 (	0 3	3	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F100CBT6	24	ARM Cortex-M3	128	8	0	LQFP48	37	2	3.6	6	0	1 0	0	1	10	0	0	2	0	0	2	0	0 2	2 (	0 3	3	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F100R4T6	24	ARM Cortex-M3	16	4	0	LQFP64	51	2	3.6	5	0	1 0	0	1	16	0	0	2	0	0	1	0	0 1	(	0 2	2	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F100R4H6	24	ARM Cortex-M3	16	4	0	TFBGA64	51	2	3.6	5	0	1 0	0	1	16	0	0	2	0	0	1	0	0 1	(	0 2	2	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F100R6T6	24	ARM Cortex-M3	32	4	0	LQFP64	51	2	3.6	5	0	1 0	0	1	16	0	0	2	0	0	1	0	0 1	1 (	0 2	2	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F100R6H6	24	ARM Cortex-M3	32	4	0	TFBGA64	51	2	3.6	5	0	1 0	0	1	16	0	0	2	0	0	1	0	0 1	(	0 2	2	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F100R8T6	24	ARM Cortex-M3	64	8	0	LQFP64	51	2	3.6	6	0	1 0	0	1	16	0	0	2	0	0	2	0	0 2	2 (	0 3	3	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F100R8H6	24	ARM Cortex-M3	64	8	0	TFBGA64	51	2	3.6	6	0	1 0	0	1	16	0	0	2	0	0	2	0	0 2	2 (	0 3	3	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F100RBT6	24	ARM Cortex-M3	128	8	0	LQFP64	51	2	3.6	6	0	1 0	0	1	16	0	0	2	0	0	2	0	0 2	2 (	0 3	3	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F100RBH6	24	ARM Cortex-M3	128	8	0	TFBGA64	51	2	3.6	6	0	1 0	0	1	16	0	0	2	0	0	2	0	0 2	2 (	0 3	3	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F100V8T6	24	ARM Cortex-M3	64	8	0	LQFP100	80	2	3.6	6	0	1 0	0	1	16	0	0	2	0	0	2	0	0 2	2 (	0 3	3	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F100VBT6	24	ARM Cortex-M3	128	8	0	LQFP100	80	2	3.6	6	0	1 0	0	1	16	0	0	2	0	0	2	0	0 2	2 (	0 3	3	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F100RCT6	24	ARM Cortex-M3	256	24	0	LQFP64	51	2	3.6	11	0	1 0	0	1	16	0	0	2	0	0	3	0	0 2	2 (	0 3+	+2	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	) 0	0
STM32F100RDT6	24	ARM Cortex-M3	384	32	0	LQFP64	51	2	3.6	11	0	1 0	0	1	16	0	0	2	0	0	3	0	0 2	2 (	0 3+	-2	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	) 0	0
STM32F100RET6	24	ARM Cortex-M3	512	32	0	LQFP64	51	2	3.6	11	0	1 0	0	1	16	0	0	2	0	0	3	0	0 2	2 (	0 3+	+2	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F100VCT6	24	ARM Cortex-M3	256	24	0	LQFP100	80	2	3.6	11	0	1 0	0	1	16	0	0	2	0	0	3	0	0 2	2 (	0 3+	+2	0 0	0	1	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F100VDT6	24	ARM Cortex-M3	384	32	0	LQFP100	80	2	3.6	11	0	1 0	0	1	16	0	0	2	0	0	3	0	0 2	2 (	0 3+	-2	0 0	0	1	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F100VET6	24	ARM Cortex-M3	512	32	0	LQFP100	80	2	3.6	11	0	1 0	0	1	16	0	0	2	0	0	3	0	0 2	2 (	0 3+	+2	0 0	0	1	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F100ZCT6	24	ARM Cortex-M3	256	24	0	LQFP144	112	2	3.6	11	0	1 0	0	1	16	0	0	2	0	0	3	0	0 2	2 (	0 3+	+2	0 0	0	1	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0

#### STM32 F1系列 – ARM® Cortex®-M3基础型MCU

产品型号	主频 (MHz)	内 核 ARM	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用io	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	氏力毛官寸器 一	高分等区 Edge	IZ位ADC通道	16位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	SPI	QUADSPI	l <sup>2</sup> S	l°C	高速元C	U(S)ART	低功耗UART	SDIO	FSMC	FMC	从USB		高東IISB OTG	以 MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI			AES	DES/TDES	
STM32F100ZDT6	24	Cortex-M3	384	32	0	LQFP144	112	2	3.6	11	0	1	) (	1	16	0	0	2	0	0	3	0	0	2	0 3	+2	0	0 0	1	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F100ZET6	24	ARM Cortex-M3	512	32	0	LQFP144	112	2	3.6	11	0	1	) (	1	16	0	0	2	0	0	3	0	0	2	0 3-	+2	0	0 0	1	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
																			STM	32F1	01入	门型	- 36	MHz																						
STM32F101T4U6	36	ARM Cortex-M3	16	4	0	VFQFPN36	26	2	3.6	2	0	0	) (	1	10	0	0	0	0	0	1	0	0	1	0	2	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101T6U6	36	ARM Cortex-M3	32	6	0	VFQFPN36	26	2	3.6	2	0	0	0 0	1	10	0	0	0	0	0	1	0	0	1	0	2	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101C6T6	36	ARM Cortex-M3	32	6	0	LQFP48	37	2	3.6	2	0	0	) (	1	10	0	0	0	0	0	1	0	0	1	0	2	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101R4T6	36	ARM Cortex-M3	16	4	0	LQFP64	51	2	3.6	2	0	0	0 0	1	10	0	0	0	0	0	1	0	0	1	0	2	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101R6T6	36	ARM Cortex-M3	32	6	0	LQFP64	51	2	3.6	2	0	0	0 0	1	10	0	0	0	0	0	1	0	0	1	0	2	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101T8U6	36	ARM Cortex-M3	64	10	0	VFQFPN36	26	2	3.6	3	0	0	0 0	1	10	0	0	0	0	0	1	0	0	1	0	2	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101TBU6	36	ARM Cortex-M3	128	16	0	VFQFPN36	26	2	3.6	3	0	0	0 0	1	10	0	0	0	0	0	1	0	0	1	0	2	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101C8T6	36	ARM Cortex-M3	64	10	0	LQFP48	37	2	3.6	3	0	0	) (	1	10	0	0	0	0	0	2	0	0	2	0	3	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101CBT6	36	ARM Cortex-M3	128	16	0	LQFP48	37	2	3.6	3	0	0	) (	1	10	0	0	0	0	0	2	0	0	2	0	3	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101C8U6	36	ARM Cortex-M3	64	10	0	UFQFPN48	37	2	3.6	3	0	0	0 0	1	10	0	0	0	0	0	2	0	0	2	0	3	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101CBU6	36	ARM Cortex-M3	128	16	0	UFQFPN48	37	2	3.6	3	0	0	0 0	1	10	0	0	0	0	0	2	0	0	2	0	3	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101R8T6	36	ARM Cortex-M3	64	10	0	LQFP64	51	2	3.6	3	0	0	0 0	) 1	16	0	0	0	0	0	2	0	0	2	0	3	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101RBT6	36	ARM Cortex-M3	128	16	0	LQFP64	51	2	3.6	3	0	0	0 0	) 1	10	0	0	0	0	0	2	0	0	2	0	3	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101V8T6	36	ARM Cortex-M3	64	10	0	LQFP100	80	2	3.6	3	0	0	0 0	) 1	16	0	0	0	0	0	2	0	0	2	0	3	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101VBT6	36	ARM Cortex-M3	128	16	0	LQFP100	80	2	3.6	3	0	0	) (	1	16	0	0	0	0	0	2	0	0	2	0	3	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101RCT6	36	ARM Cortex-M3	256	32	0	LQFP64	51	2	3.6	6	0	0	) (	) 1	16	0	0	2	0	0	3	0	0	2	0 3-	+2	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101RDT6	36	ARM Cortex-M3	384	48	0	LQFP64	51	2	3.6	6	0	0	0 0	) 1	16	0	0	2	0	0	3	0	0	2	0 3	+2	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101RET6	36	ARM Cortex-M3	512	48	0	LQFP64	51	2	3.6	6	0	0	0 0	) 1	10	0	0	2	0	0	3	0	0	2	0 3	+2	0	0 0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0
STM32F101VCT6	36	ARM Cortex-M3	256	32	0	LQFP100	80	2	3.6	6	0	0	0 0	) 1	10	0	0	2	0	0	3	0	0	2	0 3	+2	0	0 0	1	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	0

#### STM32 F1系列 – ARM® Cortex®-M3基础型MCU

		-																																												
产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	低力毛定寸器 高分熟率 5 时 暑	5分辞区配付第一元	12位ADC通道	16位ADC转换单元	16位ADC通道	12包DAC	比较器	放大器	SPI	QUADSPI	rs 5	120	U(S)AKI	低功耗UART	CAN	SDIO	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES  AES	SHA	НМАС
STM32F101VDT6	36	ARM Cortex-M3	384	48	0	LQFP100	80	2	3.6	6	0	0	0 (	1	16	0	0	2	0	0	3	0	0 2	2 (	) 3+	2 0	0	0	1	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F101VET6	36	ARM Cortex-M3	512	48	0	LQFP100	80	2	3.6	6	0	0	0 (	1	16	0	0	2	0	0	3	0	0 2	2 (	3+	2 0	0	0	1	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F101ZCT6	36	ARM Cortex-M3	256	32	0	LQFP144	112	2	3.6	6	0	0	0 (	1	16	0	0	2	0	0	3	0	0 2	2 (	) 3+	2 0	0	0	1	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F101ZDT6	36	ARM Cortex-M3	384	48	0	LQFP144	112	2	3.6	6	0	0	0 (	1	16	0	0	2	0	0	3	0	0 2	2 (	) 3+	2 0	0	0	1	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F101ZET6	36	ARM Cortex-M3	512	48	0	LQFP144	112	2	3.6	6	0	0	0 (	1	16	0	0	2	0	0	3	0	0 2	2 (	) 3+	2 0	0	0	1	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F101RFT6	36	ARM Cortex-M3	768	80	0	LQFP64	51	2	3.6	12	0	0	0 (	1	16	0	0	2	0	0	3	0	0 2	2 (	) 3+	2 0	0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F101RGT6	36	ARM Cortex-M3	1024	80	0	LQFP64	51	2	3.6	12	0	0	0 (	) 1	16	0	0	2	0	0	3	0	0 2	2 (	) 3+	2 0	0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F101VFT6	36	ARM Cortex-M3	768	80	0	LQFP100	80	2	3.6	12	0	0	0 (	1	16	0	0	2	0	0	3	0	0 2	2 (	3+	2 0	0	0	1	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F101VGT6	36	ARM Cortex-M3	1024	80	0	LQFP100	80	2	3.6	12	0	0	0 0	1	16	0	0	2	0	0	3	0	0 2	2 (	) 3+	2 0	0	0	1	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F101ZFT6	36	ARM Cortex-M3	768	80	0	LQFP144	112	2	3.6	12	0	0	0 (	1	16	0	0	2	0	0	3	0	0 2	2 (	) 3+	2 0	0	0	1	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F101ZGT6	36	ARM Cortex-M3	1024	80	0	LQFP144	112	2	3.6	12	0	0	0 (	1	16	0	0	2	0	0	3	0	0 2	2 (	) 3+	2 0	0	0	1	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
		COLLOX IIIC																	STM	32F1	02 US	SB型	- 48 [	ЛHz																						
STM32F102C4T6	48	ARM Cortex-M3	16	4	0	LQFP48	37	2	3.6	2	0	0	0 0	1	10	0	0	0	0	0			0		) 2	0	0	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F102C6T6	48	ARM Cortex-M3	32	6	0	LQFP48	37	2	3.6	2	0	0	0 (	1	10	0	0	0	0	0	1	0	0 -	1 (	) 2	0	0	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F102R4T6	48	ARM Cortex-M3	16	4	0	LQFP64	51	2	3.6	2	0	0	0 (	1	16	0	0	0	0	0	1	0	0	1 (	) 2	0	0	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F102R6T6	48	ARM Cortex-M3	32	6	0	LQFP64	51	2	3.6	2	0	0	0 (	1	16	0	0	0	0	0	1	0	0	1 (	) 2	0	0	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F102C8T6	48	ARM Cortex-M3	64	10	0	LQFP48	37	2	3.6	3	0	0	0 (	1	10	0	0	0	0	0	2	0	0 2	2 (	) 3	0	0	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F102CBT6	48	ARM Cortex-M3	128	16	0	LQFP48	37	2	3.6	3	0	0	0 (	1	10	0	0	0	0	0	2	0	0 2	2 (	) 3	0	0	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F102R8T6	48	ARM Cortex-M3	64	10	0	LQFP64	51	2	3.6	3	0	0	0 (	1	16	0	0	0	0	0	2	0	0 2	2 (	) 3	0	0	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F102RBT6	48	ARM Cortex-M3	128	16	0	LQFP64	51	2	3.6	3	0	0	0 (	1	16	0	0	0	0	0	2	0	0 2	2 (	) 3	0	0	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
																			STM3	2F10	3性創	[型 .	-72MH	z																						
STM32F103T4U6	72	ARM Cortex-M3	16	6	0	VFQFPN36	26	2	3.6	3	0	1	0 0	2	10	0	0	0	0	0	1	0	0	1 (	) 2	0	1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F103T6U6	72	ARM Cortex-M3	32	10	0	VFQFPN36	26	2	3.6	3	0	1	0 (	2	10	0	0	0	0	0	1	0	0	1 (	) 2	0	1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
	_					-	_			_	_		_	_	_				_			_	_	_	_	_		_	_	_		_				_		_	_		_	_	_			_

# STM32 F1系列 - ARM® Cortex®-M3基础型MCU

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12立DAC 比较器	放大器	SPI	QUADSPI	l²S	l²C	高速元C	U(S)ART	低功耗UART	SDIO	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES AES	SHA	HMAC
STM32F103C4T6	72	ARM Cortex-M3	16	6	0	LQFP48	37	2	3.6	3	0	1 0	0	2	10	0	0	0 0	0	1	0	0	1	0	2	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103C4U6	72	ARM Cortex-M3	16	6	0	UFQFPN48	37	2	3.6	3	0	1 0	0	2	10	0	0	0 0	0	1	0	0	1	0	2	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103C6U6	72	ARM Cortex-M3	32	10	0	UFQFPN48	37	2	3.6	3	0	1 0	0	2	10	0	0	0 0	0	1	0	0	1	0	2	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103C6T6	72	ARM Cortex-M3	32	10	0	LQFP48	37	2	3.6	3	0	1 0	0	2	10	0	0	0 0	0	1	0	0	1	0	2	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103R4H6	72	ARM Cortex-M3	16	6	0	TFBGA64	51	2	3.6	3	0	1 0	0	2	16	0	0	0 0	0	1	0	0	1	0	2	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103R4T6	72	ARM Cortex-M3	16	6	0	LQFP64	51	2	3.6	3	0	1 0	0	2	16	0	0	0 0	0	1	0	0	1	0	2	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103R6H6	72	ARM Cortex-M3	32	10	0	TFBGA64	51	2	3.6	3	0	1 0	0	2	16	0	0	0 0	0	1	0	0	1	0	2	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103R6T6	72	ARM Cortex-M3	32	10	0	LQFP64	51	2	3.6	3	0	1 0	0	2	16	0	0	0 0	0	1	0	0	1	0	2	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103T8U6	72	ARM Cortex-M3	64	20	0	VFQFPN36	26	2	3.6	4	0	1 0	0	2	10	0	0 (	0 0	0	1	0	0	1	0	2	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103TBU6	72	ARM Cortex-M3	128	20	0	VFQFPN36	26	2	3.6	4	0	1 0	0	2	10	0	0	0 0	0	1	0	0	1	0	2	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103C8T6	72	ARM Cortex-M3	64	20	0	LQFP48	37	2	3.6	4	0	1 0	0	2	10	0	0	0 0	0	2	0	0	2	0	3	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103C8U6	72	ARM Cortex-M3	64	20	0	UFQFPN48	37	2	3.6	4	0	1 0	0	2	10	0	0	0 0	0	2	0	0	2	0	3	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103CBT6	72	ARM Cortex-M3	128	20	0	LQFP48	37	2	3.6	4	0	1 0	0	2	10	0	0	0 0	0	2	0	0	2	0	3	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103CBU6	72	ARM Cortex-M3	128	20	0	UFQFPN48	37	2	3.6	4	0	1 0	0	2	10	0	0	0 0	0	2	0	0	2	0	3	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103R8T6	72	ARM Cortex-M3	64	20	0	LQFP64	51	2	3.6	4	0	1 0	0	2	16	0	0	0 0	0	2	0	0	2	0	3	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103R8H6	72	ARM Cortex-M3	64	20	0	TFBGA64	51	2	3.6	4	0	1 0	0	2	16	0	0 (	0 0	0	2	0	0	2	0	3	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103RBT6	72	ARM Cortex-M3	128	20	0	LQFP64	51	2	3.6	4	0	1 0	0	2	16	0	0 (	0 0	0	2	0	0	2	0	3	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103RBH6	72	ARM Cortex-M3	128	20	0	TFBGA64	51	2	3.6	4	0	1 0	0	2	16	0	0	0 0	0	2	0	0	2	0	3	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103V8T6	72	ARM Cortex-M3	64	20	0	LQFP100	80	2	3.6	4	0	1 0	0	2	16	0	0	0 0	0	2	0	0	2	0	3	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103V8H6	72	ARM Cortex-M3	64	20	0	LFBGA100	80	2	3.6	4	0	1 0	0	2	16	0	0	0 0	0	2	0	0	2	0	3	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103V8I6	72	ARM Cortex-M3	64	20	0	UFBGA100	80	2	3.6	4	0	1 0	0	2	16	0	0	0 0	0	2	0	0	2	0	3	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103VBT6	72	ARM Cortex-M3	128	20	0	LQFP100	80	2	3.6	4	0	1 0	0	2	16	0	0	0 0	0	2	0	0	2	0	3	0 1	0	0	0	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0 0	0	0

# STM32 F1系列 - ARM® Cortex®-M3基础型MCU

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通 用 IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12立DAC 比较器	放大器	SPI	QUADSPI	I <sup>2</sup> S	I <sup>2</sup> C	高速l°C	U(S)ART	低功耗UART	CAN	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	以太网 MDIOS	段式LCD	TFI LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES AES	SHA SHA	HMAC
STM32F103VBH6	72	ARM Cortex-M3	128	20	0	LFBGA100	80	2	3.6	4	0	1 0	0	2	16	0	0 (	0 0	0	2	0	0	2	0	3	0	1 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103VBI6	72	ARM Cortex-M3	128	20	0	UFBGA100	80	2	3.6	4	0	1 0	0	2	16	0	0	0 0	0	2	0	0	2	0	3	0	1 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103RCT6	72	ARM Cortex-M3	256	48	0	LQFP64	51	2	3.6	8	0	2 0	0	3	16	0	0 :	2 0	0	3	0	2	2	0	3+2	0	1 1	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103RDT6	72	ARM Cortex-M3	384	64	0	LQFP64	51	2	3.6	8	0	2 0	0	3	16	0	0 :	2 0	0	3	0	2	2	0	3+2	0	1 1	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103RET6	72	ARM Cortex-M3	512	64	0	LQFP64	51	2	3.6	8	0	2 0	0	3	16	0	0	2 0	0	3	0	2	2	0	3+2	0	1 1	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103RCY6	72	ARM Cortex-M3	256	48	0	WLCSP64	51	2	3.6	8	0	2 0	0	3	16	0	0 :	2 0	0	3	0	2	2	0	3+2	0	1 1	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103RDY6	72	ARM Cortex-M3	384	64	0	WLCSP64	51	2	3.6	8	0	2 0	0	3	16	0	0	2 0	0	3	0	2	2	0	3+2	0	1 1	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103REY6	72	ARM Cortex-M3	512	64	0	WLCSP64	51	2	3.6	8	0	2 0	0	3	16	0	0	2 0	0	3	0	2	2	0	3+2	0	1 1	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103VCT6	72	ARM Cortex-M3	256	48	0	LQFP100	80	2	3.6	8	0	2 0	0	3	16	0	0 :	2 0	0	3	0	2	2	0	3+2	0	1 1	1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103VDT6	72	ARM Cortex-M3	384	64	0	LQFP100	80	2	3.6	8	0	2 0	0	3	16	0	0	2 0	0	3	0	2	2	0	3+2	0	1 1	1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103VET6	72	ARM Cortex-M3	512	64	0	LQFP100	80	2	3.6	8	0	2 0	0	3	16	0	0	2 0	0	3	0	2	2	0	3+2	0	1 1	1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103VCH6	72	ARM Cortex-M3	256	48	0	LFBGA100	80	2	3.6	8	0	2 0	0	3	16	0	0	2 0	0	3	0	2	2	0	3+2	0	1 1	1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103VDH6	72	ARM Cortex-M3	384	64	0	LFBGA100	80	2	3.6	8	0	2 0	0	3	16	0	0 :	2 0	0	3	0	2	2	0	3+2	0	1 1	1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103VEH6	72	ARM Cortex-M3	512	64	0	LFBGA100	80	2	3.6	8	0	2 0	0	3	16	0	0 :	2 0	0	3	0	2	2	0	3+2	0	1 1	1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F103ZCT6	72	ARM Cortex-M3	256	48	0	LQFP144	112	2	3.6	8	0	2 0	0	3	21	0	0 :	2 0	0	3	0	2	2	0	3+2	0	1 1	1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103ZDT6	72	ARM Cortex-M3	384	64	0	LQFP144	112	2	3.6	8	0	2 0	0	3	21	0	0 :	2 0	0	3	0	2	2	0	3+2	0	1 1	1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103ZET6	72	ARM Cortex-M3	512	64	0	LQFP144	112	2	3.6	8	0	2 0	0	3	21	0	0 :	2 0	0	3	0	2	2	0	3+2	0	1 1	1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103ZCH6	72	ARM Cortex-M3	256	48	0	LFBGA144	112	2	3.6	8	0	2 0	0	3	21	0	0 :	2 0	0	3	0	2	2	0	3+2	0	1 1	1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103ZDH6	72	ARM Cortex-M3	384	64	0	LFBGA144	112	2	3.6	8	0	2 0	0	3	21	0	0	2 0	0	3	0	2	2	0	3+2	0	1 1	1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103ZEH6	72	ARM Cortex-M3	512	64	0	LFBGA144	112	2	3.6	8	0	2 0	0	3	21	0	0 :	2 0	0	3	0	2	2	0	3+2	0	1 1	1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103RFT6	72	ARM Cortex-M3	768	96	0	LQFP64	51	2	3.6	14	0	2 0	0	3	16	0	0 :	2 0	0	3	0	2	2	0	3+2	0	1 1	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F103RGT6	72	ARM Cortex-M3	1024	96	0	LQFP64	51	2	3.6	14	0	2 0	0	3	16	0	0 :	2 0	0	3	0	2	2	0	3+2	0	1 1	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0

#### STM32 F1系列 – ARM® Cortex®-M3基础型MCU

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	低功耗定时器	高分辨率定时器	12立ADC转换单元	12位ADC通道		16立ADC預查	比较器	放大器	SPI	QUADSPI	ľŝ	l²C	高速r°C	U(S)ART	低功耗UART	CAN	SDIO	HNIC	从USB	全速USB OTG	高速USB OTG	以太网	MDIOS	段式LCD	TFI LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES	SHA	HMAC
STM32F103VFT6	72	ARM Cortex-M3	768	96	0	LQFP100	80	2	3.6	14	0	2	0	0	3	16 0	) (	0 2	0	0	3	0	2	2	0	3+2	0	1	1 1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103VGT6	72	ARM Cortex-M3	1024	96	0	LQFP100	80	2	3.6	14	0	2	0	0	3	16 0	)	0 2	0	0	3	0	2	2	0	3+2	0	1	1 1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103ZFT6	72	ARM Cortex-M3	768	96	0	LQFP144	112	2	3.6	14	0	2	0	0	3 2	21 (	)	0 2	0	0	3	0	2	2	0	3+2	0	1	1 1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103ZGT6	72	ARM Cortex-M3	1024	96	0	LQFP144	112	2	3.6	14	0	2	0	0	3 2	21 (	)	0 2	0	0	3	0	2	2	0	3+2	0	1	1 1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103ZFH6	72	ARM Cortex-M3	768	96	0	LFBGA144	112	2	3.6	14	0	2	0	0	3 2	21 (	)	0 2	0	0	3	0	2	2	0	3+2	0	1	1 1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F103ZGH6	72	ARM Cortex-M3	1024	96	0	LFBGA144	112	2	3.6	14	0	2	0	0	3 2	21 (	)	0 2	0	0	3	0	2	2	0	3+2	0	1	1 1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
																		ST	M32F	105 /	107	连接	型 - 7	2 MI	z																						
STM32F105R8T6	72	ARM Cortex-M3	64	64	0	LQFP64	51	2	3.6	7	0	1	0	0	2	16 0		0 2	0	0	3	0	2	2	0	3+2	0	2	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F105RBT6	72	ARM Cortex-M3	128	64	0	LQFP64	51	2	3.6	7	0	1	0	0	2	16 0		0 2	0	0	3	0	2	2	0	3+2	0	2	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F105RCT6	72	ARM Cortex-M3	256	64	0	LQFP64	51	2	3.6	7	0	1	0	0	2	16 0	) (	0 2	0	0	3	0	2	2	0	3+2	0	2	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F105V8T6	72	ARM Cortex-M3	64	64	0	LQFP100	80	2	3.6	7	0	1	0	0	2	16 0		0 2	0	0	3	0	2	2	0	3+2	0	2	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F105VBH6	72	ARM Cortex-M3	128	64	0	LFBGA100	80	2	3.6	7	0	1	0	0	2	16 0	)	0 2	0	0	3	0	2	2	0	3+2	0	2	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F105VBT6	72	ARM Cortex-M3	128	64	0	LQFP100	80	2	3.6	7	0	1	0	0	2	16 (	)	0 2	0	0	3	0	2	2	0	3+2	0	2	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F105VCT6	72	ARM Cortex-M3	256	64	0	LQFP100	80	2	3.6	7	0	1	0	0	2	16 (		0 2	0	0	3	0	2	2	0	3+2	0	2	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F107RBT6	72	ARM Cortex-M3	128	64	0	LQFP64	51	2	3.6	7	0	1	0	0	2	16 (	)	0 2	0	0	3	0	2	1	0	3+2	0	2	0 0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F107RCT6	72	ARM Cortex-M3	256	64	0	LQFP64	51	2	3.6	7	0	1	0	0	2	16 (		0 2	0	0	3	0	2	1	0	3+2	0	2	0 0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F107VBT6	72	ARM Cortex-M3	128	64	0	LQFP100	80	2	3.6	7	0	1	0	0	2	16 (	) (	0 2	0	0	3	0	2	1	0	3+2	0	2	0 0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F107VCT6	72	ARM Cortex-M3	256	64	0	LQFP100	80	2	3.6	7	0	1	0	0	2	16 (		0 2	0	0	3	0	2	1	0	3+2	0	2	0 0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F107VCH6	72	ARM Cortex-M3	256	64	0	LFBGA100	80	2	3.6	7	0	1	0	0	2	16 (	)	0 2	0	0	3	0	2	1	0	3+2	0	2	0 0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0 0	0	0

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产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	低功耗定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	QUADSPI	S	ľc	高速I°C	U(S)ART	低功耗UART	CAN	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	以 MDIOS	段式LCD	TET LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	AES	DES/TDES	HMAC
																		ST	M32	F301	\门型	텔 - 72	2 MHz																						
STM32F301K6U6	72	ARM Cortex-M4	32	16	0	UFQFPN32	2 24	2	3.6	5	1 1	0	0	1	8	0	0	1	2	1 2	2 0	2	3	[3]	2	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F301K8U6	72	ARM Cortex-M4	64	16	0	UFQFPN32	24	2	3.6	5	1 1	0	0	1	8	0	0	1	2	1 2	2 0	2	3	[3]	2	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F301C6T6	72	ARM Cortex-M4	32	16	0	LQFP48	37	2	3.6	5	1 1	0	0	1	11	0	0	1	3	1 2	2 0	2	3	[3]	3	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F301C6Y6	72	ARM Cortex-M4	32	16	0	WLCSP49	37	2	3.6	5	1 1	0	0	1	11	0	0	1	3	1 2	2 0	2	3	[3]	3	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F301C8T6	72	ARM Cortex-M4	64	16	0	LQFP48	37	2	3.6	5	1 .	0	0	1	11	0	0	1	3	1 2	2 0	2	3	[3]	3	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F301C8Y6	72	ARM Cortex-M4	64	16	0	WLCSP49	37	2	3.6	5	1 .	0	0	1	11	0	0	1	3	1 2	2 0	2	3	[3]	3	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F301R6T6	72	ARM Cortex-M4	32	16	0	LQFP64	51	2	3.6	5	1 .	0	0	1	15	0	0	1	3	1 2	2 0	2	3	[3]	3	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F301R8T6	72	ARM Cortex-M4	64	16	0	LQFP64	51	2	3.6	5	1 -	0	0	1	15	0	0	1	3	1 2	2 0	2	3	[3]	3	0	0 (	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
																			STM	32F30	2 USE	型 -	72 M	z																					
STM32F302K6U6	72	ARM Cortex-M4	32	16	0	UFQFPN32	2 24	2	3.6	5	1 1	0	0	1	8	0	0	1	2	1 2	2 0	2	3	[3]	2	0	1 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F302K8U6	72	ARM Cortex-M4	64	16	0	UFQFPN32	2 24	2	3.6	5	1 1	0	0	1	8	0	0	1	2	1 2	2 0	2	3	[3]	2	0	1 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F302C6T6	72	ARM Cortex-M4	32	16	0	LQFP48	37	2	3.6	5	1 1	0	0	1	11	0	0	1	3	1 2	2 0	2	3	[3]	3	0	1 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F302C6Y6	72	ARM Cortex-M4	32	16	0	WLCSP49	37	2	3.6	5	1 .	0	0	1	11	0	0	1	3	1 2	2 0	2	3	[3]	3	0	1 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F302C8T6	72	ARM Cortex-M4	64	16	0	LQFP48	37	2	3.6	5	1	0	0	1	11	0	0	1	3	1 2	2 0	2	3	[3]	3	0	1 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F302C8Y6	72	ARM Cortex-M4	64	16	0	WLCSP49	37	2	3.6	5	1 .	0	0	1	11	0	0	1	3	1 2	2 0	2	3	[3]	3	0	1 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F302R6T6	72	ARM Cortex-M4	32	16	0	LQFP64	51	2	3.6	5	1 .	0	0	1	15	0	0	1	3	1 2	2 0	2	3	[3]	3	0	1 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F302R8T6	72	ARM Cortex-M4	64	16	0	LQFP64	51	2	3.6	5	1 .	0	0	1	15	0	0	1	3	1 2	2 0	2	3	[3]	3	0	1 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32F302CBT6	72	ARM Cortex-M4	128	32	0	LQFP48	37	2	3.6	7	1 1	0	0	2	9	0	0	1	4	2 3	3 0	2	2	[2]	3	0	1 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F302CCT6	72	ARM Cortex-M4	256	40	0	LQFP48	37	2	3.6	7	1 1	0	0	2	9	0	0	1	4	2 3	3 0	2	2	[2]	3	0	1 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F302RBT6	72	ARM Cortex-M4	128	32	0	LQFP64	52	2	3.6	7	1	0	0	2	16	0	0	1	4	2 3	3 0	2	2	[2]	3+2	0	1 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F302RCT6	72	ARM Cortex-M4	256	40	0	LQFP64	52	2	3.6	7	1	0	0	2	16	0	0	1	4	2 3	3 0	2	2	[2]	3+2	0	1 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0

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产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器低功耗定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12立DAC	放大器	SPI	QUADSPI	l²S	l²C	高速l°C	U(S)ART	低功耗UART	CAN	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	以太网	段式LCD	TET LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	AES	DES/TDES	HMAC
STM32F302VBT6	72	ARM Cortex-M4	128	32	0	LQFP100	87	2	3.6	7	1	1 0	0	2	17	0	0	1 4	2	3	0	2	2	[2]	3+2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0 0
STM32F302VCT6	72	ARM Cortex-M4	256	40	0	LQFP100	87	2	3.6	7	1	1 0	0	2	17	0	0	1 4	2	3	0	2	2	[2]	3+2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0 0
STM32F302VCY6	72	ARM Cortex-M4	256	40	0	WLCSP100	77	2	3.6	1	1	1 0	0	2	17	0	0	1 4	2	3	0	2	2	[2]	3+2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F302RDT6	72	ARM Cortex-M4	384	64	0	LQFP64	51	2	3.6	7	1	1 0	0	2	16	0	0	1 4	2	4	0	2	3	[3]	3+2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	) 0
STM32F302RET6	72	ARM Cortex-M4	512	64	0	LQFP64	51	2	3.6	7	1	1 0	0	2	16	0	0	1 4	2	4	0	2	3	[3]	3+2	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	) 0
STM32F302VDT6	72	ARM Cortex-M4	384	64	0	LQFP100	82	2	3.6	7	1	1 0	0	2	17	0	0	1 4	2	4	0	2	3	[3]	3+2	0	1 (	) 1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F302VDH6	72	ARM Cortex-M4	384	64	0	UFBGA100	84	2	3.6	7	1	1 0	0	2	17	0	0	1 4	2	4	0	2	3	[3]	3+2	0	1 0	) 1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F302VDY6	72	ARM Cortex-M4	384	64	0	WLCSP100	77	2	3.6	7	1	1 0	0	2	17	0	0	1 4	2	4	0	2	3	[3]	3+2	0	1 0	) 1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F302VET6	72	ARM Cortex-M4	512	64	0	LQFP100	82	2	3.6	7	1	1 0	0	2	17	0	0	1 4	2	4	0	2	3	[3]	3+2	0	1 0	) 1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F302VEH6	72	ARM Cortex-M4	512	64	0	UFBGA100	84	2	3.6	7	1	1 0	0	2	17	0	0	1 4	2	4	0	2	3	[3]	3+2	0	1 0	) 1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F302VEY6	72	ARM Cortex-M4	512	64	0	WLCSP100	77	2	3.6	7	1	1 0	0	2	17	0	0	1 4	2	4	0	2	3	[3]	3+2	0	1 0	) 1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F302ZDT6	72	ARM Cortex-M4	384	64	0	LQFP144	115	2	3.6	7	1	1 0	0	2	18	0	0	1 4	2	4	0	2	3	[3]	3+2	0	1 (	) 1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F302ZET6	72	ARM Cortex-M4	512	64	0	LQFP144	115	2	3.6	7	1	1 0	0	2	18	0	0	1 4	2	4	0	2	3	[3]	3+2	0	1 (	) 1	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
																		STN	132F3	803性	能型	- 72	MHz																						
STM32F303K6T6	72	ARM Cortex-M4	32	16	0	LQFP32	25	2	3.6	7	1	1 0	0	2	9	0	0	3 2	1	1	0	0	1	[1]	2	0	1 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F303K8T6	72	ARM Cortex-M4	64	16	0	LQFP32	25	2	3.6	7	1	1 0	0	2	9	0	0	3 2	1	1	0	0	1	[1]	2	0	1 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F303C6T6	72	ARM Cortex-M4	32	16	0	LQFP48	37	2	3.6	7	1	1 0	0	2	15	0	0	3 3	1	1	0	0	1	[1]	3	0	1 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F303C8T6	72	ARM Cortex-M4	64	16	0	LQFP48	37	2	3.6	7	1	1 0	0	2	15	0	0	3 3	1	1	0	0	1	[1]	3	0	1 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F303R6T6	72	ARM Cortex-M4	32	16	0	LQFP64	51	2	3.6	7	1	1 0	0	2	21	0	0	3 3	1	1	0	0	1	[1]	3	0	1 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F303R8T6	72	ARM Cortex-M4	64	16	0	LQFP64	51	2	3.6	7	1	1 0	0	2	21	0	0	3 3	1	1	0	0	1	[1]	3	0	1 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F303CBT6	72	ARM Cortex-M4	128	40	0	LQFP48	37	2	3.6	9	1	2 0	0	4	15	0	0	2 7	4	3	0	2	2	[2]	3	0	1 0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F303CCT6	72	ARM Cortex-M4	256	48	0	LQFP48	37	2	3.6	9	1	2 0	0	4	15	0	0	2 7	4	3	0	2	2	[2]	3	0	1 (	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	) 0
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产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器低功耗定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12型DAC	放大器	SPI	QUADSPI	l'S	l²C	高速l°C	U(S)ART	低功耗UART	CAN	SDIO	ESMC	从USB	全速USB OTG	高速USB OTG	以太网	MDIOS	没代I CD	TET LCD	DSI HOST	SAI	DFSDM	DCMI	SWPMI	TRNG	AES	DES/TDES	HMAC
STM32F303RBT6	72	ARM Cortex-M4	128	40	0	LQFP64	52	2	3.6	9	1	2 0	0	4	22	0	0	2 7	4	3	0	2	2	[2]	3+2	0	1	0 (	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F303RCT6	72	ARM Cortex-M4	256	48	0	LQFP64	52	2	3.6	9	1	2 0	0	4	22	0	0	2 7	4	3	0	2	2	[2]	3+2	0	1	0 (	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F303VBT6	72	ARM Cortex-M4	128	40	0	LQFP100	87	2	3.6	9	1	2 0	0	4	39	0	0	2 7	4	3	0	2	2	[2]	3+2	0	1	0 (	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F303VBY6	72	ARM Cortex-M4	128	40	0	WLCSP100	77	2	3.6	9	1	2 0	0	4	32	0	0	2 7	4	3	0	2	2	[2]	3+2	0	1	0 (	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F303VCT6	72	ARM Cortex-M4	256	48	0	LQFP100	87	2	3.6	9	1	2 0	0	4	39	0	0	2 7	4	3	0	2	2	[2]	3+2	0	1	0 (	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F303VCY6	72	ARM Cortex-M4	256	48	0	WLCSP100	77	2	3.6	9	1	2 0	0	4	32	0	0	2 7	4	3	0	2	2	[2]	3+2	0	1	0 (	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F303RDT6	72	ARM Cortex-M4	384	80	0	LQFP64	51	2	3.6	9	1	2 0	0	4	22	0	0	2 7	4	4	0	2	3	[3]	3+2	0	1	0 (	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F303RET6	72	ARM Cortex-M4	512	80	0	LQFP64	51	2	3.6	9	1	2 0	0	4	22	0	0	2 7	4	4	0	2	3	[3]	3+2	0	1	0 (	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0 (	0	0 0
STM32F303VDT6	72	ARM Cortex-M4	384	80	0	LQFP100	82	2	3.6	10	1	3 0	0	4	39	0	0	2 7	4	4	0	2	3	[3]	3+2	0	1	0 1	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F303VDH6	72	ARM Cortex-M4	384	80	0	UFBGA100	84	2	3.6	10	1	3 0	0	4	39	0	0	2 7	4	4	0	2	3	[3]	3+2	0	1	0 1	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F303VDY6	72	ARM Cortex-M4	384	80	0	WLCSP100	77	2	3.6	10	1	3 0	0	4	33	0	0	2 7	4	4	0	2	3	[3]	3+2	0	1	0 1	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F303VET6	72	ARM Cortex-M4	512	80	0	LQFP100	82	2	3.6	10	1	3 0	0	4	39	0	0	2 7	4	4	0	2	3	[3]	3+2	0	1	0 1	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F303VEH6	72	ARM Cortex-M4	512	80	0	UFBGA100	84	2	3.6	10	1	3 0	0	4	39	0	0	2 7	4	4	0	2	3	[3]	3+2	0	1	0 1	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F303VEY6	72	ARM Cortex-M4	512	80	0	WLCSP100	77	2	3.6	10	1	3 0	0	4	33	0	0	2 7	4	4	0	2	3	[3]	3+2	0	1	0 1	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F303ZDT6	72	ARM Cortex-M4	384	80	0	LQFP144	115	2	3.6	10	1	3 0	0	4	40	0	0	2 7	4	4	0	2	3	[3]	3+2	0	1	0 1	0	1	0	0	0	0	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F303ZET6	72	ARM Cortex-M4	512	80	0	LQFP144	115	2	3.6	10	1	3 0	0	4	40	0	0	2 7	4	4	0	2	3	[3]	3+2	0	1	0 1	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0 (	0	0 0
																	5	STM3	2F373	精确	测量	型 -	72 MF	z																					
STM32F373C8T6	72	ARM Cortex-M4	64	16	0	LQFP48	36	2	3.6	12	2	0 0	0	1	9	3	8	3 2	0	3	0	0	2	[2]	3	0	1	0 0	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F373CBT6	72	ARM Cortex-M4	128	24	0	LQFP48	36	2	3.6	12	2	0 0	0	1	9	3	8	3 2	0	3	0	0	2	[2]	3	0	1	0 (	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F373CCT6	72	ARM Cortex-M4	256	32	0	LQFP48	36	2	3.6	12	2	0 0	0	1	9	3	8	3 2	0	3	0	0	2	[2]	3	0	1	0 (	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F373R8T6	72	ARM Cortex-M4	64	16	0	LQFP64	52	2	3.6	12	2	0 0	0	1	16	3	8	3 2	0	3	0	0	2	[2]	3	0	1	0 (	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0
STM32F373RBT6	72	ARM Cortex-M4	128	24	0	LQFP64	52	2	3.6	12	2	0 0	0	1	16	3	8	3 2	0	3	0	0	2	[2]	3	0	1	0 (	0	1	0	0	0	)	0	0	0 0	0 0	0 0	0	0	0	0	0	0 0

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电弧空制定时器低功耗定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16过ADC通道	1243DAC	比較器器	女に器	QUADSPI	l²S	I²C	高速l <sup>2</sup> C	U(S)ART	低功耗UART	SDIO CAN	FSMC	FMC	从USB	全速USB OTG	以太网	MDIOS	段式LCD	TET LCD	DSI HOST	SAI	SPDIFRX	DGMI	SWPMI	TRNG	AES	DES/TDES	HMAC
STM32F373RCT6	72	ARM Cortex-M4	256	32	0	LQFP64	52	2	3.6	12	2	0 0	0	1	16	3	8	3	2 (	) 3	0	0	2	[2]	3	0	1 0	0	0	1	0 0	0	0	0	0	0	0	0 (	0	0	0	0	0	0 0
STM32F373V8T6	72	ARM Cortex-M4	64	16	0	LQFP100	84	2	3.6	12	2	0 0	0	1	16	3	21	3	2 (	) 3	0	0	2	[2]	3	0	1 0	0	0	1	0 0	0	0	0	0	0	0	0 0	0	0	0	0	0	0 0
STM32F373VBT6	72	ARM Cortex-M4	128	24	0	LQFP100	84	2	3.6	12	2	0 0	0	1	16	3	21	3	2 (	) 3	0	0	2	[2]	3	0	1 0	0	0	1	0 0	0	0	0	0	0	0	0 (	0	0	0	0	0	0 0
STM32F373VCT6	72	ARM Cortex-M4	256	32	0	LQFP100	84	2	3.6	12	2	0 0	0	1	16	3	21	3	2 (	) 3	0	0	2	[2]	3	0	1 0	0	0	1	0 0	0	0	0	0	0	0	0 (	0	0	0	0	0	0 0
STM32F373VCH6	72	ARM Cortex-M4	256	32	0	UFBGA100	84	2	3.6	12	2	0 0	0	1	16	3	21	3	2 (	) 3	0	0	2	[2]	3	0	1 0	0	0	1	0 0	0	0	0	0	0	0	0 0	0	0	0	0	0	0 0
																		<b>STM</b> 3	2F3x	4数字	≥电源	型 -	72 MI	lz																				
STM32F334K4T6	72	ARM Cortex-M4	16	16	0	LQFP32	25	2	3.6	7	1	1 0	1	2	9	0			2 1	1	0	0	1		2	0 -	1 0	0	0	0	0 0	0	0	0	0	0	0	0 0	0	0	0	0	0	0 0
STM32F334K6T6	72	ARM Cortex-M4	32	16	0	LQFP32	25	2	3.6	7	1	1 0	1	2	9	0	0	3	2 1	1	0	0	1	[1]	2	0	1 0	0	0	0	0 0	0	0	0	0	0	0	0 0	0	0	0	0	0	0 0
STM32F334K8T6	72	ARM Cortex-M4	64	16	0	LQFP32	25	2	3.6	7	1	1 0	1	2	9	0	0	3	2 -	1	0	0	1	[1]	2	0	1 0	0	0	0	0 0	0	0	0	0	0	0	0 0	0	0	0	0	0	0 0
STM32F334C4T6	72	ARM Cortex-M4	16	16	0	LQFP48	37	2	3.6	7	1	1 0	1	2	15	0	0	3	3 1	1	0	0	1	[1]	3	0	1 0	0	0	0	0 0	0	0	0	0	0	0	0 0	0	0	0	0	0	0 0
STM32F334C6T6	72	ARM Cortex-M4	32	16	0	LQFP48	37	2	3.6	7	1	1 0	1	2	15	0	0	3	3	1	0	0	1	[1]	3	0	1 0	0	0	0	0 0	0	0	0	0	0	0	0 (	0	0	0	0	0	0 0
STM32F334C8T6	72	ARM Cortex-M4	64	16	0	LQFP48	37	2	3.6	7	1	1 0	1	2	15	0	0	3	3	1	0	0	1	[1]	3	0	1 0	0	0	0	0 0	0	0	0	0	0	0	0 (	0	0	0	0	0	0 0
STM32F334R4T6	72	ARM Cortex-M4	16	16	0	LQFP64	51	2	3.6	7	1	1 0	1	2	21	0	0	3	3	1	0	0	1	[1]	3	0 -	1 0	0	0	0	0 0	0	0	0	0	0	0	0 (	0	0	0	0	0	0 0
STM32F334R6T6	72	ARM Cortex-M4	32	16	0	LQFP64	51	2	3.6	7	1	1 0	1	2	21	0	0	3	3	1	0	0	1	[1]	3	0	1 0	0	0	0	0 0	0	0	0	0	0	0	0 (	0	0	0	0	0	0 0
STM32F334R8T6	72	ARM Cortex-M4	64	16	0	LQFP64	51	2	3.6	7	1	1 0	1	2	21	0	0	3	3	1	0	0	1	[1]	3	0	1 0	0	0	0	0 0	0	0	0	0	0	0	0 (	0	0	0	0	0	0 0
																		ST	M32F	3x8们	5.电压	型 -	72 M	lz																				
STM32F318K8U6	72	ARM Cortex-M4	64	16	0	UFQFPN32	23	1.65	1.95	5	1	1 0	0	1	8	0	0	1	2 1	2	0	2	3	[3]	2	0 (	0 0	0	0	0	0 0	0	0	0	0	0	0	0 0	0	0	0	0	0	0 0
STM32F318C8Y6	72	ARM Cortex-M4	64	16	0	WLCSP49	36	1.65	1.95	5	1	1 0	0	1	11	0	0	1	3 1	2	. 0	2	3	[3]	3	0 (	0 0	0	0	0	0 0	0	0	0	0	0	0	0 0	0	0	0	0	0	0 0
STM32F318C8T6	72	ARM Cortex-M4	64	16	0	LQFP48	36	1.65	1.95	5	1	1 0	0	1	11	0	0	1	3 1	2	. 0	2	3	[3]	3	0 (	0 0	0	0	0	0 0	0	0	0	0	0	0	0 (	0	0	0	0	0	0 0
STM32F328C8T6	72	ARM Cortex-M4	64	16	0	LQFP48	36	1.65	1.95	7	1	1 0	0	2	14	0	0	3	3	1	0	0	1	[1]	3	0	1 0	0	0	0	0 0	0	0	0	0	0	0	0 (	0	0	0	0	0	0 0
STM32F358CCT6	72	ARM Cortex-M4	256	48	0	LQFP48	36	1.65	1.95	9	1	2 0	0	4	14	0	0	2	7 4	1 3	0	2	2	[2]	3+2	0	1 0	0	0	0	0 0	0	0	0	0	0	0	0 (	0	0	0	0	0	0 0
STM32F358RCT6	72	ARM Cortex-M4	256	48	0	LQFP64	51	1.65	1.95	9	1	2 0	0	4	21	0	0	2	7 4	1 3	0	2	2	[2]	3+2	0	1 0	0	0	0	0 0	0	0	0	0	0	0	0 0	0	0	0	0	0	0 0

产品型号	主频 (MHz)	内核	FLASH (KB)	<b>ВАМ (КВ)</b>	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机空制定对器	毛力毛三寸器 高分辨率定时器	LADU 較接	12位ADC通道	Tb位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	SPI	QUADSPI	l²S	l²C	高速l°C	U(S)ART	低功耗UART	CAN	SDIO	HMIC	从USB	全速USB OTG	高速USB OTG	以太网	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DESDIM	DCMI	SWPMI	TRNG	AES I DES	DES/TRES	HMAC
STM32F358VCT6	72	ARM Cortex-M4	256	48	0	LQFP100	86	1.65	1.95	9	1	2 (	0	4	4 38	3 0	0	2	7	4	3	0	2	2	[2]	3+2	0	1 (	0 0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 (	0 0	0	0
STM32F378CCT6	72	ARM Cortex-M4	256	32	0	LQFP48	36	1.65	1.95	12	2	0 (	0	1	1 9	3	21	3	2	0	3	0	0	2	[2]	3	0	1 (	0 0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 (	0 0	) (	0
STM32F378RCT6	72	ARM Cortex-M4	256	32	0	LQFP64	51	1.65	1.95	12	2	0 (	0	1	1 16	3	21	3	2	0	3	0	0	2	[2]	3	0	1 (	0 0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 (	0 0	0 0	0
STM32F378RCY6	72	ARM Cortex-M4	256	32	0	WLCSP66	51	1.65	1.95	12	2	0 (	0	1	1 10	3	21	3	2	0	3	0	0	2	[2]	3	0	1	0 0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 (	0 0	) (	0
STM32F378VCT6	72	ARM Cortex-M4	256	32	0	LQFP100	83	1.65	1.95	12	2	0 (	0	1	1 10	3	21	3	2	0	3	0	3	2	[2]	3	0	1	0 0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 (	0 0	) (	0
STM32F378VCH6	72	ARM Cortex-M4	256	32	0	UFBGA100	83	1.65	1.95	12	2	0 (	0	1	1 10	3	21	3	2	0	3	0	3	2	[2]	3	0	1	0 0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0	) (	0
STM32F398VET6	72	ARM Cortex-M4	512	80	0	LQFP100	85	1.65	1.95	10	1	3 (	0	4	4 38	3 0	0	2	7	4	4	0	2	3	[3]	3+2	0	1	0 1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 (	0 0	0

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	氏力毛定寸器 一	12位ADG转换单元	12位ADC運道	16位ADC转换单元	16位ADC通道	12包DAC	比较器	放大器	SPI	QUADSPI	I'S	-20 X	司東での	II(S)ART	氏功耗IIART	SDIO	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES AES	SHA	HMAC
																		S	TM32	2F2x5	基础	型 -	120 N	Hz																						
STM32F205RBT6	120	ARM Cortex-M3	128	64	0	LQFP64	51	1.8	3.6	12	2	2	0 0	3	16	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	1	0	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F205RCT6	120	ARM Cortex-M3	256	96	0	LQFP64	51	1.8	3.6	12	2	2	0 0	3	16	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	1	0	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F205RET6	120	ARM Cortex-M3	512	128	0	LQFP64	51	1.8	3.6	12	2	2	0 0	3	16	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	! 1	0	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F205REY6	120	ARM Cortex-M3	512	128	0	WLCSP66	51	1.7	3.6	12	2	2	0 0	3	16	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	! 1	0	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F205RFT6	120	ARM Cortex-M3	768	128	0	LQFP64	51	1.8	3.6	12	2	2	0 0	3	16	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	! 1	0	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F205RGT6	120	ARM Cortex-M3	1024	128	0	LQFP64	51	1.8	3.6	12	2	2	0 0	3	16	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	1	0	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F205RGY6	120	ARM Cortex-M3	1024	128	0	WLCSP66	51	1.7	3.6	12	2	2	0 0	3	16	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	2 1	0	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F205VBT6	120	ARM Cortex-M3	128	64	0	LQFP100	82	1.8	3.6	12	2	2	0 0	3	16	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	2 1	1	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F205VCT6	120	ARM Cortex-M3	256	96	0	LQFP100	82	1.8	3.6	12	2	2	0 0	3	16	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	1	1	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F205VET6	120	ARM Cortex-M3	512	128	0	LQFP100	82	1.8	3.6	12	2	2	0 0	3	16	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	1	1	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F205VFT6	120	ARM Cortex-M3	768	128	0	LQFP100	82	1.8	3.6	12	2	2	0 0	3	16	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	1	1	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F205VGT6	120	ARM Cortex-M3	1024	128	0	LQFP100	82	1.8	3.6	12	2	2	0 0	3	16	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	! 1	1	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F205ZCT6	120	ARM Cortex-M3	256	96	0	LQFP144	114	1.8	3.6	12	2	2	0 0	3	24	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	1	1	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F205ZET6	120	ARM Cortex-M3	512	128	0	LQFP144	114	1.8	3.6	12	2	2	0 0	3	24	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	2 1	1	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F205ZFT6	120	ARM Cortex-M3	768	128	0	LQFP144	114	1.8	3.6	12	2	2	0 0	3	24	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	2 1	1	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F205ZGT6	120	ARM Cortex-M3	1024	128	0	LQFP144	114	1.8	3.6	12	2	2	0 0	3	24	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	! 1	1	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F215RET6	120	ARM Cortex-M3	512	128	0	LQFP64	51	1.8	3.6	12	2	2	0 0	3	16	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	1	0	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	1 1	1	0
STM32F215RGT6	120	ARM Cortex-M3	1024	128	0	LQFP64	51	1.8	3.6	12	2	2	0 0	3	16	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	1	0	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	1 1	1	0
STM32F215VET6	120	ARM Cortex-M3	512	128	0	LQFP100	82	1.8	3.6	12	2	2	0 0	3	16	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	2 1	1	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	1 1	1	0
STM32F215VGT6	120	ARM Cortex-M3	1024	128	0	LQFP100	82	1.8	3.6	12	2	2	0 0	3	16	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	1	1	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	1 1	1	0
STM32F215ZET6	120	ARM Cortex-M3	512	128	0	LQFP144	114	1.8	3.6	12	2	2	0 0	3	24	0	0	2	0	0	3	0	2	3	0 4-	+2	0 2	1	1	0	0	1	1 (	0	0	0	0	0	0	0	0	0	1	1 1	1	0

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产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	Ib位ADC通道	12位DAC	比较器	放大器	SPI	QUADSPI	T'S	I <sup>2</sup> C	高速元G	II(S)ART	氏为毛IIART	SDIO	FSMC	FMC	从USB	高速USBOIG	以太网	MDIOS	段式LCD	TFI LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES AES	SHA	HMAC
STM32F215ZGT6	120	ARM Cortex-M3	1024	128	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	2	0	0	3	0	2	3	0 4	+2	0 2	1	1	0	0 1	1	0	0	0	0	0	0	0	0	0	0	1	1 1	1	0
																		STM3	2F2x	7基础	型 -	120 N	ЛHz																						
STM32F207VCT6	120	ARM Cortex-M3	256	128	0	LQFP100	82	1.8	3.6	12	2	2 0	0	3	16	0	2	0	0	3	0	2	3	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F207VET6	120	ARM Cortex-M3	512	128	0	LQFP100	82	1.8	3.6	12	2	2 0	0	3	16	0	2	0	0	3	0	2	3	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F207VFT6	120	ARM Cortex-M3	768	128	0	LQFP100	82	1.8	3.6	12	2	2 0	0	3	16	0	2	0	0	3	0	2	3	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F207VGT6	120	ARM Cortex-M3	1024	128	0	LQFP100	82	1.8	3.6	12	2	2 0	0	3	16	0	2	0	0	3	0	2	3	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F207ZCT6	120	ARM Cortex-M3	256	128	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	2	0	0	3	0	2	3	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F207ZET6	120	ARM Cortex-M3	512	128	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	) 0	2	0	0	3	0	2	3	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F207ZFT6	120	ARM Cortex-M3	768	128	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	) (	2	0	0	3	0	2	3	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F207ZGT6	120	ARM Cortex-M3	1024	128	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	2	0	0	3	0	2	3	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F207ICT6	120	ARM Cortex-M3	256	128	0	LQFP176	140	1.8	3.6	12	2	2 0	0	3	24	0	2	0	0	3	0	2	3	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F207IET6	120	ARM Cortex-M3	512	128	0	LQFP176	140	1.8	3.6	12	2	2 0	0	3	24	0	2	0	0	3	0	2	3	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F207IFT6	120	ARM Cortex-M3	768	128	0	LQFP176	140	1.8	3.6	12	2	2 0	0	3	24	0	2	0	0	3	0	2	3	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F207IGT6	120	ARM Cortex-M3	1024	128	0	LQFP176	140	1.8	3.6	12	2	2 0	0	3	24	0	2	0	0	3	0	2	3	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F207ICH6	120	ARM Cortex-M3	256	128	0	UFBGA176	140	1.8	3.6	12	2	2 0	0	3	24	) 0	2	0	0	3	0	2	3	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F207IEH6	120	ARM Cortex-M3	512	128	0	UFBGA176	140	1.8	3.6	12	2	2 0	0	3	24	) (	2	0	0	3	0	2	3	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F207IFH6	120	ARM Cortex-M3	768	128	0	UFBGA176	140	1.8	3.6	12	2	2 0	0	3	24	0	2	0	0	3	0	2	3	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F207IGH6	120	ARM Cortex-M3	1024	128	0	UFBGA176	140	1.8	3.6	12	2	2 0	0	3	24	0	2	0	0	3	0	2	3	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F217VET6	120	ARM Cortex-M3	512	128	0	LQFP100	82	1.8	3.6	12	2	2 0	0	3	16	0	2	0	0	3	0	2	2	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	1 1	1	0
STM32F217VGT6	120	ARM Cortex-M3	1024	128	0	LQFP100	82	1.8	3.6	12	2	2 0	0	3	16	0	2	0	0	3	0	2	2	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	1 1	1	0
STM32F217ZET6	120	ARM Cortex-M3	512	128	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	2	0	0	3	0	2	2	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	1 1	1	0
STM32F217ZGT6	120	ARM Cortex-M3	1024	128	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	2	0	0	3	0	2	2	0 4	+2	0 2	1	1	0	0 1	1	1	0	0	0	0	0	0	0	1	0	1	1 1	1	0

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	<b>电</b> 几空制定寸器 作功制定时器	<b>毛力能三寸器</b>	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12包DAC	比较器	放大器	SPI	QUADSPI	l²S		高速I°C	U(S)ART	低功耗UART	CAN	SDIO	FMC	从USB	全速USB OTG	高速USB OTG	以太网	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	AES	SHA DES/TDES	HMAC
STM32F217IET6	120	ARM Cortex-M3	512	128	0	LQFP176	140	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0	3	0	2	2	0 4	1+2	0	2	1 1	0	0	1	1	1	0	0	0	0	0	0	0	1	0	1	1	1 1	0
STM32F217IGT6	120	ARM Cortex-M3	1024	128	0	LQFP176	140	1.8	3.6	12	2 2	2 0	0	3	24	0	0	2	0	0	3	0	2	2	0 4	1+2	0	2	1 1	0	0	1	1	1	0	0	0	0	0	0	0	1	0	1	1	1 1	0
STM32F217IEH6	120	ARM Cortex-M3	512	128	0	UFBGA176	140	1.8	3.6	12	2 2	2 0	0	3	24	0	0	2	0	0	3	0	2	2	0 4	1+2	0	2	1 1	0	0	1	1	1	0	0	0	0	0	0	0	1	0	1	1	1 1	0
STM32F217IGH6	120	ARM Cortex-M3	1024	128	0	UFBGA176	140	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0	3	0	2	2	0 4	1+2	0	2	1 1	0	0	1	1	1	0	0	0	0	0	0	0	1	0	1	1	1 1	0

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产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	<b>旭见空制官时器</b> 低功耗定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	QUADSPI	I'S	ľc	高速r°C	U(S)ART	低功耗UART	CAN	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	以 MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	AES	NES/TNES	НМАС
						'	<u> </u>											Sī	M32	F401 <i>)</i>	' \门型	빌 - 84	MHz																						
STM32F401CBU6	84	ARM Cortex-M4	128	64	0	UFQFPN48	36	1.7	3.6	6	2	0	0	1	10	0	0	0	0	0 3	3 0	2	3	0	3	0	0 0	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401CBY6	84	ARM Cortex-M4	128	64	0	WLCSP49	36	1.7	3.6	6	2	1 0	0	1	10	0	0	0	0	0 3	3 0	2	3	0	3	0	0 0	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401RBT6	84	ARM Cortex-M4	128	64	0	LQFP64	50	1.7	3.6	6	2	1 0	0	1	16	0	0	0	0	0 3	3 0	2	3	0	3	0	0 1	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401VBT6	84	ARM Cortex-M4	128	64	0	LQFP100	81	1.7	3.6	6	2	1 0	0	1	16	0	0	0	0	0 4	1 0	2	3	0	3	0	0 1	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401VBH6	84	ARM Cortex-M4	128	64	0	UFBGA100	81	1.7	3.6	6	2	1 0	0	1	16	0	0	0	0	0 4	1 0	2	3	0	3	0	0 1	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401CCU6	84	ARM Cortex-M4	256	64	0	UFQFPN48	36	1.7	3.6	6	2	1 0	0	1	10	0	0	0	0	0 3	3 0	2	3	0	3	0	0 0	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401CCY6	84	ARM Cortex-M4	256	64	0	WLCSP49	36	1.7	3.6	6	2	1 0	0	1	10	0	0	0	0	0 3	3 0	2	3	0	3	0	0 0	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401RCT6	84	ARM Cortex-M4	256	64	0	LQFP64	50	1.7	3.6	6	2	1 0	0	1	16	0	0	0	0	0 3	3 0	2	3	0	3	0	0 1	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401VCT6	84	ARM Cortex-M4	256	64	0	LQFP100	81	1.7	3.6	6	2	0	0	1	16	0	0	0	0	0 4	1 0	2	3	0	3	0	0 1	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401VCH6	84	ARM Cortex-M4	256	64	0	UFBGA100	81	1.7	3.6	6	2	0	0	1	16	0	0	0	0	0 4	1 0	2	3	0	3	0	0 1	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401CDU6	84	ARM Cortex-M4	384	96	0	UFQFPN48	36	1.7	3.6	6	2	1 0	0	1	10	0	0	0	0	0 3	3 0	2	3	0	3	0	0 0	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401CDY6	84	ARM Cortex-M4	384	96	0	WLCSP49	36	1.7	3.6	6	2	1 0	0	1	10	0	0	0	0	0 3	3 0	2	3	0	3	0	0 0	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401RDT6	84	ARM Cortex-M4	384	96	0	LQFP64	50	1.7	3.6	6	2	1 0	0	1	16	0	0	0	0	0 3	3 0	2	3	0	3	0	0 1	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401VDT6	84	ARM Cortex-M4	384	96	0	LQFP100	81	1.7	3.6	6	2	1 0	0	1	16	0	0	0	0	0 4	1 0	2	3	0	3	0	0 1	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401VDH6	84	ARM Cortex-M4	384	96	0	UFBGA100	81	1.7	3.6	6	2	1 0	0	1	16	0	0	0	0	0 4	1 0	2	3	0	3	0	0 1	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401CEU6	84	ARM Cortex-M4	512	96	0	UFQFPN48	36	1.7	3.6	6	2	1 0	0	1	10	0	0	0	0	0 3	3 0	2	3	0	3	0	0 0	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401CEY6	84	ARM Cortex-M4	512	96	0	WLCSP49	36	1.7	3.6	6	2	1 0	0	1	10	0	0	0	0	0 3	3 0	2	3	0	3	0	0 0	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401RET6	84	ARM Cortex-M4	512	96	0	LQFP64	50	1.7	3.6	6	2	1 0	0	1	16	0	0	0	0	0 3	3 0	2	3	0	3	0	0 1	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32F401VET6	84	ARM Cortex-M4	512	96	0	LQFP100	81	1.7	3.6	6	2	1 0	0	1	16	0	0	0	0	0 4	1 0	2	3	0	3	0	0 1	0	0	0	1		0 (	0	0	0	0	0	0	$\dashv$	$\dashv$	_	0 0	0 0	+
STM32F401VEH6	84	ARM Cortex-M4	512	96	0	UFBGA100	81	1.7	3.6	6	2	1 0	0	1	16	0	0			0 4				0	3	0	0 1	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0 0	0 0	0
																		31	VI3ZI	410/	니니쬬	- 10	O MHz																						
STM32F410T8Y6	100	ARM Cortex-M4	64	32	0	WLCSP36	23	1.7	3.6	4	1	1	0	1	4	0	0	1	0	0 1	0	1	2	[1]	2	0	0 0	0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	1	0 0	0 0	0

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产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通 用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	12位DAC	比较器	放大器	SPI	QUADSPI	I's	高速FC	U(S)ART	低功耗UART	CAN	SDIO	FMC	从USB	全速USB OTG	高速USB OTG	以太网	段左LGD	171 170	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES AES	SHA	HMAC
STM32F410TBY6	100	ARM Cortex-M4	128	32	0	WLCSP36	23	1.7	3.6	4	1	1 1	0	1	4	0 (	) 1	0	0	1	0	1 2	[1]	2	0	0	0 0	0	0	0	0	0	0 0	(	0	0	0	0	0	0	1	0 0	0	0
STM32F410C8U6	100	ARM Cortex-M4	64	32	0	UFQFPN48	36	1.7	3.6	4	1	1 1	0	1	10	0 (	) 1	0	0	3	0	3 3	[1]	3	0	0	0 0	0	0	0	0	0	0 0	(	0	0	0	0	0	0	1	0 0	0	0
STM32F410CBU6	100	ARM Cortex-M4	128	32	0	UFQFPN48	36	1.7	3.6	4	1	1 1	0	1	10	0 (	) 1	0	0	3	0	3 3	[1]	3	0	0	0 0	0	0	0	0	0	0 0	(	0	0	0	0	0	0	1	0 0	0	0
STM32F410R8T6	100	ARM Cortex-M4	64	32	0	LQFP64	50	1.7	3.6	4	1	1 1	0	1	16	0 (	) 1	0	0	3	0	3 3	[1]	3	0	0	0 0	0	0	0	0	0	0 (	(	0	0	0	0	0	0	1	0 0	0	0
STM32F410RBT6	100	ARM Cortex-M4	128	32	0	LQFP64	50	1.7	3.6	4	1	1 1	0	1	16	0 (	) 1	0	0	3	0	3 3	[1]	3	0	0	0 0	0	0	0	0	0	0 0	(	0	0	0	0	0	0	1	0 0	0	0
																		STM3	2F41	入ì.	型 -	100 MI	z																					
STM32F411CCU6	100	ARM Cortex-M4	256	128	0	UFQFPN48	36	1.7	3.6	6	2	1 0	0	1	10	0 (	0	0	0	5	0	5 3	0	3	0	0	1 0	0	0	1	0	0	0 0	(	0	0	0	0	0	0	0	0 0	0	0
STM32F411CCY6	100	ARM Cortex-M4	256	128	0	WLCSP49	36	1.7	3.6	6	2	1 0	0	1	10	0 (	0	0	0	5	0	5 3	0	3	0	0	1 0	0	0	1	0	0	0 0	(	0	0	0	0	0	0	0	0 0	0	0
STM32F411CEU6	100	ARM Cortex-M4	512	128	0	UFQFPN48	36	1.7	3.6	6	2	1 0	0	1	10	0 (	0	0	0	5	0	5 3	0	3	0	0	1 0	0	0	1	0	0	0 0	(	0	0	0	0	0	0	0	0 0	0	0
STM32F411CEY6	100	ARM Cortex-M4	512	128	0	WLCSP49	36	1.7	3.6	6	2	1 0	0	1	10	0 (	0	0	0	5	0	5 3	0	3	0	0	1 0	0	0	1	0	0	0 0	(	0	0	0	0	0	0	0	0 0	0	0
STM32F411RCT6	100	ARM Cortex-M4	256	128	0	LQFP64	50	1.7	3.6	6	2	1 0	0	1	16	0 (	0	0	0	5	0	5 3	0	3	0	0	1 0	0	0	1	0	0	0 0	(	0	0	0	0	0	0	0	0 0	0	0
STM32F411RET6	100	ARM Cortex-M4	512	128	0	LQFP64	50	1.7	3.6	6	2	1 0	0	1	16	0 (	0	0	0	5	0	5 3	0	3	0	0	1 0	0	0	1	0	0	0 0	(	0	0	0	0	0	0	0	0 0	0	0
STM32F411VCT6	100	ARM Cortex-M4	256	128	0	LQFP100	81	1.7	3.6	6	2	1 0	0	1	16	0 (	0	0	0	5	0	5 3	0	3	0	0	1 0	0	0	1	0	0	0 0	(	0	0	0	0	0	0	0	0 0	0	0
STM32F411VCH6	100	ARM Cortex-M4	256	128	0	UFBGA100	81	1.7	3.6	6	2	1 0	0	1	16	0 (	0	0	0	5	0	5 3	0	3	0	0	1 0	0	0	1	0	0	0 0	(	0	0	0	0	0	0	0	0 0	0	0
STM32F411VET6	100	ARM Cortex-M4	512	128	0	LQFP100	81	1.7	3.6	6	2	1 0	0	1	16	0 (	0	0	0	5	0	5 3	0	3	0	0	1 0	0	0	1	0	0	0 0	(	0	0	0	0	0	0	0	0 0	0	0
STM32F411VEH6	100	ARM Cortex-M4	512	128	0	UFBGA100	81	1.7	3.6	6	2	1 0	0	1	16	0 (	0	0	0	5	0	5 3	0	3	0	0	1 0	0	0	1	0	0	0 (	(	0	0	0	0	0	0	0	0 0	0	0
																		STM3	2F41	2入门	型 -	100 MI	z																					
STM32F412CEU6	100	ARM Cortex-M4	512	256	0	UFQFPN48	36	1.7	3.6	12	2	2 0	0	1	10	0 (	0	0	0	5	0	5 4	[1]	3	0	2	1 0	0	0	1	0	0	0 0	(	0	0	0	1	0	0	1	0 0	0	0
STM32F412RET6	100	ARM Cortex-M4	512	256	0	LQFP64	50	1.7	3.6	12	2	2 0	0	1	16	0 (	0	0	0	5	1	5 4	[1]	4	0	2	1 0	0	0	1	0	0	0 0	(	0	0	0	1	0	0	1	0 0	0	0
STM32F412REY6	100	ARM Cortex-M4	512	256	0	WLCSP64	50	1.7	3.6	12	2	2 0	0	1	16	0 (	0	0	0	5	1	5 4	[1]	4	0	2	1 0	0	0	1	0	0	0 0	(	0	0	0	1	0	0	1	0 0	0	0
STM32F412VET6	100	ARM Cortex-M4	512	256	0	LQFP100	81	1.7	3.6	12	2	2 0	0	1	16	0 (	0	0	0	5	1	5 4	[1]	4	0	2	1 1	0	0	1	0	0	0 0	(	0	0	0	1	0	0	1	0 0	0	0
STM32F412VEH6	100	ARM Cortex-M4	512	256	0	UFBGA100	81	1.7	3.6	12	2	2 0	0	1	16	0 (	0	0	0	5	1	5 4	[1]	4	0	2	1 1	0	0	1	0	0	0 0	(	0	0	0	1	0	0	1	0 0	0	0
STM32F412ZET6	100	ARM Cortex-M4	512	256	0	LQFP144	114	1.7	3.6	12	2	2 0	0	1	16	0 (	0	0	0	5	1	5 4	[1]	4	0	2	1 1	0	0	1	0	0	0 0	(	0	0	0	1	0	0	1	0 0	0	0
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产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电机空制定付器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	CDI	Ollangai	l'c	高速lfC	U(S)ART	低功耗UART	CAN	SDIO	FSMC	FMC	从USB	高速USB OIG	以太网	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES	SHA	HMAC
STM32F412ZEH6	100	ARM Cortex-M4	512	256	0	UFBGA144	114	1.7	3.6	12	2	2 (	0	1	16	0	0	0	0	0 ;	5 1	1 5	5 4	[1]	4	0	2	1	1	0	0	1 0	0	0	0	0	0	0	0	1	0	0	1 (	0 0	0	0
STM32F412CGU6	100	ARM Cortex-M4	1024	256	0	UFQFPN48	36	1.7	3.6	12	2	2 (	0	1	10	0	0	0	0	0 ;	5 (	0 5	5 4	[1]	3	0	2	1	0	0	0	1 0	0	0	0	0	0	0	0	1	0	0	1 (	0 0	0	0
STM32F412RGT6	100	ARM Cortex-M4	1024	256	0	LQFP64	50	1.7	3.6	12	2	2 (	0	1	16	0	0	0	0	0 !	5 1	1 5	5 4	[1]	4	0	2	1	0	0	0	1 0	0	0	0	0	0	0	0	1	0	0	1 (	0 0	0	0
STM32F412RGY6	100	ARM Cortex-M4	1024	256	0	WLCSP64	50	1.7	3.6	12	2	2 (	0	1	16	0	0	0	0	0 :	5 1	1 5	5 4	[1]	4	0	2	1	0	0	0	1 0	0	0	0	0	0	0	0	1	0	0	1 (	0 0	0	0
STM32F412VGT6	100	ARM Cortex-M4	1024	256	0	LQFP100	81	1.7	3.6	12	2	2 (	0	1	16	0	0	0	0	0 ;	5 1	1 5	5 4	[1]	4	0	2	1	1	0	0	1 0	0	0	0	0	0	0	0	1	0	0	1 1	0 0	0	0
STM32F412VGH6	100	ARM Cortex-M4	1024	256	0	UFBGA100	81	1.7	3.6	12	2	2 (	0	1	16	0	0	0	0	0 !	5 1	1 5	5 4	[1]	4	0	2	1	1	0	0	1 0	0	0	0	0	0	0	0	1	0	0	1 1	0 0	0	0
STM32F412ZGT6	100	ARM Cortex-M4	1024	256	0	LQFP144	114	1.7	3.6	12	2	2 (	0	1	16	0	0	0	0	0 !	5 1	1 5	5 4	[1]	4	0	2	1	1	0	0	1 0	0	0	0	0	0	0	0	1	0	0	1 (	0 0	0	0
STM32F412ZGH6	100	ARM Cortex-M4	1024	256	0	UFBGA144	114	1.7	3.6	12	2	2 (	0	1	16	0	0	0	0	0	5 1	1 5	5 4	[1]	4	0	2	1	1	0	0	1 0	0	0	0	0	0	0	0	1	0	0	1 (	0 0	0	0
																		STM	3 <b>2F4</b> 1	3/42	ìΛ	]型·	- 100N	1Hz																						
STM32F413CGU6	100	ARM Cortex-M4	1024	320	0	UFQFPN48	36	1.7	3.6	12	2	2 1	0	1	10	0	0	2	0	0 ;	5 (	0 5	5 4	[1]	3+3	0	3	1	0	0	0	1 0	0	0	0	0	0	1	0	2	0	0	1 (	0 0	0	0
STM32F413RGT6	100	ARM Cortex-M4	1024	320	0	LQFP64	50	1.7	3.6	12	2	2 1	0	1	16	0	0	2	0	0 ;	5 1	1 5	5 4	[1]	4+3	0	3	1	0	0	0	1 0	0	0	0	0	0	1	0	2	0	0	1 (	0 0	0	0
STM32F413VGT6	100	ARM Cortex-M4	1024	320	0	LQFP100	81	1.7	3.6	12	2	2 1	0	1	16	0	0	2	0	0 ;	5 1	1 5	5 4	[1]	4+6	0	3	1	1	0	0	1 0	0	0	0	0	0	1	0	2	0	0	1 (	0 0	0	0
STM32F413VGJ6	100	ARM Cortex-M4	1024	320	0	UFBGA100	81	1.7	3.6	12	2	2 1	0	1	16	0	0	2	0	0 !	5 1	1 5	5 4	[1]	4+6	0	3	1	1	0	0	1 0	0	0	0	0	0	1	0	2	0	0	1 (	0 0	0	0
STM32F413ZGT6	100	ARM Cortex-M4	1024	320	0	LQFP144	114	1.7	3.6	12	2	2 1	0	1	16	0	0	2	0	0 :	5 1	1 5	5 4	[1]	4+6	0	3	1	1	0	0	1 0	0	0	0	0	0	1	0	2	0	0	1 (	0 0	0	0
STM32F413ZGJ6	100	ARM Cortex-M4	1024	320	0	UFBGA144	114	1.7	3.6	12	2	2 1	0	1	16	0	0	2	0	0 :	5 1	1 5	5 4	[1]	4+6	0	3	1	1	0	0	1 0	0	0	0	0	0	1	0	2	0	0	1 (	0 0	0	0
STM32F413CHU6	100	ARM Cortex-M4	1536	320	0	UFQFPN48	36	1.7	3.6	12	2	2 1	0	1	10	0	0	2	0	0 !	5 (	0 5	5 4	[1]	3+3	0	3	1	0	0	0	1 0	0	0	0	0	0	1	0	2	0	0	1 1	0 0	0	0
STM32F413RHT6	100	ARM Cortex-M4	1536	320	0	LQFP64	50	1.7	3.6	12	2	2 1	0	1	16	0	0	2	0	0 !	5 1	1 5	5 4	[1]	4+3	0	3	1	0	0	0	1 0	0	0	0	0	0	1	0	2	0	0	1 (	0 0	0	0
STM32F413VHT6	100	ARM Cortex-M4	1536	320	0	LQFP100	81	1.7	3.6	12	2	2 1	0	1	16	0	0	2	0	0 !	5 1	1 5	5 4	[1]	4+6	0	3	1	1	0	0	1 0	0	0	0	0	0	1	0	2	0	0	1 1	0 0	0	0
STM32F413VHJ6	100	ARM Cortex-M4	1536	320	0	UFBGA100	81	1.7	3.6	12	2	2 1	0	1	16	0	0	2	0	0 :	5 1	1 5	5 4	[1]	4+6	0	3	1	1	0	0	1 0	0	0	0	0	0	1	0	2	0	0	1 1	0 0	0	0
STM32F413ZHT6	100	ARM Cortex-M4	1536	320	0	LQFP144	114	1.7	3.6	12	2	2 1	0	1	16	0	0	2	0	0 !	5 1	1 5	5 4	[1]	4+6	0	3	1	1	0	0	1 0	0	0	0	0	0	1	0	2	0	0	1 '	0 0	0	0
STM32F413ZHJ6	100	ARM Cortex-M4	1536	320	0	UFBGA144	114	1.7	3.6	12	2	2 1	0	1	16	0	0	2	0	0 !	5 1	1 5	5 4	[1]	4+6	0	3	1	1	0	0	1 0	0	0	0	0	0	1	0	2	0	0	1 '	0 0	0	0
STM32F423CHU6	100	ARM Cortex-M4	1536	320	0	UFQFPN48	36	1.7	3.6	12	2	2 1	0	1	10	0	0	2	0	0 ;	5 (	0 5	5 4	[1]	3+3	0	3	1	0	0	0	1 0	0	0	0	0	0	1	0	2	0	0	1	1 0	0	0

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产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通 用 IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12位DAC	比较器と	放大器	QUADSPI	J		高速I°C	U(S)ART	低功耗UART	CAN	FSIMC	FMC	从USB	全速USB OTG	高速USB OTG	MDIOS	段式LCD	TFI LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES AES	SHA	HMAC
STM32F423RHT6	100	ARM Cortex-M4	1536	320	0	LQFP64	50	1.7	3.6	12	2	2 1	0	1	16	0	0	2	0 (	0 !	5 1	5	5 4	[1]	4+3	0	3 1	0	0	0	1	0	0 0	0	0	0	1	0	2	0	0	1	1 0	0	0
STM32F423VHT6	100	ARM Cortex-M4	1536	320	0	LQFP100	81	1.7	3.6	12	2	2 1	0	1	16	0	0	2	0 (	0 !	5 1	5	5 4	[1]	4+6	0	3 1	1	0	0	1	0	0 0	0	0	0	1	0	2	0	0	1	1 0	0	0
STM32F423VHJ6	100	ARM Cortex-M4	1536	320	0	UFBGA100	81	1.7	3.6	12	2	2 1	0	1	16	0	0	2	0 (	0 !	5 1	5	5 4	[1]	4+6	0	3 1	1	0	0	1	0	0 0	0	0	0	1	0	2	0	0	1	1 0	0	0
STM32F423ZHT6	100	ARM Cortex-M4	1536	320	0	LQFP144	114	1.7	3.6	12	2	2 1	0	1	16	0	0	2	0 (	0 !	5 1	5	5 4	[1]	4+6	0	3 1	1	0	0	1	0	0 0	0	0	0	1	0	2	0	0	1	1 0	0	0
STM32F423ZHJ6	100	ARM Cortex-M4	1536	320	0	UFBGA144	114	1.7	3.6	12	2	2 1	0	1	16	0	0	2	0 (	0 !	5 1	5	5 4	[1]	4+6	0	3 1	1	0	0	1	0	0 0	0	0	0	1	0	2	0	0	1	1 0	0	0
		COLLOX III I																STM	32F4	05 / 4	415基	本型	<u>4</u> - 168	MHz																					
STM32F405RGT6	168	ARM Cortex-M4	1024	192	0	LQFP64	51	1.8	3.6	12	2	2 0	0	3	16	0	0				3 0	Ť		0	4+2	0	2 1	0	0	0	1	1	0 0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F4050GY6	168	ARM Cortex-M4	1024	192	0	WLCSP90	72	1.8	3.6	12	2	2 0	0	3	13	0	0	2	0 (	0 :	3 0	2	2 3	0	4+2	0	2 1	1	0	0	1	1	0 0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F4050EY6	168	ARM Cortex-M4	512	192	0	WLCSP90	72	1.8	3.6	12	2	2 0	0	3	13	0	0	2	0 (	0 :	3 0	2	2 3	0	4+2	0	2 1	1	0	0	1	1	0 0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F405VGT6	168	ARM Cortex-M4	1024	192	0	LQFP100	82	1.8	3.6	12	2	2 0	0	3	16	0	0	2	0 (	0 ;	3 0	2	2 3	0	4+2	0	2 1	1	0	0	1	1	0 0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F405ZGT6	168	ARM Cortex-M4	1024	192	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0 (	0 ;	3 0	2	2 3	0	4+2	0	2 1	1	0	0	1	1	0 0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32F415RGT6	168	ARM Cortex-M4	1024	192	0	LQFP64	51	1.8	3.6	12	2	2 0	0	3	16	0	0	2	0 (	0 ;	3 0	2	2 3	0	4+2	0	2 1	0	0	0	1	1	0 0	0	0	0	0	0	0	0	0	1	1 1	1	1
STM32F4150GY6	168	ARM Cortex-M4	1024	192	0	WLCSP90	72	1.8	3.6	12	2	2 0	0	3	13	0	0	2	0 (	0 :	3 0	2	2 3	0	4+2	0	2 1	1	0	0	1	1	0 0	0	0	0	0	0	0	0	0	1	1 1	1	1
STM32F415VGT6	168	ARM Cortex-M4	1024	192	0	LQFP100	82	1.8	3.6	12	2	2 0	0	3	16	0	0	2	0 (	0 ;	3 0	2	2 3	0	4+2	0	2 1	1	0	0	1	1	0 0	0	0	0	0	0	0	0	0	1	1 1	1	1
STM32F415ZGT6	168	ARM Cortex-M4	1024	192	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0 (	0 :	3 0	2	2 3	0	4+2	0	2 1	1	0	0	1	1	0 0	0	0	0	0	0	0	0	0	1	1 1	1	1
		OUTLEX WIT															5	тмз	2F407	7 / 41	7基2	型	- 168 N	ИHz																					
STM32F407VET6	168	ARM Cortex-M4	512	192	0	LQFP100	82	1.8	3.6	12	2	2 0	0	3	16	0	0	2	0 (	0 :	3 0	2	2 3	0	4+2	0	2 1	1	0	0	1	1	1 0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F407VGT6	168	ARM Cortex-M4	1024	192	0	LQFP100	82	1.8	3.6	12	2	2 0	0	3	16	0	0	2	0 (	0 ;	3 0	2	2 3	0	4+2	0	2 1	1	0	0	1	1	1 0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F407ZET6	168	ARM Cortex-M4	512	192	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0 (	0 ;	3 0	2	2 3	0	4+2	0	2 1	1	0	0	1	1	1 0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F407ZGT6	168	ARM Cortex-M4	1024	192	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0 (	0 :	3 0	2	2 3	0	4+2	0	2 1	1	0	0	1	1	1 0	0	0	0	0	0	0	1	0	1	0 0	0	0
STM32F407IET6	168	ARM Cortex-M4	512	192	0	LQFP176	140	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0 (	0 ;	3 0	2	2 3	0	4+2	0	2 1	1	0	0	1	1	1 0	0	0	0	0	0	0	1	0	1	0 0	0 0	0
STM32F407IEH6	168	ARM	512	192	0	UFBGA176	140	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0 (	0 ;	3 0	2	2 3	0	4+2	0	2 1	1	0	0	1	1	1 0	0	0	0	0	0	0	1	0	1	0 0	0 0	0
STM32F407IGT6	168	ARM Cortex-M4		192		LQFP176			3.6	12	-	2 0	0	3	24	0	_	+	0 (	+	3 0	+	+	0	4+2	0	2 1	1	0	0	1	1	1 0		0	0	0	0	0	1	0	1	0 0	0	0
		COLLEX-IVI4																																				ш							ш

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产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16:立ADC通道	比较器	放大器	SPI	QUADSPI	I's	高速「G	U(S)ART	低功耗UART	CAN	SDIO	FMC	从USB	全速USB OTG	高速USB OTG	以太网	段式LCD	TET LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DGMI	SWPMI	AES	DES/TDES	SHA	HMAC
STM32F407IGH6	168	ARM Cortex-M4	1024	192	0	UFBGA176	140	1.8	3.6	12	2	2 0	0	3	24	0	0 2	2 0	0	3	0	2 3	0	4+2	0	2	1 1	0	0	1	1	1 (	0	0	0	0	0	0	1	0 -	0	0	0	0
STM32F417VET6	168	ARM Cortex-M4	512	192	0	LQFP100	82	1.8	3.6	12	2	2 0	0	3	16	0	0 2	2 0	0	3	0	2 3	0	4+2	0	2	1 1	0	0	1	1	1 (	0	0	0	0	0	0	1	0 .	1	1	1	1
STM32F417VGT6	168	ARM Cortex-M4	1024	192	0	LQFP100	82	1.8	3.6	12	2	2 0	0	3	16	0	0 2	2 0	0	3	0	2 3	0	) 4+2	0	2	1 1	0	0	1	1	1 (	0	0	0	0	0	0	1	0 -	1 1	1	1	1
STM32F417ZET6	168	ARM Cortex-M4	512	192	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	0 2	2 0	0	3	0	2 3	0	) 4+2	0	2	1 1	0	0	1	1	1 (	0	0	0	0	0	0	1	0	1 1	1	1	1
STM32F417ZGT6	168	ARM Cortex-M4	1024	192	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	0 2	2 0	0	3	0	2 3	0	) 4+2	0	2	1 1	0	0	1	1	1 (	0	0	0	0	0	0	1	0 -	1 1	1	1	1
STM32F417IET6	168	ARM Cortex-M4	512	192	0	LQFP176	140	1.8	3.6	12	2	2 0	0	3	24	0	0 2	2 0	0	3	0	2 3	0	) 4+2	0	2	1 1	0	0	1	1	1 (	0	0	0	0	0	0	1	0 -	1 1	1	1	1
STM32F417IEH6	168	ARM Cortex-M4	512	192	0	UFBGA176	140	1.8	3.6	12	2	2 0	0	3	24	0	0 2	2 0	0	3	0	2 3	0	) 4+2	0	2	1 1	1 0	0	1	1	1 (	0	0	0	0	0	0	1	0 -	1 1	1	1	1
STM32F417IGT6	168	ARM Cortex-M4	1024	192	0	LQFP176	140	1.8	3.6	12	2	2 0	0	3	24	0	0 2	2 0	0	3	0	2 3	0	) 4+2	0	2	1 1	1 0	0	1	1	1 (	0	0	0	0	0	0	1	0 -	1	1	1	1
STM32F417IGH6	168	ARM Cortex-M4	1024	192	0	UFBGA176	140	1.8	3.6	12	2	2 0	0	3	24	0	0 2	2 0	0	3	0	2 3	0	4+2	0	2	1 1	0	0	1	1	1 (	0	0	0	0	0	0	1	0 -	1	1	1	1
																		STM	32F4	6基4	型.	- 180 N	Hz																					
STM32F446MCY6	180	ARM Cortex-M4	256	128	0	WLCSP81	63	1.8	3.6	12	2	2 0	0	3	14	0	0 2	2 0	0	4	1	3 4	[1	] 4+2	0	2	1 (	0	0	1	1	0 0	0	0	0	2	1	0	1	0 (	0	0	0	0
STM32F446MEY6	180	ARM Cortex-M4	512	128	0	WLCSP81	63	1.8	3.6	12	2	2 0	0	3	14	0	0 2	2 0	0	4	1	3 4	[1	] 4+2	0	2	1 (	0	0	1	1	0 0	0	0	0	2	1	0	1	0 (	0	0	0	0
STM32F446RCT6	180	ARM Cortex-M4	256	128	0	LQFP64	50	1.8	3.6	12	2	2 0	0	3	16	0	0 2	2 0	0	4	1	3 4	[1	] 4+2	0	2	1 (	0	0	1	1	0 0	0	0	0	2	1	0	1	0 (	0	0	0	0
STM32F446RET6	180	ARM Cortex-M4	512	128	0	LQFP64	50	1.8	3.6	12	2	2 0	0	3	16	0	0 2	2 0	0	4	1	3 4	[1	] 4+2	0	2	1 (	0	0	1	1	0 0	0	0	0	2	1	0	1	0 (	0	0	0	0
STM32F446VCT6	180	ARM Cortex-M4	256	128	0	LQFP100	81	1.8	3.6	12	2	2 0	0	3	16	0	0 2	2 0	0	4	1	2 4	[1	] 4+2	0	2	1 (	) 1	0	1	1	0 0	0	0	0	2	1	0	1	0 (	0	0	0	0
STM32F446VET6	180	ARM Cortex-M4	512	128	0	LQFP100	81	1.8	3.6	12	2	2 0	0	3	16	0	0 2	2 0	0	4	1	3 4	[1	] 4+2	0	2	1 (	) 1	0	1	1	0 0	0	0	0	2	1	0	1	0 (	0	0	0	0
STM32F446ZCT6	180	ARM Cortex-M4	256	128	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	0 2	2 0	0	4	1	3 4	[1	] 4+2	0	2	1 (	) 1	0	1	1	0 0	0	0	0	2	1	0	1	0 (	0	0	0	0
STM32F446ZCH6	180	ARM Cortex-M4	256	128	0	UFBGA144	114	1.8	3.6	12	2	2 0	0	3	24	0	0 2	2 0	0	4	1	3 4	[1	] 4+2	0	2	1 (	) 1	0	1	1	0 0	0	0	0	2	1	0	1	0 (	0	0	0	0
STM32F446ZET6	180	ARM Cortex-M4	512	128	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	0 2	2 0	0	4	1	3 4	[1	] 4+2	0	2	1 (	) 1	0	1	1	0 0	0	0	0	2	1	0	1	0 (	0	0	0	0
STM32F446ZEH6	180	ARM Cortex-M4	512	128	0	UFBGA144	114	1.8	3.6	12	2	2 0	0	3	24	0	0 2	2 0	0	4	1	3 4	[1	] 4+2	0	2	1 (	) 1	0	1	1	0 0	0	0	0	2	1	0	1	0 (	0	0	0	0
																	S	TM32	427	437∄	<b>高级</b> 型	型 - 18	MH	z																				
STM32F427VGT6	180	ARM Cortex-M4	1024	256	0	LQFP100	82	1.8	3.6	12	2	2 0	0	3	16	0	0 2	2 0	0	4	0	2 3	0	4+4	0	2	1 1	0	0	1	1	1 (	0	0	0	1	0	0	1	0	0	0	0	0
		ARM	2048			LQFP100			3.6	12	2	2 0	0	3	16	0	0 2	2 0	0	4	0	2 3	0	) 4+4	0	2	1 1	0	0	1	1	1 (	0	0	0		0	0	1	0 -	0	0	0	0

产品型号	主频 (MHz)	<b>内</b> 核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	上#點 12位DAC	比交易	文 SPI	QUADSPI	l²S	l²C	高速rc	U(S)ART	低功耗UART	CAN	SDIO	FMC	从USB	全速USB OTG	高速USB OTG	以太网	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DOMI	SWPMI	TRNG	DES/TDES	SHA	НМАС
STM32F427ZGT6	180	ARM Cortex-M4	1024	256	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	0	2 (	0	6	0	2	3	0	4+4	0	2	1 1	0	0	1	1	1	0	0	0	0	1	0	0	1	0	1 (	0 0	0	0
STM32F427ZIT6	180	ARM Cortex-M4	2048	256	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	0	2 (	0	) 6	0	2	3	0	4+4	0	2	1 1	0	0	1	1	1	0	0	0	0	1	0	0	1	0	1 (	0 0	0	0
STM32F427AGH6	180	ARM Cortex-M4	1024	256	0	UFBGA169	130	1.8	3.6	12	2	2 0	0	3	24	0	0	2 (	) (	) 6	0	2	3	0	4+4	0	2	1 1	0	0	1	1	1	0	0	0	0	1	0	0	1	0	1 (	0 0	0	0
STM32F427AIH6	180	ARM Cortex-M4	2048	256	0	UFBGA169	130	1.8	3.6	12	2	2 0	0	3	24	0	0	2 (	) (	) 6	0	2	3	0	4+4	0	2	1 1	0	0	1	1	1	0	0	0	0	1	0	0 -	1	0	1 (	0 0	0	0
STM32F427IGT6	180	ARM Cortex-M4	1024	256	0	LQFP176	140	1.8	3.6	12	2	2 0	0	3	24	0	0	2 (	) (	) 6	0	2	3	0	4+4	0	2	1 1	0	0	1	1	1	0	0	0	0	1	0	0	1	0	1 (	0 0	0	0
STM32F427IGH6	180	ARM Cortex-M4	1024	256	0	UFBGA176	140	1.8	3.6	12	2	2 0	0	3	24	0	0	2 (	) (	) 6	0	2	3	0	4+4	0	2	1 1	0	0	1	1	1	0	0	0	0	1	0	0 -	1	0	1 (	0 0	0	0
STM32F427IIT6	180	ARM Cortex-M4	2048	256	0	LQFP176	140	1.8	3.6	12	2	2 0	0	3	24	0	0	2 (	) (	) 6	0	2	3	0	4+4	0	2	1 1	0	0	1	1	1	0	0	0	0	1	0	0 -	1	0	1 (	0 0	0	0
STM32F427IIH6	180	ARM Cortex-M4	2048	256	0	UFBGA176	140	1.8	3.6	12	2	2 0	0	3	24	0	0	2 (	) (	) 6	0	2	3	0	4+4	0	2	1 1	0	0	1	1	1	0	0	0	0	1	0	0 -	1	0	1 (	0 0	0	0
STM32F437VGT6	180	ARM Cortex-M4	1024	256	0	LQFP100	82	1.7	3.6	12	2	2 0	0	3	16	0	0	2 (	0	) 4	0	2	3	0	4+4	0	2	1 1	0	0	1	1	1	0	0	0	0	1	0	0 -	1	0	1 1	1 1	1	1
STM32F437VIT6	180	ARM Cortex-M4	2048	256	0	LQFP100	82	1.7	3.6	12	2	2 0	0	3	16	0	0	2 (	) (	) 4	0	2	3	0	4+4	0	2	1 1	0	0	1	1	1	0	0	0	0	1	0	0 -	1	0	1 1	1 1	1	1
STM32F437ZGT6	180	ARM Cortex-M4	1024	256	0	LQFP144	114	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	) (	) 6	0	2	3	0	4+4	0	2	1 1	0	0	1	1	1	0	0	0	0	1	0	0	1	0	1 1	1 1	1	1
STM32F437ZIT6	180	ARM Cortex-M4	2048	256	0	LQFP144	114	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	) (	) 6	0	2	3	0	4+4	0	2	1 1	0	0	1	1	1	0	0	0	0	1	0	0	1	0	1 1	1 1	1	1
STM32F437AIH6	180	ARM Cortex-M4	2048	256	0	UFBGA169	114	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	) 6	0	2	3	0	4+4	0	2	1 1	0	0	1	1	1	0	0	0	0	1	0	0 -	1	0	1 1	1 1	1	1
STM32F437IGT6	180	ARM Cortex-M4	1024	256	0	LQFP176	140	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	) 6	0	2	3	0	4+4	0	2	1 1	0	0	1	1	1	0	0	0	0	1	0	0 -	1	0	1 1	1 1	1	1
STM32F437IGH6	180	ARM Cortex-M4	1024	256	0	UFBGA176	140	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	) (	) 6	0	2	3	0	4+4	0	2	1 1	0	0	1	1	1	0	0	0	0	1	0	0 -	1	0	1 1	1 1	1	1
STM32F437IIT6	180	ARM Cortex-M4	2048	256	0	LQFP176	140	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	6	0	2	3	0	4+4	0	2	1 1	0	0	1	1	1	0	0	0	0	1	0	0 -	1	0	1 1	1 1	1	1
STM32F437IIH6	180	ARM Cortex-M4	2048	256	0	UFBGA176	140	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	6	0	2	3	0	4+4	0	2	1 1	0	0	1	1	1	0	0	0	0	1	0	0 -	1	0	1 1	1 1	1	1
		4014																STM	32F42	29 / 4	39高	级型	- 180	MHz							ı															
STM32F429VET6	180	ARM Cortex-M4	512	256	0	LQFP100	82	1.8	3.6	12	2	2 0	0	3	16	0	0	2 (	0	) 4	0	2	3	0	4+4	0	2	1 (	1	0	1	1	1	0	0	1	0	1	0	0 -	1	0	1 (	0 0	0	0
STM32F429VGT6	180	ARM Cortex-M4	1024	256	0	LQFP100	82	1.8	3.6	12	2	2 0	0	3	16	0	0	2 (	0	) 4	0	2	3	0	4+4	0	2	1 (	1	0	1	1	1	0	0	1	0	1	0	0 -	1	0	1 (	0 0	0	0
STM32F429VIT6	180	ARM Cortex-M4	2048	256	0	LQFP100	82	1.8	3.6	12	2	2 0	0	3	16	0	0	2 (	0	6	0	2	3	0	4+4	0	2	1 (	1	0	1	1	1	0	0	1	0	1	0	0 -	1	0	1 (	0 0	0	0
STM32F429ZET6	180	ARM Cortex-M4	512	256	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	0	2 (	) (	) 6	0	2	3	0	4+4	0	2	1 (	1	0	1	1	1	0	0	1	0	1	0	0 -	1	0	1 (	0 0	0	0

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产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通 用 O	最低工作电压	最高工作电压		32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	QUADOFI	OHADEBI	දී දි	高速I°C	U(S)ART	低功耗UART	CAN	SDIO	FSMC	FMC	全速USB OTG	高速USB OTG	以太网	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES AES	DES/TRES	HMAC
STM32F429ZGT6	180	ARM Cortex-M4	1024	256	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	6 0	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0 0	0
STM32F429ZIT6	180	ARM Cortex-M4	2048	256	0	LQFP144	114	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	6 0	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0	0
STM32F429ZEY6	180	ARM Cortex-M4	512	256	0	WLCSP143	114	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	6 0	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0 0	0
STM32F429ZGY6	180	ARM Cortex-M4	1024	256	0	WLCSP143	114	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	6 0	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0 0	0
STM32F429ZIY6	180	ARM Cortex-M4	2048	256	0	WLCSP143	114	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	6 (	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0 0	0
STM32F429AGH6	180	ARM Cortex-M4	1024	256	0	UFBGA169	130	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	6 (	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0 0	0
STM32F429AIH6	180	ARM Cortex-M4	2048	256	0	UFBGA169	130	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	6 (	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0 0	0
STM32F429IEH6	180	ARM Cortex-M4	512	256	0	UFBGA176	140	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0	6 (	) :	2 3	0	4+4	0	2	1	0	1	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0	0
STM32F429IET6	180	ARM Cortex-M4	512	256	0	LQFP176	140	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	6 0	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0	0
STM32F429IGH6	180	ARM Cortex-M4	1024	256	0	UFBGA176	140	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	6 0	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0 0	0
STM32F429IGT6	180	ARM Cortex-M4	1024	256	0	LQFP176	140	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	6 0	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0 0	0
STM32F429IIH6	180	ARM Cortex-M4	2048	256	0	UFBGA176	140	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	6 0	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0 0	0
STM32F429IIT6	180	ARM Cortex-M4	2048	256	0	LQFP176	140	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	6 0	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0 0	0
STM32F429BET6	180	ARM Cortex-M4	512	256	0	LQFP208	168	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	6 0	) 2	2 3	0	4+4	0	2	1	0	1	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0 0	0
STM32F429BGT6	180	ARM Cortex-M4	1024	256	0	LQFP208	168	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	6 0	) ;	2 3	0	4+4	0	2	1	0	1	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0 0	0
STM32F429BIT6	180	ARM Cortex-M4	2048	256	0	LQFP208	168	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	3 0	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0	0
STM32F429NEH6	180	ARM Cortex-M4	512	256	0	TFBGA216	168	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	3 0	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0 0	0
STM32F429NGH6	180	ARM Cortex-M4	1024	256	0	TFBGA216	168	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	6 0	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0 0	0
STM32F429NIH6	180	ARM Cortex-M4	2048	256	0	TFBGA216	168	1.8	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	6 0	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	0 0	0 0	0
STM32F439VGT6	180	ARM Cortex-M4	1024	256	0	LQFP100	82	1.7	3.6	12	2	2 0	0	3	16	0	0	2	0	0 4	4 C	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	1 1	1	1
STM32F439VIT6	180	ARM Cortex-M4	2048	256	0	LQFP100	82	1.7	3.6	12	2	2 0	0	3	16	0	0	2	0	0 4	4 (	) 2	2 3	0	4+4	0	2	1	0	1 (	0 1	1	1	0	0	1	0	1	0	0	1	0	1	1 1	1	1
STM32F439AIH6	180	ARM Cortex-M4	2048	256	0	UFBGA169	114	1.7	3.6	12	2	2 0	0	3	24	0	0	2	0	0 (	6 0	) 2	2 3	0	4+4	0	2	1	0	1	0 1	1	1	0	0	1	0	1	0	0	1	0	1	1 1	1	1

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12担DAC	比交器	SPI	QUADSPI	l <sup>2</sup> S	l²C	高速rc	U(S)ART	低功耗UART	GAN	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	以太网	段式LCD	IFI LLD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	AES	DES/TDES	HMAC
STM32F439ZGT6	180	ARM Cortex-M4	1024	256	0	LQFP144	114	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	) (	6	0	2	3	0	4+4	0	2	0	1	0	1	1	1 (	0	1	0	1	0	0	1	0	1	1 1	1 1	1 1
STM32F439ZIT6	180	ARM Cortex-M4	2048	256	0	LQFP144	114	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	6	0	2	3	0	4+4	0	2 1	0	1	0	1	1	1 (	0	1	0	1	0	0	1	0	1	1 1	1 1	. 1
STM32F439ZIY6	180	ARM Cortex-M4	2048	256	0	WLCSP143	114	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	6	0	2	3	0	4+4	0	2 1	0	1	0	1	1	1 (	0	1	0	1	0	0	1	0	1	1 1	1 1	1
STM32F439IGT6	180	ARM Cortex-M4	1024	256	0	LQFP176	140	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	6	0	2	3	0	4+4	0	2	0	1	0	1	1	1 (	0	1	0	1	0	0	1	0	1	1 1	1 1	1
STM32F439IGH6	180	ARM Cortex-M4	1024	256	0	UFBGA176	140	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	6	0	2	3	0	4+4	0	2	0	1	0	1	1	1 (	0	1	0	1	0	0	1	0	1	1 1	1 1	1
STM32F439IIT6	180	ARM Cortex-M4	2048	256	0	LQFP176	140	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	6	0	2	3	0	4+4	0	2	0	1	0	1	1	1 (	0	1	0	1	0	0	1	0	1	1 1	1 1	1
STM32F439IIH6	180	ARM Cortex-M4	2048	256	0	UFBGA176	140	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0 0	6	0	2	3	0	4+4	0	2	0	1	0	1	1	1 (	0	1	0	1	0	0	1	0	1	1 1	1 1	1
STM32F439BGT6	180	ARM Cortex-M4	1024	256	0	LQFP208	168	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0 0	6	0	2	3	0	4+4	0	2	0	1	0	1	1	1 (	0	1	0	1	0	0	1	0	1	1 1	1 1	1
STM32F439BIT6	180	ARM Cortex-M4	2048	256	0	LQFP208	168	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	6	0	2	3	0	4+4	0	2 1	0	1	0	1	1	1 (	0	1	0	1	0	0	1	0	1	1 1	1 1	1
STM32F439NGH6	180	ARM Cortex-M4	1024	256	0	TFBGA216	168	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	) (	6	0	2	3	0	4+4	0	2 1	0	1	0	1	1	1 (	0	1	0	1	0	0	1	0	1	1 1	1 1	1
STM32F439NIH6	180	ARM Cortex-M4	2048	256	0	TFBGA216	168	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	6	0	2	3	0	4+4	0	2 1	0	1	0	1	1	1 (	0	1	0	1	0	0	1	0	1	1 1	1 1	1
																		STM3	2F469	/ 479	高级	型 -	180 N	ИHZ																					
STM32F469AEH6	180	ARM Cortex-M4	512	384	0	UFBGA169	114	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	6	1	2	3	0	4+4	0	2 1	0	1	0	1	1	0 (	0	1	1	1	0	0	1	0	1	0 0	0 0	0
STM32F469AGH6	180	ARM Cortex-M4	1024	384	0	UFBGA169	114	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	) (	6	1	2	3	0	4+4	0	2 1	0	1	0	1	1	0 (	0	1	1	1	0	0	1	0	1	0 0	0 0	0
STM32F469AlH6	180	ARM Cortex-M4	2048	384	0	UFBGA169	114	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	) (	6	1	2	3	0	4+4	0	2 1	0	1	0	1	1	0 (	0	1	1	1	0	0	1	0	1	0 0	0 0	0
STM32F469AEY6	180	ARM Cortex-M4	512	384	0	WLCSP168	114	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	6	1	2	3	0	4+4	0	2	0	1	0	1	1	0 (	0	1	1	1	0	0	1	0	1	0 0	0 0	0
STM32F469AGY6	180	ARM Cortex-M4	1024	384	0	WLCSP168	114	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	6	1	2	3	0	4+4	0	2 1	0	1	0	1	1	0 (	0	1	1	1	0	0	1	0	1	0 0	0 0	0
STM32F469AIY6	180	ARM Cortex-M4	2048	384	0	WLCSP168	114	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	6	1	2	3	0	4+4	0	2 1	0	1	0	1	1	0 (	0	1	1	1	0	0	1	0	1	0 0	0 0	0
STM32F469IET6	180	ARM Cortex-M4	512	384	0	LQFP176	131	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	6	1	2	3	0	4+4	0	2 1	0	1	0	1	1	1 (	0	1	1	1	0	0	1	0	1	0 0	0 0	0
STM32F469IEH6	180	ARM Cortex-M4	512	384	0	UFBGA176	131	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	6	1	2	3	0	4+4	0	2	0	1	0	1	1	1 (	0	1	1	1	0	0	1	0	1	0 0	0 (	0
STM32F469IGT6	180	ARM Cortex-M4	1024	384	0	LQFP176	131	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0	6	1	2	3	0	4+4	0	2	0	1	0	1	1	1 (	0	1	1	1	0	0	1	0	1	0 0	0 0	0
STM32F469IGH6	180	ARM Cortex-M4	1024	384	0	UFBGA176	131	1.7	3.6	12	2	2 0	0	3	24	0	0	2 (	0 0	6	1	2	3	0	4+4	0	2	0	1	0	1	1	1 (	0	1	1	1	0	0	1	0	1	0 0	0 0	0

产品型号	主频 (MHz)	<b>内</b> 核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压		32位定时器	电机控制定时器	この ままれる こう こう こう こう おう おう おう おう ままれる こう おう こう	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	CDI	OHADEBI	r c	高速I°C	U(S)ART	低功耗UART	CAN	SDIO	FSMC	FMC	全速USB OTG	高速USB OTG	以太网	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES AES	SHA	HMAC
STM32F469IIT6	180	ARM Cortex-M4	2048	384	0	LQFP176	131	1.7	3.6	12	2	2 0	0	3	24	0	0	2	0	0 6	6 1	2	2 3	0	4+4	0	2	1	0	1	) 1	1	1	0	0	1	1	1	0	0	1	0	1	0 0	0	0
STM32F469IIH6	180	ARM Cortex-M4	2048	384	0	UFBGA176	131	1.7	3.6	12	2	2 0	0	3	24	0	0	2	0	0 6	6 1	2	2 3	0	4+4	0	2	1	0	1	) 1	1	1	0	0	1	1	1	0	0	1	0	1	0 0	0	0
STM32F469BET6	180	ARM Cortex-M4	512	384	0	LQFP208	161	1.7	3.6	12	2	2 0	0	3	24	0	0	2	0	0 6	6 1	2	2 3	0	4+4	0	2	1	0	1	) 1	1	1	0	0	1	1	1	0	0	1	0	1	0 0	0	0
STM32F469BGT6	180	ARM Cortex-M4	1024	384	0	LQFP208	161	1.7	3.6	12	2	2 (	0	3	24	0	0	2	0	0 6	ô 1	2	2 3	0	4+4	0	2	1	0	1	) 1	1	1	0	0	1	1	1	0	0	1	0	1	0 0	0	0
STM32F469BIT6	180	ARM Cortex-M4	2048	384	0	LQFP208	161	1.7	3.6	12	2	2 (	0	3	24	0	0	2	0	0 6	6 1	2	2 3	0	4+4	0	2	1	0	1	) 1	1	1	0	0	1	1	1	0	0	1	0	1	0 0	0	0
STM32F469NEH6	180	ARM Cortex-M4	512	384	0	TFBGA216	161	1.7	3.6	12	2	2 (	0	3	24	0	0	2	0	0 6	6 1	1 2	2 3	0	4+4	0	2	1	0	1	) 1	1	1	0	0	1	1	1	0	0	1	0	1	0 0	0	0
STM32F469NGH6	180	ARM Cortex-M4	1024	384	0	TFBGA216	161	1.7	3.6	12	2	2 (	0	3	24	0	0	2	0	0 6	6 1	2	2 3	0	4+4	0	2	1	0	1	) 1	1	1	0	0	1	1	1	0	0	1	0	1	0 0	0	0
STM32F469NIH6	180	ARM Cortex-M4	2048	384	0	TFBGA216	161	1.7	3.6	12	2	2 (	0	3	24	0	0	2	0	0 6	6 1	1 2	2 3	0	4+4	0	2	1	0	1	) 1	1	1	0	0	1	1	1	0	0	1	0	1	0 0	0	0
STM32F479AGH6	180	ARM Cortex-M4	1024	384	0	UFBGA169	114	1.7	3.6	12	2	2 0	0	3	24	0	0	2	0	0 6	6 1	2	2 3	0	4+4	0	2	1	0	1	) 1	1	0	0	0	1	1	1	0	0	1	0	1	1 1	1	1
STM32F479AIH6	180	ARM Cortex-M4	2048	384	0	UFBGA169	114	1.7	3.6	12	2	2 0	0	3	24	0	0	2	0	0 6	6 1	2	2 3	0	4+4	0	2	1	0	1	) 1	1	0	0	0	1	1	1	0	0	1	0	1	1 1	1	1
STM32F479AGY6	180	ARM Cortex-M4	1024	384	0	WLCSP168	114	1.7	3.6	12	2	2 0	0	3	24	0	0	2	0	0 6	6 1	2	2 3	0	4+4	0	2	1	0	1	) 1	1	0	0	0	1	1	1	0	0	1	0	1	1 1	1	1
STM32F479AIY6	180	ARM Cortex-M4	2048	384	0	WLCSP168	114	1.7	3.6	12	2	2 (	0	3	24	0	0	2	0	0 6	6 1	2	2 3	0	4+4	0	2	1	0	1	) 1	1	0	0	0	1	1	1	0	0	1	0	1	1 1	1	1
STM32F479IGT6	180	ARM Cortex-M4	1024	384	0	LQFP176	131	1.7	3.6	12	2	2 (	0	3	24	0	0	2	0	0 6	6 1	1 2	2 3	0	4+4	0	2	1	0	1	) 1	1	1	0	0	1	1	1	0	0	1	0	1	1 1	1	1
STM32F479IGH6	180	ARM Cortex-M4	1024	384	0	UFBGA176	131	1.7	3.6	12	2	2 (	0	3	24	0	0	2	0	0 6	6 1	1 2	2 3	0	4+4	0	2	1	0	1	) 1	1	1	0	0	1	1	1	0	0	1	0	1	1 1	1	1
STM32F479IIT6	180	ARM Cortex-M4	2048	384	0	LQFP176	131	1.7	3.6	12	2	2 (	0	3	24	0	0	2	0	0 6	6 1	2	2 3	0	4+4	0	2	1	0	1	) 1	1	1	0	0	1	1	1	0	0	1	0	1	1 1	1	1
STM32F479IIH6	180	ARM Cortex-M4	2048	384	0	UFBGA176	131	1.7	3.6	12	2	2 (	0	3	24	0	0	2	0	0 6	3 1	2	2 3	0	4+4	0	2	1	0	1	) 1	1	1	0	0	1	1	1	0	0	1	0	1	1 1	1	1
STM32F479BGT6	180	ARM Cortex-M4	1024	384	0	LQFP208	161	1.7	3.6	12	2	2 0	0	3	24	0	0	2	0	0 6	3 1	2	2 3	0	4+4	0	2	1	0	1	) 1	1	1	0	0	1	1	1	0	0	1	0	1	1 1	1	1
STM32F479BIT6	180	ARM Cortex-M4	2048	384	0	LQFP208	161	1.7	3.6	12	2	2 0	0	3	24	0	0	2	0	0 6	6 1	2	2 3	0	4+4	0	2	1	0	1	) 1	1	1	0	0	1	1	1	0	0	1	0	1	1 1	1	1
STM32F479NGH6	180	ARM Cortex-M4	1024	384	0	TFBGA216	161	1.7	3.6	12	2	2 (	0	3	24	0	0	2	0	0 6	6 1	2	2 3	0	4+4	0	2	1	0	1	) 1	1	1	0	0	1	1	1	0	0	1	0	1	1 1	1	1
STM32F479NIH6	180	ARM Cortex-M4	2048	384	0	TFBGA216	161	1.7	3.6	12	2	2 (	0	3	24	0	0	2	0	0 6	6 1	2	2 3	0	4+4	0	2	1	0	1	) 1	1	1	0	0	1	1	1	0	0	1	0	1	1 1	1	1

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	(1) (1) (1) (1) (1) (1) (1) (1) (1) (1)	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	SPI	QUADSPI	I'S	l²C	高速元C	U(S)ART	低功耗UART	SDIO	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	以太网	段式LCD	TFI LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	AES	NES/TNES	НМАС
	<u>'                                    </u>		'					_										S	TM32	F7x2	基础	型 -	216 N	lHz														<u>'                                     </u>								
STM32F722RCT6	216	ARM Cortex-M7	256	256+ 16	0	LQFP64	50	1.7	3.6	12	2	2	0	3	16	0	0	2	0	0	3	1	3	3	[3] 4	+2	0	1 1	0	0	0	1	1	0 0	0	0	0	2	0	0	0	0	1	0 0	0 0	0
STM32F722RET6	216	ARM Cortex-M7	512	256+ 16	0	LQFP64	50	1.7	3.6	12	2	2	0	3	16	0	0	2	0	0	3	1	3	3	[3] 4	+2	0	1 1	0	0	0	1	1	0 0	0	0	0	2	0	0	0	0	1	0 0	0 0	0
STM32F722VCT6	216	ARM Cortex-M7	256	256+ 16	0	LQFP100	82	1.7	3.6	12	2	2	1 0	3	16	0	0	2	0	0	3	1	3	3	[3] 4	+4	0	1 1	0	1	0	1	1	0 0	0	0	0	2	0	0	0	0	1	0 0	0 0	0
STM32F722VET6	216	ARM Cortex-M7	512	256+ 16	0	LQFP100	82	1.7	3.6	12	2	2	1 0	3	16	0	0	2	0	0	4	1	3	3	[3] 4	+4	0	1 1	0	1	0	1	1	0 0	0	0	0	2	0	0	0	0	1	0 0	0 0	0
STM32F722ZCT6	216	ARM Cortex-M7	256	256+ 16	0	LQFP144	114	1.7	3.6	12	2	2	1 0	3	24	0	0	2	0	0	5	1	3	3	[3] 4	+4	0	1 2	0	1	0	1	1	0 0	0	0	0	2	0	0	0	0	1	0 0	0 0	0
STM32F722ZET6	216	ARM Cortex-M7	512	256+ 16	0	LQFP144	114	1.7	3.6	12	2	2	1 0	3	24	0	0	2	0	0	5	1	3	3	[3] 4	+4	0	1 2	0	1	0	1	1	0 0	0	0	0	2	0	0	0	0	1	0 0	0 0	0
STM32F722IET6	216	ARM Cortex-M7	512	256+ 16	0	LQFP176	140	1.7	3.6	12	2	2	1 0	3	24	0	0	2	0	0	5	1	3	3	[3] 4	+4	0	1 2	0	1	0	1	1	0 0	0	0	0	2	0	0	0	0	1	0 0	0 0	0
STM32F722IEK6	216	ARM Cortex-M7	512	256+ 16	0	UFBGA176	140	1.7	3.6	12	2	2	1 0	3	24	0	0	2	0	0	5	1	3	3	[3] 4	+4	0	1 2	0	1	0	1	1	0 0	0	0	0	2	0	0	0	0	1	0 0	0 0	0
STM32F732RET6	216	ARM Cortex-M7	512	256+ 16	0	LQFP64	50	1.7	3.6	12	2	2	0	3	16	0	0	2	0	0	3	1	3	3	[3] 4	+2	0	1 1	0	0	0	1	1	0 0	0	0	0	2	0	0	0	0	1	1 0	0 0	0
STM32F732VET6	216	ARM Cortex-M7	512	256+ 16	0	LQFP100	82	1.7	3.6	12	2	2	1 0	3	16	0	0	2	0	0	4	1	3	3	[3] 4	+4	0	1 1	0	1	0	1	1	0 0	0	0	0	2	0	0	0	0	1	1 0	0 0	0
STM32F732ZET6	216	ARM Cortex-M7	512	256+ 16	0	LQFP144	114	1.7	3.6	12	2	2	1 0	3	24	0	0	2	0	0	5	1	3	3	[3] 4	+4	0	1 2	0	1	0	1	1	0 0	0	0	0	2	0	0	0	0	1	1 0	0 0	0
STM32F732IET6	216	ARM Cortex-M7	512	256+ 16	0	LQFP176	140	1.7	3.6	12	2	2	1 0	3	24	0	0	2	0	0	5	1	3	3	[3] 4	+4	0	1 2	0	1	0	1	1	0 0	0	0	0	2	0	0	0	0	1	1 0	0 0	0
STM32F732IEK6	216	ARM Cortex-M7	512	256+ 16	0	UFBGA176	140	1.7	3.6	12	2	2	1 0	3	24	0	0	2	0	0	5	1	3	3	[3] 4	+4	0	1 2	0	1	0	1	1	0 0	0	0	0	2	0	0	0	0	1	1 0	0 0	0
																		S	TM32	F7x3	基础	型 -	216 N	lHz																						
STM32F723VEY6	216	ARM Cortex-M7	512	256+ 16	0	WLCSP100	79	1.7	3.6	11	2	2	1 0	3	16	0	0	2	0	0	4	1	3	3	[3] 4	+4	0	1 1	0	1	0	1	1	0 0	0	0	0	2	0	0	0	0	1	0 0	0 0	0
STM32F723ZCT6	216	ARM Cortex-M7	256	256+ 16	0	LQFP144	112	1.7	3.6	11	2	2	1 0	3	24	0	0	2	0	0	5	1	3	3	[3] 4	+4	0	1 1	0	1	0	1	1	0 0	0	0	0	2	0	0	0	0	1	0 0	0 0	0
STM32F723ZCI6	216	ARM Cortex-M7	256	256+ 16	0	UFBGA144	112	1.7	3.6	11	2	2	1 0	3	24	0	0	2	0	0	5	1	3	3	[3] 4	+4	0	1 1	0	1	0	1	1	0 0	0	0	0	2	0	0	0	0	1	0 0	0 0	0
STM32F723ZET6	216	ARM Cortex-M7	512	256+ 16	0	LQFP144	112	1.7	3.6	11	2	2	1 0	3	24	0	0	2	0	0	5	1	3	3	[3] 4	+4	0	1 1	0	1	0	1	1	0 0	0	0	0	2	0	0	0	0	1	0 0	0 0	0
STM32F723ZEI6	216	ARM Cortex-M7	512	256+ 16	0	UFBGA144	112	1.7	3.6	11	2	2	1 0	3	24	0	0	2	0	0	5	1	3	3	[3] 4	+4	0	1 1	0	1	0	1	1	0 0	0	0	0	2	0	0	0	0	1	0 0	0 0	0
STM32F723ICT6	216	ARM Cortex-M7	256	256+ 16	0	LQFP176	138	1.7	3.6	12	2	2	1 0	3	24	0	0	2	0	0	5	1	3	3	[3] 4	+4	0	1 1	0	1	0	1	1	0 0	0	0	0	2	0	0	0	0	1	0 0	0 0	0
STM32F723ICK6	216	ARM Cortex-M7	256	256+ 16	0	UFBGA176	138	1.7	3.6	12	2	2	1 0	3	24	0	0	2	0	0	5	1	3	3	[3] 4	+4	0	1 1	0	1	0	1	1	0 0	0	0	0	2	0	0	0	0	1	0 0	0 0	0

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	QUADSPI		් දි	高速r°C	U(S)ART	低功耗UART	CAN	SDIO	FSMC	FMC	全速USB UIG	高速USB OTG	以太网	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES AES	SHA	HMAC
STM32F723IET6	216	ARM Cortex-M7	512	256+ 16	0	LQFP176	138	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0 5	i 1	3	3 3	[3]	4+4	0	1	1	0	1	0 1	1	0	0	0	0	0	2	0	0	0	0	1	0 0	0	0
STM32F723IEK6	216	ARM Cortex-M7	512	256+ 16	0	UFBGA176	138	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0 5	i 1	3	3 3	[3]	4+4	0	1	1	0	1	0 1	1	0	0	0	0	0	2	0	0	0	0	1	0 0	0	0
STM32F733VEY6	216	ARM Cortex-M7	512	256+ 16	0	WLCSP100	79	1.7	3.6	11	2	2 1	0	3	16	0	0	2	0	0 4	1	3	3 3	[3]	4+4	0	1	1	0	1	0 1	1	0	0	0	0	0	2	0	0	0	0	1	1 0	0	0
STM32F733ZET6	216	ARM Cortex-M7	512	256+ 16	0	LQFP144	112	1.7	3.6	11	2	2 1	0	3	24	0	0	2	0	0 5	5 1	3	3 3	[3]	4+4	0	1	1	0	1	0 1	1	0	0	0	0	0	2	0	0	0	0	1	1 0	0	0
STM32F733ZEI6	216	ARM Cortex-M7	512	256+ 16	0	UFBGA144	112	1.7	3.6	11	2	2 1	0	3	24	0	0	2	0	0 5	5 1	3	3 3	[3]	4+4	0	1	1	0	1	0 1	1	0	0	0	0	0	2	0	0	0	0	1	1 0	0	0
STM32F733IET6	216	ARM Cortex-M7	512	256+ 16	0	LQFP176	138	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0 5	5 1	3	3 3	[3]	4+4	0	1	1	0	1	0 1	1	0	0	0	0	0	2	0	0	0	0	1	1 0	0	0
STM32F733IEK6	216	ARM Cortex-M7	512	256+ 16	0	UFBGA176	138	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0 5	5 1	3	3 3	[3]	4+4	0	1	1	0	1	0 1	1	0	0	0	0	0	2	0	0	0	0	1	1 0	0	0
																		ST	M32F	7x5高	级型	- 2	16 MH	Z																						
STM32F745VET6	216	ARM Cortex-M7	512	320+ 16	0	LQFP100	82	1.7	3.6	12	2	2 1	0	3	16	0	0	2	0	0 4	1	3	3 4	0	4+4	0	2	1	0	1	0 1	1	1	0	0	0	0	2	4	0	1	0	1	0 0	0	0
STM32F745VGT6	216	ARM Cortex-M7	1024	320+ 16	0	LQFP100	82	1.7	3.6	12	2	2 1	0	3	16	0	0	2	0	0 4	. 1	3	3 4	0	4+4	0	2	1	0	1	0 1	1	1	0	0	0	0	2	4	0	1	0	1	0 0	0	0
STM32F745VEH6	216	ARM Cortex-M7	512	320+ 16	0	TFBGA100	82	1.7	3.6	12	2	2 1	0	3	16	0	0	2	0	0 4	l 1	3	3 4	0	4+4	0	2	1	0	1	0 1	1	1	0	0	0	0	2	4	0	1	0	1	0 0	0	0
STM32F745VGH6	216	ARM Cortex-M7	1024	320+ 16	0	TFBGA100	82	1.7	3.6	12	2	2 1	0	3	16	0	0	2	0	0 4	1	3	3 4	0	4+4	0	2	1	0	1	0 1	1	1	0	0	0	0	2	4	0	1	0	1	0 0	0	0
STM32F745ZET6	216	ARM Cortex-M7	512	320+ 16	0	LQFP144	114	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0 6	5 1	3	3 4	0	4+4	0	2	1	0	1	0 1	1	1	0	0	0	0	2	4	0	1	0	1	0 0	0	0
STM32F745ZGT6	216	ARM Cortex-M7	1024	320 1	0	LQFP144	114	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0 6	5 1	3	3 4	0	4+4	0	2	1	0	1	0 1	1	1	0	0	0	0	2	4	0	1	0	1	0 0	0	0
STM32F745ZEY6	216	ARM Cortex-M7	512	320+ 16	0	WLCSP143	114	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0 6	5 1	3	3 4	0	4+4	0	2	1	0	1	0 1	1	1	0	0	0	0	2	4	0	1	0	1	0 0	0	0
STM32F745ZGY6	216	ARM Cortex-M7	1024	3201⊤	0	WLCSP143	114	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0 6	5 1	3	3 4	0	4+4	0	2	1	0	1	0 1	1	1	0	0	0	0	2	4	0	1	0	1	0 0	0	0
STM32F745IET6	216	ARM Cortex-M7	512	320+ 16	0	LQFP176	140	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0 6	5 1	3	3 4	0	4+4	0	2	1	0	1	0 1	1	1	0	0	0	0	2	4	0	1	0	1	0 0	0	0
STM32F745IGT6	216	ARM Cortex-M7	1024	330 1	0	LQFP176	140	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0 6	5 1	3	3 4	0	4+4	0	2	1	0	1	0 1	1	1	0	0	0	0	2	4	0	1	0	1	0 0	0	0
STM32F745IEK6	216	ARM Cortex-M7	512	320+ 16	0	UFBGA176	140	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0 6	5 1	3	3 4	0	4+4	0	2	1	0	1	0 1	1	1	0	0	0	0	2	4	0	1	0	1	0 0	0	0
STM32F745IGK6	216	ARM Cortex-M7	1024	220.	0	UFBGA176	140	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0 6	5 1	3	3 4	0	4+4	0	2	1	0	1	0 1	1	1	0	0	0	0	2	4	0	1	0	1	0 0	0	0
STM32F765VGT6	216	ARM Cortex-M7	1024	512+ 16	0	LQFP100	82	1.7	3.6	12	2	2 1	0	3	16	0	0	2	0	0 4	1	3	3 4	0	4+4	0	3	1	0	1	0 1	1	1	1	0	0	0	2	4	1	1	0	1	0 0	0	0
STM32F765VIT6	216		2048	512+ 16	0	LQFP100	82	1.7	3.6	12	2	2 1	0	3	16	0	0	2	0	0 4	1	3	3 4	0	4+4	0	3	1	0	1	0 1	1	1	1	0	0	0	2	4	1	1	0	1	0 0	0	0
		JOILON WIT		10					ш						ш																						لسنا	$\Box$		$\bot$						

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产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	无力 毛 三 寸 器 同分辨率 定时器	2位ADC转换单元	12位ADC通道	6位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	SPI	QUADSPI	I'S	高速10	U(S)ART	低功耗UART	CAN	SDIO	FSMC	FMC	从USB	全速USB OTG	高東IISB OTG	MDIOS	段式LCD	TH LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES AES	SHA	НМАС
STM32F765ZGT6	216	ARM Cortex-M7	1024	512+ 16	0	LQFP144	114	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0	6	1	3 4	. 0	) 4+4	4 0	3	1	0	1	0	1	1 1	1	0	0	0	2	4	1	1	0	1	0 0	0	0
STM32F765ZIT6	216	ARM Cortex-M7	2040	10	0	LQFP144	114	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0	6	1	3 4	. 0	4+4	4 0	3	1	0	1	0	1	1 1	1	0	0	0	2	4	1	1	0	1	0 0	0	0
STM32F765IGT6	216	ARM Cortex-M7	1024	512+ 16	0	LQFP176	140	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0	6	1	3 4	. 0	) 4+4	4 0	3	1	0	1	0	1	1 1	1	0	0	0	2	4	1	1	0	1	0 0	0	0
STM32F765IIT6	216	ARM Cortex-M7	2048	512+ 16	0	LQFP176	140	1.7	3.6	12	2	2	0	3	24	0	0	2	0	0	6	1	3 4	. 0	) 4+4	4 0	3	1	0	1	0	1	1 1	1	0	0	0	2	4	1	1	0	1	0 0	0	0
STM32F765IGK6	216	ARM Cortex-M7	1024	10	0	UFBGA176	140	1.7	3.6	12	2	2	0	3	24	0	0	2	0	0	6	1	3 4	. 0	) 4+4	4 0	3	1	0	1	0	1	1 1	1	0	0	0	2	4	1	1	0	1	0 0	0	0
STM32F765IIK6	216	ARM Cortex-M7	2048			UFBGA176	140	1.7	3.6	12	2	2	0	3	24	0	0	2	0	0	6	1	3 4	. 0	) 4+4	4 0	3	1	0	1	0	1	1 1	1	0	0	0	2	4	1	1	0	1	0 0	0	0
STM32F765BGT6	216	ARM Cortex-M7		512+ 16		LQFP208	168	1.7	3.6	12	2	2	0	3	24	0	0	2	0	0	6	1	3 4	. 0	) 4+4	4 0	3	1	0	1	0	1	1 1	1	0	0	0	2	4	1	1	0	1	0 0	0	0
STM32F765BIT6	216	ARM Cortex-M7	2048	512+ 16	0	LQFP208	168	1.7	3.6	12	2	2	0	3	24	0	0	2	0	0	6	1	3 4	. 0	4+4	4 0	3	1	0	1	0	1	1 1	1	0	0	0	2	4	1	1	0	1	0 0	0	0
STM32F765NGH6	216	ARM Cortex-M7	1024	512+ 16	0	TFBGA216	168	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0	6	1	3 4	. 0	4+4	4 0	3	1	0	1	0	1	1 1	1	0	0	0	2	4	1	1	0	1	0 0	0	0
STM32F765NIH6	216	ARM Cortex-M7	2048	512+ 16	0	TFBGA216	168	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0		-		3 4		) 4+4	4 0	3	1	0	1	0	1	1 1	1	0	0	0	2	4	1	1	0	1	0 0	0	0
																		S	TM32	F7x6	高级:	型 - 2	216 M	łz																						
STM32F746VET6	216	COLLEX-INI/	512	320+ 16	0	LQFP100	82	1.7	3.6	12	2	2 1	0	3	16	0	0	2	0	0	4	1	3 4	. 0	4+4	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1	0 0	0	0
STM32F746VGT6	216	ARM Cortex-M7	1024	320+ 16	0	LQFP100	82	1.7	3.6	12	2	2 1	0	3	16	0	0	2	0	0	4	1	3 4	. 0	4+4	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1	0 0	0	0
STM32F746VEH6	216	ARM Cortex-M7	512	320+ 16	0	TFBGA100	82	1.7	3.6	12	2	2 1	0	3	16	0	0	2	0	0	4	1	3 4	. 0	) 4+4	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1	0 0	0	0
STM32F746VGH6	216	ARM Cortex-M7	1024	320+ 16	0	TFBGA100	82	1.7	3.6	12	2	2	0	3	16	0	0	2	0	0	4	1	3 4	. 0	4+4	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1	0 0	0	0
STM32F746ZET6	216	ARM Cortex-M7	512	320+ 16	0	LQFP144	114	1.7	3.6	12	2	2	0	3	24	0	0	2	0	0	6	1	3 4	. 0	) 4+4	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1	0 0	0	0
STM32F746ZGT6	216	ARM Cortex-M7	1024	320+ 16	0	LQFP144	114	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0	6	1	3 4	. 0	4+4	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1	0 0	0	0
STM32F746ZEY6	216	ARM Cortex-M7	512	320+ 16	0	WLCSP143	114	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0	6	1	3 4	. 0	4+4	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1	0 0	0	0
STM32F746ZGY6	216	ARM Cortex-M7	1024	320+ 16	0	WLCSP143	114	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0	6	1	3 4	. 0	) 4+4	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1	0 0	0	0
STM32F746IET6	216	ARM Cortex-M7	512	320+ 16	0	LQFP176	140	1.7	3.6	12	2	2	0	3	24	0	0	2	0	0	6	1	3 4	. 0	) 4+4	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1	0 0	0	0
STM32F746IGT6	216	ARM Cortex-M7	1024	320+ 16	0	LQFP176	140	1.7	3.6	12	2	2	0	3	24	0	0	2	0	0	6	1	3 4	. 0	) 4+4	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1	0 0	0	0
STM32F746IEK6	216	ARM Cortex-M7	512	320+ 16	0	UFBGA176	140	1.7	3.6	12	2	2	0	3	24	0	0	2	0	0	6	1	3 4	. 0	) 4+4	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1	0 0	0	0

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产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	氏力毛定寸器	高分牌率定寸器	12位ADC長色に	16位ADC转换单元	16位ADC通道	12包DAC	比较器	放大器	SPI	QUADSPI	S.I.	12°C	U(S)AKI	但以料UAK!	CAN	SDIO	FSMC	FMC	从USB	全速USB OTG	高速IISB OTG	MDIOS	段式LCD	TFI LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPIMI	TRNG	DES/TDES AES	SHA	HMAC
STM32F746IGK6	216	ARM Cortex-M7	1024	320+ 16	0	UFBGA176	140	1.7	3.6	12	2	2	1 (	o (3	3 24	0	0	2	0	0	6	1	3	1 (	0 4+	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1 (	0 0	0	0
STM32F746BET6	216	ARM Cortex-M7	512	320+ 16	0	LQFP208	168	1.7	3.6	12	2	2	1 (	) 3	3 24	0	0	2	0	0	6	1	3	4 (	0 4+	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1 (	0 0	0	0
STM32F746BGT6	216	ARM Cortex-M7	1024	320+ 16	0	LQFP208	168	1.7	3.6	12	2	2	1 (	0 3	3 24	0	0	2	0	0	6	1	3	1 (	0 4+	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1 1	0 0	0	0
STM32F746NEH6	216	ARM Cortex-M7	512	320+ 16	0	TFBGA216	168	1.7	3.6	12	2	2	1 (	0 3	3 24	0	0	2	0	0	6	1	3	1	0 4+	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1 '	0 0	0	0
STM32F746NGH6	216	ARM Cortex-M7	1024	320+ 16	0	TFBGA216	168	1.7	3.6	12	2	2	1 (	) 3	3 24	0	0	2	0	0	6	1	3	1	0 4+	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1 '	0 0	0	0
STM32F756VGT6	216	ARM Cortex-M7	1024	320+ 16	0	LQFP100	82	1.7	3.6	12	2	2	1 (	0 3	3 10	0	0	2	0	0	4	1	3	4 (	0 4+	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1	1 1	1	1
STM32F756ZGT6	216	ARM Cortex-M7	1024	320+ 16	0	LQFP144	114	1.7	3.6	12	2	2	1 (	0 3	3 2	0	0	2	0	0	6	1	3	4 (	0 4+	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1	1 1	1	1
STM32F756ZGY6	216	ARM Cortex-M7	1024	320+ 16	0	WLCSP143	114	1.7	3.6	12	2	2	1 (	0 3	3 2	0	0	2	0	0	6	1	3	1 (	0 4+	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1 -	1 1	1	1
STM32F756IGT6	216	ARM Cortex-M7	1024	320+ 16	0	LQFP176	140	1.7	3.6	12	2	2	1 (	0 3	3 24	0	0	2	0	0	6	1	3	1 (	0 4+	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1 -	1 1	1	1
STM32F756IGK6	216	ARM Cortex-M7	1024	320+ 16	0	UFBGA176	140	1.7	3.6	12	2	2	1 (	0 3	3 24	0	0	2	0	0	6	1	3	1	0 4+	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1 .	1 1	1	1
STM32F756BGT6	216	ARM Cortex-M7	1024	220.	0	LQFP208	168	1.7	3.6	12	2	2	1 (	0 3	3 24	0	0	2	0	0	6	1	3	1	0 4+	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1 -	1 1	1	1
STM32F756NGH6	216	ARM Cortex-M7	1024	320+ 16	0	TFBGA216	168	1.7	3.6	12	2	2	1 (	0 3	3 24	0	0	2	0	0	6	1	3	4	0 4+	4 0	2	1	0	1	0	1	1 1	0	0	1	0	2	4	0	1	0	1 .	1 1	1	1
																		S	TM32	F7x7	高级	型 -	216 M	Hz																						
STM32F767VGT6	216	ARM Cortex-M7	1024	512+ 16	0	LQFP100	82	1.7	3.6	12	2	2	1 (	) 3	3 16	0	0	2	0	0	4	1	3	4 [·	4] 4+	4 0	3	2	0	1	0	1	1 1	1	0	1	0	2	4	1	1	0	1 (	0 0	0	0
STM32F767VIT6	216		2048	512+ 16	0	LQFP100	82	1.7	3.6	12	2	2	1 (	0 3	3 16	0	0	2	0	0	4	1	3	4 [·	4] 4+	4 0	3	2	0	1	0	1	1 1	1	0	1	0	2	4	1	1	0	1 (	0 0	0	0
STM32F767ZGT6	216	ARM Cortex-M7	1024	512+	0	LQFP144	114	1.7	3.6	12	2	2	1 (	0 3	3 24	0	0	2	0	0	6	1	3	4 [·	4] 4+	4 0	3	2	0	1	0	1	1 1	1	0	1	0	2	4	1	1	0	1 (	0 0	0	0
STM32F767ZIT6	216	ARM Cortex-M7	2048	-	0	LQFP144	114	1.7	3.6	12	2	2	1 (	) 3	3 24	0	0	2	0	0	6	1	3	4 [·	4] 4+	4 0	3	2	0	1	0	1	1 1	1	0	1	0	2	4	1	1	0	1 (	0 0	0	0
STM32F767IGT6	216	ARM		512	0	LQFP176	132	1.7	3.6	12	2	2	1 (	0 3	3 24	0	0	2	0	0	6	1	3	4 [·	4] 4+	4 0	3	2	0	1	0	1	1 1	1	0	1	0	2	4	1	1	0	1 1	0 0	0	0
STM32F767IIT6	216	Cortex-M7	2048	_	0	LQFP176	132	1.7	3.6	12	2	2	1 (	0 3	3 24	0	0	2	0	0	6	1	3	+	4] 4+	4 0	3	2	0	1	0	1	1 1	1	0	1	0	2	4	1	1	0	1	0 0	0	0
STM32F767IGK6	216	Cortex-M7	1024	512+		UFBGA176				12	2			0 3	+	+	0	2	0	0	6	$\dashv$	_		4] 4+			2	0	1	0	1	1 1	1	0	1	0	2	4	1	+	0	+	0 0		$\vdash$
STM32F767IIK6	216			512+		UFBGA176			3.6	12	2	2		0 3	+	+	0	2	0	0	6	-	+	4 [·	+		+	2	0	1	0		1 1	1	0	1	0	2	4		+	0	+	0 0	+	0
STM32F767BGT6		ARM		512+		LQFP208	159		3.6	12	2	2	+	0 3	+	+	0	2	0	0	6	$\dashv$	_	4 [				2	0	1	0	+	1 1	1	0	1	0	2	4		-	0	+	0 0	+	+
		Cortex-M7		16																					1	L													ш							Ш

# STM32 F7系列 – ARM® Cortex®-M7高性能MCU

产品型号	主频 (MHz)	<b>内</b> 核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	低功耗定时器	高分辨率定付器	12立ADC转换单元	15位ADC转换单元	Tb位ADC通道	12位DAC	比较器	放大器	SPI	QUADSPI	l <sup>2</sup> S	l²C	高速l <sup>2</sup> C	U(S)ART	低功耗UART	CAN	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	MDIOS	段式LCD	TET LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES AES	SHA	HIMAC
STM32F767BIT6	216			512+ 16		LQFP208	159	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 1	1	0	1	0	2	4	1	1	0	1 (	0 0	0	0
STM32F767NGH6	216			512+ 16		TFBGA216	159	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 1	1	0	1	0	2	4	1	1	0	1 (	0 0	0	0
STM32F767NIH6	216			512+ 16		TFBGA216	159	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 1	1	0	1	0	2	4	1	1	0	1 1	0 0	0	0
STM32F777VIT6	216			512+ 16		LQFP100	82	1.7	3.6	12	2	2	1	0	3 1	6 0	0	2	0	0	4	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 1	1	0	1	0	2	4	1	1	0	1	1 1	1	1
STM32F777ZIT6	216			512+ 16		LQFP144	114	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 1	1	0	1	0	2	4	1	1	0	1 .	1 1	1	1
STM32F777IIT6	216	ARM Cortex-M7	2048	512+ 16	0	LQFP176	132	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 1	1	0	1	0	2	4	1	1	0	1	1 1	1	1
STM32F777IIK6	216	ARM Cortex-M7	2048	512+ 16	0	UFBGA176	132	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 1	1	0	1	0	2	4	1	1	0	1	1 1	1	1
STM32F777BIT6	216	ARM Cortex-M7	2048	512+ 16	0	LQFP208	159	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 1	1	0	1	0	2	4	1	1	0	1 .	1 1	1	1
STM32F777NIH6	216	ARM Cortex-M7	2048	512+ 16	0	TFBGA216	159	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 1	1	0	1	0	2	4	1	1	0	1	1 1	1	1
			,				,											,	STM3	2F7x	8高级	- 埋소	216	VIHz																						
STM32F778AIY6	216	ARM Cortex-M7	2048	512+ 16	0	WLCSP180	129	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 (	1	0	1	1	2	4	1	1	0	1	1 1	1	1
																			STM3	2F7x	9高级	- 埋夕	216	VIHz																						
STM32F769AGY6	216	ARM Cortex-M7	1024	512+ 16	0	WLCSP180	129	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 (	1	0	1	1	2	4	1	1	0	1 (	0 0	0	0
STM32F769AIY6	216	ARM Cortex-M7	2048	512+ 16	0	WLCSP180	129	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 (	1	0	1	1	2	4	1	1	0	1 (	0 0	0	0
STM32F769BGT6	216	ARM Cortex-M7	1024	512+ 16	0	LQFP208	159	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 1	1	0	1	1	2	4	1	1	0	1 (	0 0	0	0
STM32F769BIT6	216	ARM Cortex-M7	2048	512+ 16	0	LQFP208	159	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 1	1	0	1	1	2	4	1	1	0	1 (	0 0	0	0
STM32F769IGT6	216	ARM Cortex-M7	1024	512+ 16	0	LQFP176	132	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 1	1	0	1	1	2	4	1	1	0	1 (	0 0	0	0
STM32F769IIT6	216	ARM Cortex-M7	2048	512+ 16	0	LQFP176	132	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 1	1	0	1	1	2	4	1	1	0	1 (	0 0	0	0
STM32F769NGH6	216	ARM Cortex-M7	1024	512+ 16	0	TFBGA216	159	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 1	1	0	1	1	2	4	1	1	0	1 (	0 0	0	0
STM32F769NIH6	216	ARM Cortex-M7	2048	512+ 16	0	TFBGA216	159	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 1	1	0	1	1	2	4	1	1	0	1 /	0 0	0	0
STM32F779AIY6	216	ARM Cortex-M7	2048	512+ 16	0	WLCSP180	129	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	. 0	1	0	1	1 (	1	0	1	1	2	4	1	1	0	1	1 1	1	1
STM32F779BIT6	216	ARM Cortex-M7	2048	512+	0	LQFP208	159	1.7	3.6	12	2	2	1	0	3 2	4 0	0	2	0	0	6	1	3	4	[4]	4+4	0	3 2	! 0	1	0	1	1 1	1	0	1	1	2	4	1	1	0	1	1 1	1	1

# STM32 F7系列 – ARM® Cortex®-M7高性能MCU

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电几空制定寸器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	SPI	QUADSPI		I <sup>2</sup> C	高速r°C	们以料UAKI	CAN	SDIO	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	以太 <sub>网</sub>	MDIOC	受式LCD	TFI LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	AFS	DES/TDES	SHA	
STM32F779IIT6	216	ARM Cortex-M7	2048	512+ 16	0	LQFP176	132	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0	6	1	3	4 [	[4] 4+	-4 0	3	2	0	1	0	1	1	1		0	1	1	2	4	1	1	0	1	1 1	1 1	1 1	
STM32F779NIH6	216	ARM Cortex-M7	2048	512+ 16	0	TFBGA216	159	1.7	3.6	12	2	2 1	0	3	24	0	0	2	0	0	6	1	3	4 [	[4] 4+	-4 0	3	2	0	1	0	1	1	1 '		0	1	1	2	4	1	1	0	1	1 1	1 1	1 1	

# STM32 H7系列 – ARM® Cortex®-M7超高性能MCU

产 品型 号	主频 (MHz)	<b>内</b> 核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	氏力毛定寸器   高分辨率   5日 器	高 <b>分</b> 牌 图 已 寸 居	12立ADC转换单元	13位ADC報授单元	16位ADC通道	12位DAC	比较器	放大器	SPI	QUADSPI	l <sup>2</sup> S	l²C	高速元C	U(S)ART	低功耗UART	GAN	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	以太网	MDIOS	段式LCD	TH LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCIMI	SWPMI	TRNG	AES	DES/TDES	HMAC
																		:	тмз:	2H7x	3产品	3线-	216	MHz																							
STM32H743VIT6	400	ARM Cortex-M7	2048	1060	0	LQFP100	82	1.71	3.6	18	2	2	5		0 [2	0] 3	20	2	2	2	6	1	3	4	[4]	1+4	1	2 2	2 0	1	0	1	1	1	1	0	1	0	4	4	1	1	1	1	0 1	0 0	0
STM32H743ZIT6	400	ARM Cortex-M7	2048	1060	0	LQFP144	114	1.62	3.6	18	2	2	5	1	0 [2	0] 3	20	2	2	2	6	1	3	4	[4]	1+4	1	2 2	2 0	1	0	1	1	1	1	0	1	0	4	4	1	1	1	1	0 '	0 0	0
STM32H743IIT6	400	ARM Cortex-M7	2048	1060	0	LQFP176	140	1.62	3.6	18	2	2	5	1	0 [2	0] 3	20	2	2	2	6	1	3	4	[4]	1+4	1	2 2	2 0	1	0	1	1	1	1	0	1	0	4	4	1	1	1	1	0 (	0 (	0
STM32H743BIT6	400	ARM Cortex-M7	2048	1060	0	LQFP208	168	1.62	3.6	18	2	2	5	1	0 [2	0] 3	20	2	2	2	6	1	3	4	[4]	1+4	1	2 2	2 0	1	0	1	1	1	1	0	1	0	4	4	1	1	1	1	0 '	0 0	0
STM32H743XIH6	400	ARM Cortex-M7	2048	1060	0	TFBGA265	168	1.62	3.6	18	2	2	5	1	0 [2	0] 3	20	2	2	2	6	1	3	4	[4]	1+4	1	2 2	2 0	1	0	1	1	1	1	0	1	0	4	4	1	1	1	1	0	0 (	0
STM32H753VIT6		ΛRM	0040			LQFP100	82	1.71	3.6	18	2	2	5	1	0 [2	0] 3	20	2	2	2	6	1	3	4	[4]	1+4	1	2 2	2 0	1	0	1	1	1	1	0	1	0	4	4	1	1	1	1	1	1 1	. 1
STM32H753ZIT6	400	ARM Cortex-M7	2048	1060	0	LQFP144	114	1.62	3.6	18	2	2	5	1	0 [2	:0] 3	20	2	2	2	6	1	3	4	[4]	1+4	1	2 2	2 0	1	0	1	1	1	1	0	1	0	4	4	1	1	1	1	1	1 .	. 1
STM32H753IIT6	400	ARM Cortex-M7	2048	1060	0	LQFP176	140	1.62	3.6	18	2	2	5	1	0 [2	:0] 3	20	2	2	2	6	1	3	4	[4]	1+4	1	2 2	2 0	1	0	1	1	1	1	0	1	0	4	4	1	1	1	1	1 .	1 1	. 1
STM32H753BIT6		ADM	0040			LQFP208	168	1.62	3.6	18	2	2	5	1	0 [2	0] 3	20	2	2	2	6	1	3	4	[4]	1+4	1	2 2	2 0	1	0	1	1	1	1	0	1	0	4	4	1	1	1	1	1 .	1 1	. 1
STM32H753XIH6	400	ARM Cortex-M7	2048	1060	0	TFBGA265	168	1.62	3.6	18	2	2	5	1	0 [2	0] 3	20	2	2	2	6	1	3	4	[4]	1+4	1	2 2	2 0	1	0	1	1	1	1	0	1	0	4	4	1	1	1	1	1	1 1	1

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	低力毛定寸器 高分數率 50mm器	5.4 A E H	12立ADC专英单三	16位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	SPI	QUADSPI	-2°	12°C	高速l <sup>2</sup> C	U(S)ART	低功耗UART	SDIO	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	以太网 MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	AES	DES/TDES	НМАС
								压	压			器	計	¥   5	Ē /	-   羌																"	٠,													
														Ţ					STM3	2L0x	1入[	]型·	- 32 M	Iz																						
STM32L011D3P6	32	ARM Cortex-M0+	8	2	512	TSS0P14	11	1.65	3.6	2	0	0	1 (	) .	1 -	1 0	0	0	2	0	1	0	0	1 [	[1]	1	1 (	0 0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L011D4P6	32	ARM Cortex-M0+	16	2	512	TSS0P14	11	1.65	3.6	2	0	0	1 (	)   .	1 -	1 0	0	0	2	0	1	0	0	1 [	[1]	1	1	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L011F3P6	32	ARM Cortex-M0+	8	2	512	TSS0P20	16	1.65	3.6	2	0	0	1 (	) .	1	0	0	0	2	0	1	0	0	1 [	[1]	1	1	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L011F3U6	32	ARM Cortex-M0+	8	2	512	UFQFPN20	16	1.65	3.6	2	0	0	1 (	) .	1	7 0	0	0	2	0	1	0	0	1 [	[1]	1	1 (	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L011F4P6	32	ARM Cortex-M0+	16	2	512	TSS0P20	16	1.65	3.6	2	0	0	1 (	)	1	9 0	0	0	2	0	1	0	0	1 [	1]	1	1 (	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L011F4U6	32	ARM	16	2	512	UFQFPN20	16	1.65	3.6	2	0	0	1 (	)	1	7 0	0	0	2	0	1	0	0	1 [	[1]	1	1	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L011E3Y6	32	Cortex-M0+	8	2	512	WLCSP25	21	1.65	3.6	2	0	0	1 (	) .	1 1	0 0	0	0	2	0	1	0	0	+		1	1	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L011E4Y6		Cortex-M0+	16	2	512		$\vdash$	1.65		2	0		1 (	) .	+	0 0	0	+	2	0	1	0	+	+	[1]	1		0 0	-	0	0	0		0 0		0	0	0	0	0	0	0	0	0 0	+	0
STM32L011G3U6		Cortex-M0+	8	2	512	UFQFPN28		1.65		2	0		1 (	) .	+	0 0	0	+	2	0	1	0		+	+	1		0 0	-	0	0	0	_	0 0		0	0	0	0	0	0	0	0	0 0	+	+
STM32L011G4U6	Н	Cortex-M0+	16	2	512			1.65		2	0			) .	+	0 0	0	+	2	0	1	0		+		1		0 0		0	0	0		0 0		0	0	0	0	0	0	0	-	0 0	+	+
		Cortex-M0+	8	2	512	LQFP32	$\vdash$	1.65		2	0			) .	+	+	0	+	2	0	1	0		+		1		0 0		0	0	0		0 0	0	0	0	0	0	0	0	0	$\dashv$	0 0	+	+
STM32L011K3T6	$\vdash$	Cortex-M0+	_				$\vdash$			$\dashv$	_	_		+	+	+	+	+			'		+	+		+								+							-	$\dashv$	_	_		+
STM32L011K3U6	$\vdash$	Cortex-M0+	8	2	512	UFQFPN32		1.65		2	0	+	1 (	+	+	0 0	0	+	2	0	1	0	_	+	-	1	_	0 0	-	0	0	0		0 0	0	0	0	0	0	0	0	0	$\dashv$	0 0	+	+
STM32L011K4T6	32	Cortex-M0+	16	2	512	LQFP32	26	1.65	3.6	2	0	0	1 (	) .	1 1	0 0	0	0	2	0	1	0	0	1 [	[1]	1	1	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L011K4U6	32	ARM Cortex-M0+	16	2	512	UFQFPN32	28	1.65	3.6	2	0	0	1 (	) '	1 1	0 0	0	0	2	0	1	0	0	1 [	[1]	1	1	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L021D4P6	32	ARM Cortex-M0+	16	2	512	TSS0P14	11	1.65	3.6	2	0	0	1 (	)	1	1 0	0	0	2	0	1	0	0	1 [	[1]	1	1	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	1 0	0 0	0
STM32L021F4P6	32	ARM Cortex-M0+	16	2	512	TSS0P20	16	1.65	3.6	2	0	0	1 (	)	1 !	0	0	0	2	0	1	0	0	1 [	[1]	1	1 (	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	1 0	0 0	0
STM32L021F4U6	32	ARM Cortex-M0+	16	2	512	UFQFPN20	16	1.65	3.6	2	0	0	1 (	) .	1	7 0	0	0	2	0	1	0	0	1 [	[1]	1	1	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	1 0	0 0	0
STM32L021G4U6	32	ARM Cortex-M0+	16	2	512	UFQFPN28	24	1.65	3.6	2	0	0	1 (	) .	1 1	0 0	0	0	2	0	1	0	0	1 [	[1]	1	1	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	1 0	0 0	0
STM32L021K4T6	32	ARM Cortex-M0+	16	2	512	LQFP32	26	1.65	3.6	2	0	0	1 (	) .	1 1	0 0	0	0	2	0	1	0	0	1 [	[1]	1	1	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	1 0	0 0	0
STM32L021K4U6	32	ARM Cortex-M0+	16	2	512	UFQFPN32	28	1.65	3.6	2	0	0	1 (	)	1 1	0 0	0	0	2	0	1	0	0	1 [	[1]	1	1	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	1 0	0 0	0
STM32L031F4P6	32	ARM Cortex-M0+	16	8	1024	TSS0P20	15	1.65	3.6	3	0	0	1 (	) .	1 1	0 0	0	0	2	0	1	0	0	1 [	[1]	1	1	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0 0	0

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12妇DAC	比较器	放大器	SPI .	Ollandin		高速r°C	U(S)ART	低功耗UART	CAN	SDIO	FEMO	从USB	全速USB OTG	高速USB OTG	以太网	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DOMI	SWPMI	AES	DES/TDES	SHA	HMAC
STM32L031F6P6	32	ARM Cortex-M0+	32	8	1024	TSS0P20	15	1.65	3.6	3	0	0 1	0	1	10	0	0	0	2	0	1 (	) (	0 1	[1]	1	1	0	0 0	) 0	0	0	0	0	0	0	0	0	0	0	0 0	0 (	0 (	0 0	0	0	0
STM32L031E4Y6	32	ARM Cortex-M0+	16	8	1024	WLCSP25	20	1.65	3.6	3	0	0 1	0	1	10	0	0	0	2	0	1 (	) (	0 1	[1]	1	1	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	0 (	0 (	0 0	0	0	0
STM32L031E6Y6	32	ARM Cortex-M0+	32	8	1024	WLCSP25	20	1.65	3.6	3	0	0 1	0	1	10	0	0	0	2	0	1 (	) (	0 1	[1]	1	1	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	0 (	0 (	0 0	0	0	0
STM32L031G4U6	32	ARM Cortex-M0+	16	8	1024	UFQFPN28	21	1.65	3.6	3	0	0 1	0	1	10	0	0	0	2	0	1 (	) (	0 1	[1]	1	1	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	0 (	0 (	0 0	0	0	0
STM32L031G6U6	32	ARM Cortex-M0+	32	8	1024	UFQFPN28	21	1.65	3.6	3	0	0 1	0	1	10	0	0	0	2	0	1 (	0 (	0 1	[1]	1	1	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	0 (	0 (	0 0	0	0	0
STM32L031K4T6	32	ARM Cortex-M0+	16	8	1024	LQFP32	25	1.65	3.6	3	0	0 1	0	1	10	0	0	0	2	0	1 (	0 (	0 1	[1]	1	1	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	D 1	0 (	0 0	0	0	0
STM32L031K4U6	32	ARM Cortex-M0+	16	8	1024	UFQFPN32	27	1.65	3.6	3	0	0 1	0	1	10	0	0	0	2	0	1 (	0 (	0 1	[1]	1	1	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	0 (	0 (	0 0	0	0	0
STM32L031K6T6	32	ARM Cortex-M0+	32	8	1024	LQFP32	25	1.65	3.6	3	0	0 1	0	1	10	0	0	0	2	0	1 (	0	0 1	[1]	1	1	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	0 (	0 (	0 0	0	0	0
STM32L031K6U6	32	ARM Cortex-M0+	32	8	1024	UFQFPN32	27	1.65	3.6	3	0	0 1	0	1	10	0	0	0	2	0	1 (	0 (	0 1	[1]	1	1	0	0 0	0	0	0	0	0	0	0	0	0	0	0	0 0	) I	0 (	0 0	0	0	0
STM32L031C6T6	32	ARM Cortex-M0+	32	8	1024	LQFP48	38	1.65	3.6	3	0	0 1	0	1	10	0	0	0	2	0	1 (	) (	0 1	[1]	1	1	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	) (	0 (	0 0	0	0	0
STM32L041E6Y6	32	ARM Cortex-M0+	32	8	1024	WLCSP25	20	1.65	3.6	3	0	0 1	0	1	10	0	0	0	2	0	1 (	) (	0 1	[1]	1	1	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	0 (	0 (	) 1	0	0	0
STM32L041F6P6	32	ARM Cortex-M0+	32	8	1024	TSS0P20	15	1.65	3.6	3	0	0 1	0	1	10	0	0	0	2	0	1 (	) (	0 1	[1]	1	1	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	) I	0 (	) 1	0	0	0
STM32L041G6U6	32	ARM Cortex-M0+	32	8	1024	UFQFPN28	21	1.65	3.6	3	0	0 1	0	1	10	0	0	0	2	0	1 (	0 (	0 1	[1]	1	1	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	D 1	0 (	) 1	0	0	0
STM32L041K6T6	32	ARM Cortex-M0+	32	8	1024	LQFP32	25	1.65	3.6	3	0	0 1	0	1	10	0	0	0	2	0	1 (	0 (	0 1	[1]	1	1	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	D 1	0 (	) 1	0	0	0
STM32L041K6U6	32	ARM Cortex-M0+	32	8	1024	UFQFPN32	27	1.65	3.6	3	0	0 1	0	1	10	0	0	0	2	0	1 (	0 (	0 1	[1]	1	1	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	D 1	0 (	) 1	0	0	0
STM32L041C6T6	32	ARM Cortex-M0+	32	8	1024	LQFP48	38	1.65	3.6	3	0	0 1	0	1	10	0	0	0	2	0	1 (	) (	0 1	[1]	1	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0	0 0	) (	0 (	) 1	0	0	0
STM32L051K6T6	32	ARM Cortex-M0+	32	8	2048	LQFP32	25	1.65	3.6	4	0	0 1	0	1	10	0	0	0	2	0	1 (	) (	0 1	[1]	2	0	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	0 (	0 (	0	0	0	0
STM32L051K6U6	32	ARM Cortex-M0+	32	8	2048	UFQFPN32	27	1.65	3.6	4	0	0 1	0	1	10	0	0	0	2	0	1 (	) (	0 1	[1]	2	0	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	0 (	0 (	0	0	0	0
STM32L051K8T6	32	ARM Cortex-M0+	64	8	2048	LQFP32	25	1.65	3.6	4	0	0 1	0	1	10	0	0	0	2	0	1 (	) (	0 1	[1]	2	0	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	0 (	0 (	0	0	0	0
STM32L051K8U6	32	ARM Cortex-M0+	64	8	2048	UFQFPN32	27	1.65	3.6	4	0	0 1	0	1	10	0	0	0	2	0	1 (	) (	0 1	[1]	2	0	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	0 (	0 (	0	0	0	0
STM32L051T6Y6	32	ARM Cortex-M0+	32	8	2048	WLCSP36	29	1.65	3.6	4	0	0 1	0	1	10	0	0	0	2	0	1 (	0 (	0 2	[2]	2	1	0	0 (	) (	0	0	0	0	0	0	0	0	0	0	0 0	0 (	0 (	0 0	0	0	0

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12位DAC	比较:器	放大器	SDI COMPOST	OHADSPI	ကို ငြိ	高速rc	U(S)ART	低功耗UART	CAN	SDIO	ESMC	EMC NUSB	全速USB OTG	高速USB OTG	以太网	MDIOS	段式LCD	TFI LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES	SHA	НМАС
STM32L051T8Y6	32	ARM Cortex-M0+	64	8	2048	WLCSP36	29	1.65	3.6	4	0	0 1	0	1	10	0	0	0	2	0	1 (	0 (	0 2	[2]	2	1	0	0 (	) (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L051C6T6	32	ARM Cortex-M0+	32	8	2048	LQFP48	37	1.65	3.6	4	0	0 1	0	1	10	0	0	0	2	0	2 (	0 -	1 2	[2]	2	1	0	0 (	0 (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L051C8T6	32	ARM Cortex-M0+	64	8	2048	LQFP48	37	1.65	3.6	4	0	0 1	0	1	10	0	0	0	2	0	2 (	0 -	1 2	[2]	2	1	0	0 (	) (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L051R6T6	32	ARM Cortex-M0+	32	8	2048	LQFP64	51	1.65	3.6	4	0	0 1	0	1	16	0	0	0	2	0	2 (	0 -	1 2	[2]	2	1	0	0 (	) (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L051R6H6	32	ARM Cortex-M0+	32	8	2048	TFBGA64	51	1.65	3.6	4	0	0 1	0	1	16	0	0	0	2	0	2 (	0 -	1 2	[2]	2	1	0	0 (	) (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L051R8T6	32	ARM Cortex-M0+	64	8	2048	LQFP64	51	1.65	3.6	4	0	0 1	0	1	16	0	0	0	2	0	2 (	0 -	1 2	[2]	2	1	0	0 (	0 (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L051R8H6	32	ARM Cortex-M0+	64	8	2048	TFBGA64	51	1.65	3.6	4	0	0 1	0	1	16	0	0	0	2	0	2 (	0 .	1 2	[2]	2	1	0	0 (	0 (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L071K8U6	32	ARM Cortex-M0+	64	20	3072	UFQFPN32	23	1.65	3.6	6	0	0 1	0	1	10	0	0	0	2	0	1 (	0 (	0 2	[2]	3	1	0	0	0 (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L071KBT6	32	ARM Cortex-M0+	128	20	6144	LQFP32	25	1.65	3.6	6	0	0 1	0	1	10	0	0	0	2	0	1 (	0 (	0 2	[2]	3	1	0	0 (	) (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L071KBU6	32	ARM Cortex-M0+	128	20	6144	UFQFPN32	23	1.65	3.6	6	0	0 1	0	1	10	0	0	0	2	0	1 (	0 (	0 2	[2]	3	1	0	0 (	0 (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L071KZT6	32	ARM Cortex-M0+	192	20	6144	LQFP32	25	1.65	3.6	6	0	0 1	0	1	10	0	0	0	2	0	1 (	0 (	0 2	[2]	3	1	0	0 (	) (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L071KZU6	32	ARM Cortex-M0+	192	20	6144	UFQFPN32	23	1.65	3.6	6	0	0 1	0	1	10	0	0	0	2	0	1 (	0 (	0 2	[2]	3	1	0	0 (	) (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L071C8T6	32	ARM Cortex-M0+	64	20	3072	LQFP48	37	1.65	3.6	6	0	0 1	0	1	13	0	0	0	2	0	2 (	0 -	1 3	[3]	4	1	0	0 (	0 (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L071CBT6	32	ARM Cortex-M0+	128	20	6144	LQFP48	37	1.65	3.6	6	0	0 1	0	1	13	0	0	0	2	0	2 (	0 -	1 3	[3]	4	1	0	0 (	0 (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L071CBY6	32	ARM Cortex-M0+	128	20	6144	WLCSP49	40	1.65	3.6	6	0	0 1	0	1	13	0	0	0	2	0	2 (	0 -	1 3	[3]	4	1	0	0 (	0 (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L071CZT6	32	ARM Cortex-M0+	192	20	6144	LQFP48	37	1.65	3.6	6	0	0 1	0	1	13	0	0	0	2	0	2 (	0 -	1 3	[3]	4	1	0	0 (	) (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L071CZY6	32	ARM Cortex-M0+	192	20	6144	WLCSP49	40	1.65	3.6	6	0	0 1	0	1	13	0	0	0	2	0	2 (	0 -	1 3	[3]	4	1	0	0 (	) (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L071RBT6	32	ARM Cortex-M0+	128	20	6144	LQFP64	51	1.65	3.6	6	0	0 1	0	1	16	0	0	0	2	0	2 (	0 -	1 3	[3]	4	1	0	0 (	) (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L071RBH6	32	ARM Cortex-M0+	128	20	6144	TFBGA64	51	1.65	3.6	6	0	0 1	0	1	16	0	0	0	2	0	2 (	0 -	1 3	[3]	4	1	0	0 (	) (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L071RZT6	32	ARM Cortex-M0+	192	20	6144	LQFP64	51	1.65	3.6	6	0	0 1	0	1	16	0	0	0	2	0	2 (	0 -	1 3	[3]	4	1	0	0 (	) (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0
STM32L071RZH6	32	ARM Cortex-M0+	192	20	6144	TFBGA64	51	1.65	3.6	6	0	0 1	0	1	16	0	0	0	2	0 :	2 (	0 -	1 3	[3]	4	1	0	0 (	) (	0 0	0	0	0	0	0	0	0	0	0	0 (	0	0	0 0	0 0	0	0

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产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通 用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16立ADC专奂单元	16·立ADC角首	比较器	放大器	SPI	QUADSPI	l <sup>2</sup> S	l²C	高速l°C	U(S)ART	低功耗UART	CAN	FSMC	FINIC	从USB	全速USB OTG	高速USB OTG	以太网 MDIOS	段式LCD	TFI LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES AES	SHA	HMAC
STM32L071V8T6	32	ARM Cortex-M0+	64	20	3072	LQFP100	84	1.65	3.6	6	0	0 1	0	1	16	0	0 (	) 2	0	2	0	1	3	[3]	4	1 (	0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L071V8I6	32	ARM Cortex-M0+	64	20	3072	UFBGA100	84	1.65	3.6	6	0	0 1	0	1	16	0	0 (	) 2	0	2	0	1	3	[3]	4	1 (	0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L071VBT6	32	ARM Cortex-M0+	128	20	6144	LQFP100	84	1.65	3.6	6	0	0 1	0	1	16	0	0 (	) 2	0	2	0	1	3	[3]	4	1 (	0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L071VBI6	32	ARM Cortex-M0+	128	20	6144	UFBGA100	84	1.65	3.6	6	0	0 1	0	1	16	0	0 (	) 2	0	2	0	1	3	[3]	4	1 (	0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L071VZT6	32	ARM Cortex-M0+	192	20	6144	LQFP100	84	1.65	3.6	6	0	0 1	0	1	16	0	0 (	) 2	0	2	0	1	3	[3]	4	1 (	0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L071VZI6	32	ARM Cortex-M0+	192	20	6144	UFBGA100	84	1.65	3.6	6	0	0 1	0	1	16	0	0 (	) 2	0	2	0	1	3	[3]	4	1 (	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L081KZT6	32	ARM Cortex-M0+	192	20	6144	LQFP32	25	1.65	3.6	6	0	0 1	0	1	10	0	0 (	) 2	0	1	0	0	2	[2]	3	1 (	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	1 0	0	0
STM32L081KZU6	32	ARM Cortex-M0+	192	20	6144	UFQFPN32	23	1.65	3.6	6	0	0 1	0	1	10	0	0 (	) 2	0	1	0	0	2	[2]	3	1 (	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	1 0	0	0
STM32L081CBT6	32	ARM Cortex-M0+	128	20	6144	LQFP48	40	1.65	3.6	6	0	0 1	0	1	13	0	0 (	) 2	0	2	0	1	3	[3]	4	1 (	0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	1 0	0	0
STM32L081CZT6	32	ARM Cortex-M0+	192	20	6144	LQFP48	40	1.65	3.6	6	0	0 1	0	1	13	0	0 (	) 2	0	2	0	1	3	[3]	4	1 (	0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	1 0	0	0
																		STM	132L0	)x2 US	B型	- 32 I	ИHz																						
STM32L052K6T6	32	ARM Cortex-M0+	32	8	2048	LQFP32	25	1.65	3.6	4	0	0 1	0	1	10	0	0 1	1 2	0	1	0	0	1	[1]	2	0 (	0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L052K6U6	32	ARM Cortex-M0+	32	8	2048	UFQFPN32	27	1.65	3.6	4	0	0 1	0	1	10	0	0 1	1 2	0	1	0	0	1	[1]	2	0 (	0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L052K8T6	32	ARM Cortex-M0+	64	8	2048	LQFP32	25	1.65	3.6	4	0	0 1	0	1	10	0	0 1	1 2	0	1	0	0	1	[1]	2	0 (	0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L052K8U6	32	ARM Cortex-M0+	64	8	2048	UFQFPN32	27	1.65	3.6	4	0	0 1	0	1	10	0	0 1	1 2	0	1	0	0	1	[1]	2	0 (	0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L052T6Y6	32	ARM Cortex-M0+	32	8	2048	WLCSP36	29	1.65	3.6	4	0	0 1	0	1	10	0	0 1	1 2	0	1	0	0	2	[2]	2	1 (	0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L052T8Y6	32	ARM Cortex-M0+	64	8	2048	WLCSP36	29	1.65	3.6	4	0	0 1	0	1	10	0	0 1	1 2	0	1	0	1	2	[2]	2	1 (	0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L052C6T6	32	ARM Cortex-M0+	32	8	2048	LQFP48	37	1.65	3.6	4	0	0 1	0	1	10	0	0 1	1 2	0	2	0	1	2	[2]	2	1 (	0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L052C8T6	32	ARM Cortex-M0+	64	8	2048	LQFP48	37	1.65	3.6	4	0	0 1	0	1	10	0	0 1	1 2	0	2	0	1	2	[2]	2	1 (	0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L052R6T6	32	ARM Cortex-M0+	32	8	2048	LQFP64	51	1.65	3.6	4	0	0 1	0	1	16	0	0 1	1 2	0	2	0	1	2	[2]	2	1 (	0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L052R6H6	32	ARM Cortex-M0+	32	8	2048	TFBGA64	50	1.65	3.6	4	0	0 1	0	1	15	0	0 1	1 2	0	2	0	1	2	[2]	2	1 (	0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L052R8T6	32	ARM Cortex-M0+	64	8	2048	LQFP64	51	1.65	3.6	4	0	0 1	0	1	16	0	0 1	1 2	0	2	0	1	2	[2]	2	1 (	0	0	0	1	0	0	0 0	0	0	0	0	0	0	0	0	1	0 0	0	0

																																								_						_
产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通 用 IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	低力毛定寸器 1高分射率 1分时器	高子牌窓, E 寸器	12位ADC专奂自己	16位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	SPI	QUADSPI	I <sup>2</sup> S	_2°   ;	高速元C	U(S)ART	低功耗UART	CAN	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	从 MDIOS	段式LCD	TFI LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES AES	SHA	НМАС
STM32L052R8H6	32	ARM Cortex-M0+	64	8	2048	TFBGA64	50	1.65	3.6	4	0	0	1 (	) -	1 1	5 0	0	1	2	0	2	0	1 :	2 [	[2]	2	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L062K8T6	32	ARM Cortex-M0+	64	8	2048	LQFP32	25	1.65	3.6	4	0	0	1 (	) .	1 10	0	0	1	2	0	1	0	0	1 [	[1]	2	0	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	1 0	0	0
STM32L062K8U6	32	ARM Cortex-M0+	64	8	2048	UFQFPN32	27	1.65	3.6	4	0	0	1 (	) -	1 10	0	0	1	2	0	1	0	0	1 [	[1]	2	0	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	1 0	0	0
STM32L062T8Y6	32	ARM Cortex-M0+	64	8	2048	WLCSP36	29	1.65	3.6	4	0	0	1 (	) .	1 1	0	0	1	2	0	1	0	0	2 [	[2]	2	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	1 0	0	0
STM32L072KBT6	32	ARM Cortex-M0+	128	20	6144	LQFP32	25	1.65	3.6	6	0	0	1 (	) .	1 1	0	0	2	2	0	1	0	0	2 [	[2]	3	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L072KBU6	32	ARM Cortex-M0+	128	20	6144	UFQFPN32	23	1.65	3.6	6	0	0	1 (	) .	1 1	0	0	2	2	0	1	0	0	2 [	[2]	3	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L072KZT6	32	ARM Cortex-M0+	192	20	6144	LQFP32	25	1.65	3.6	6	0	0	1 (	) .	1 1	0	0	2	2	0	1	0	0	2 [	[2]	3	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L072KZU6	32	ARM Cortex-M0+	192	20	6144	UFQFPN32	23	1.65	3.6	6	0	0	1 (	) .	1 1	0	0	2	2	0	1	0	0	2 [	[2]	3	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L072CBT6	32	ARM Cortex-M0+	128	20	6144	LQFP48	37	1.65	3.6	6	0	0	1 (	) -	1 10	0	0	2	2	0	2	0	1	3 [	[3]	4	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L072CBY6	32	ARM Cortex-M0+	128	20	6144	WLCSP49	40	1.65	3.6	6	0	0	1 (	) .	1 1:	3 0	0	2	2	0	2	0	1	3 [	[3]	4	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L072CZT6	32	ARM Cortex-M0+	192	20	6144	LQFP48	37	1.65	3.6	6	0	0	1 (	) .	1 10	0	0	2	2	0	2	0	1	3 [	[3]	4	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L072CZY6	32	ARM Cortex-M0+	192	20	6144	WLCSP49	40	1.65	3.6	6	0	0	1 (	) .	1 1:	3 0	0	2	2	0	2	0	1	3 [	[3]	4	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L072RBT6	32	ARM Cortex-M0+	128	20	6144	LQFP64	51	1.65	3.6	6	0	0	1 (	) .	1 1	6 0	0	2	2	0	2	0	1	3 [	[3]	4	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L072RBH6	32	ARM Cortex-M0+	128	20	6144	TFBGA64	50	1.65	3.6	6	0	0	1 (	) .	1 1:	5 0	0	2	2	0	2	0	1	3 [	[3]	4	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L072RZT6	32	ARM Cortex-M0+	192	20	6144	LQFP64	51	1.65	3.6	6	0	0	1 (	) .	1 1	6 0	0	2	2	0	2	0	1	3 [	[3]	4	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L072RZH6	32	ARM Cortex-M0+	192	20	6144	TFBGA64	50	1.65	3.6	6	0	0	1 (	) .	1 1	5 0	0	2	2	0	2	0	1	3 [	[3]	4	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L072RZI6	32	ARM Cortex-M0+	192	20	6144	UFBGA64	51	1.65	3.6	6	0	0	1 (	) .	1 10	6 0	0	2	2	0	2	0	1	3 [	[3]	4	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L072V8T6	32	ARM Cortex-M0+	64	20	3072	LQFP100	84	1.65	3.6	6	0	0	1 (	) .	1 10	6 0	0	2	2	0	2	0	1	3 [	[3]	4	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L072V8I6	32	ARM Cortex-M0+	64	20	3072	UFBGA100	84	1.65	3.6	6	0	0	1 (	) -	1 10	6 0	0	2	2	0	2	0	1	3 [	[3]	4	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L072VBT6	32	ARM Cortex-M0+	128	20	6144	LQFP100	84	1.65	3.6	6	0	0	1 (	) .	1 1	6 0	0	2	2	0	2	0	1	3 [	[3]	4	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0
STM32L072VBI6	32	ARM Cortex-M0+	128	20	6144	UFBGA100	84	1.65	3.6	6	0	0	1 (	) .	1 1	6 0	0	2	2	0	2	0	1	3 [	[3]	4	1	0 0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0 0	0	0

主频 (MHz)	内核	FLASH (KB)	ВАМ (КВ)	EEPROM (B)	封装	通 用 O	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16立ADC转换单元	18位ADC報首	比较器	放大器	SPI	QUADSPI	<b>1</b> % ₹	高速r°C	U(S)ART	低功耗UART	CAN	SDIO	FSMC	WOSB	全速USB OTG	高速USB OTG	以太网	MDIOS	段大CD	TFT LCD	DSI HOST	SAI	SPOIFRY	DCMI	SWPMI	TRNG	AES	DES/TDES	HMAC
32	ARM Cortex-M0+	192	20	6144	LQFP100	84	1.65	3.6	6	0	0 1	0	1	16	0	) 2	2	0	2	0	1 3	[3]	4	1	0	0	0 (	) 1	0	0	0	0	0	0	0	0	0 0	0	0	1	0	0	0 0
32	ARM Cortex-M0+	192	20	6144	UFBGA100	84	1.65	3.6	6	0	0 1	0	1	16	0	) 2	2	0	2	0	1 3	[3]	4	1	0	0	0 (	) 1	0	0	0	0	0	0	0	0	0 0	0	0	1	0	0	0 0
32	ARM Cortex-M0+	128	20	6144	LQFP32	25	1.65	3.6	6	0	0 1	0	1	10	0	) 2	2	0	1	0	0 2	[2]	] 3	1	0	0	0 (	) 1	0	0	0	0	0	0	0	0	0 0	0	0	1	1 (	0	0 0
32	ARM Cortex-M0+	128	20	6144	UFQFPN32	23	1.65	3.6	6	0	0 1	0	1	10	0	) 2	2	0	1	0	0 2	[2]	] 3	1	0	0	0 (	) 1	0	0	0	0	0	0	0	0	0 0	0	0	1	1 (	0	0 0
32	ARM Cortex-M0+	192	20	6144	LQFP32	25	1.65	3.6	6	0	0 1	0	1	10	0	) 2	2	0	1	0	0 2	[2]	] 3	1	0	0	0 (	) 1	0	0	0	0	0	0	0	0	0 0	0	0	1	1 (	0	0 0
32	ARM Cortex-M0+	192	20	6144	UFQFPN32	23	1.65	3.6	6	0	0 1	0	1	10	0	) 2	2	0	1	0	0 2	[2]	] 3	1	0	0	0 (	) 1	0	0	0	0	0	0	0	0	0 0	0	0	1	1	0	0 0
32	ARM Cortex-M0+	192	20	6144	WLCSP49	40	1.65	3.6	6	0	0 1	0	1	13	0	0 2	2	0	2	0	1 3	[3]	] 4	1	0	0	0 (	) 1	0	0	0	0	0	0	0	0	0 0	0	0	1	1	0	0 0
																STM	32L0x	3 USE	& LC	D功能	能型 - :	2 MF	łz																				
32	ARM Cortex-M0+	32	8	2048	LQFP48	37	1.65	3.6	4	0	0 1	0	1	10	0	) 1	2	0	2	0	1 2	[2]	2	1	0	0	0 (	) 1	0	0	0	0 4	x18	0	0	0	0 0	0	0	1	0 (	0	0 0
32	ARM Cortex-M0+	64	8	2048	LQFP48	37	1.65	3.6	4	0	0 1	0	1	10	0	) 1	2	0	2	0	1 2	[2]	2	1	0	0	0 (	) 1	0	0	0	0 4	x18	0	0	0	0 0	0	0	1	0	0	0 0
32	ARM Cortex-M0+	32	8	2048	LQFP64	51	1.65	3.6	4	0	0 1	0	1	16	0	) 1	2	0	2	0	1 2	[2]	2	1	0	0	0 (	) 1	0	0	0			0	0	0	0 0	0	0	1	0	0	0 0
32	ARM Cortex-M0+	32	8	2048	TFBGA64	50	1.65	3.6	4	0	0 1	0	1	15	0	) 1	2	0	2	0	1 2	[2]	2	1	0	0	0 (	) 1	0	0	0			0	0	0	0 0	0	0	1	0	0	0 0
32	ARM Cortex-M0+	64	8	2048	LQFP64	51	1.65	3.6	4	0	0 1	0	1	16	0	) 1	2	0	2	0	1 2	[2]	] 2	1	0	0	0 (	) 1	0	0	0			0	0	0	0 0	0	0	1	0	0	0 0
32	ARM Cortex-M0+	64	8	2048	TFBGA64	50	1.65	3.6	4	0	0 1	0	1	15	0	) 1	2	0	2	0	1 2	[2]	] 2	1	0	0	0 (	) 1	0	0	0			0	0	0	0 0	0	0	1	0	0	0 0
32	ARM Cortex-M0+	64	8	2048	LQFP48	37	1.65	3.6	4	0	0 1	0	1	10	0	) 1	2	0	2	0	1 2	[2]	] 2	1	0	0	0 (	) 1	0	0	0	0 4	x18	0	0	0	0 0	0	0	1	1	0	0 0
32	ARM Cortex-M0+	64	8	2048	LQFP64	51	1.65	3.6	4	0	0 1	0	1	16	0	0 1	2	0	2	0	1 2	[2]	2	1	0	0	0 (	) 1	0	0	0			0	0	0	0 0	0	0	1	1	0	0 0
32	ARM Cortex-M0+	128	20	6144	LQFP48	37	1.65	3.6	6	0	0 1	0	1	10	0	) 2	2	0	2	0	1 3	[3]	4	1	0	0	0 (	) 1	0	0	0	0 4	x18	0	0	0	0 0	0	0	1	0	0	0 0
32	ARM Cortex-M0+	192	20	6144	LQFP48	37	1.65	3.6	6	0	0 1	0	1	10	0	) 2	2	0	2	0	1 3	[3]	4	1	0	0	0 (	) 1	0	0	0	0 4	x18	0	0	0	0 0	0	0	1	0	0	0 0
32	ARM	128	20	6144	LQFP64	51	1.65	3.6	6	0	0 1	0	1	16	0	) 2	2	0	2	0	1 3	[3]	] 4	1	0	0	0 (	) 1	0	0	0	0 8	x28/ x32	0	0	0	0 0	0	0	1	0	0	0 0
32	ARM Cortex-M0+	128	20	6144	TFBGA64	50	1.65	3.6	6	0	0 1	0	1	15	0	) 2	2	0	2	0	1 3	[3]	] 4	1	0	0	0 (	) 1	0	0	0			0	0	0	0 0	0	0	1	0	0	0 0
32	ARM	192	20	6144	LQFP64	51	1.65	3.6	6	0	0 1	0	1	16	0	) 2	2	0	2	0	1 3	[3]	] 4	1	0	0	0 (	) 1	0	0	0	0 8	x28/	0	0	0	0 0	0	0	1	0	0	0 0
32	ARM Cortex-M0+	192	20	6144	TFBGA64	50	1.65	3.6	6	0	0 1	0	1	15	0	) 2	2	0	2	0	1 3	[3]	1 4	1	0	0	0 (	) 1	0	0	0	n 8	v28/	0	0	0	0 0	0	0	1	0	0	0 0
3: 3: 3: 3: 3: 3: 3: 3: 3: 3: 3: 3: 3: 3		ARM Cortex-MO+ ARM CO	Remarks   Rema	ARM Cortex-MO+ 192 20  ARM 192 20  ARM 20 20	ARM   192   20   6144	ARM	ARM   192   20   6144   LQFP100   84	2 ARM (192 20 6144 LOFP100 84 1.65 (2 ARM COrtex-MO+ 192 20 6144 LOFP100 84 1.65 1.65 (2 ARM CORTEX-MO+ 128 20 6144 LOFP132 25 1.65 (2 ARM CORTEX-MO+ 128 20 6144 LOFP132 23 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP132 23 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP132 23 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP132 23 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP132 23 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP132 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP132 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP132 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP132 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP132 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 37 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 50 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 50 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 50 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 50 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 50 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 50 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 51 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 51 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 51 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 51 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 51 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 51 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 51 1.65 (2 ARM CORTEX-MO+ 192 20 6144 LOFP134 51 1.65 (2 ARM CORTEX-MO+ 192 20 61	ARM   192   20   6144   LQFP100   84   1.65   3.6	ARM   192   20   6144   LQFP100   84   1.65   3.6   6   6	ARM   192   20   6144   LOFP100   84   1.65   3.6   6   0	2	2 CARM COrtex-MO+ 192 20 6144 LOFP100 84 1.65 3.6 6 0 0 0 1 0 1 0 0 0 0 0 0 0 0 0 0 0 0	2 ARM Cortex-MO+ 192 20 6144 LOFP100 84 1.65 3.6 6 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 192 20 6144 UFBGA100 84 1.65 3.6 6 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 128 20 6144 LOFP32 25 1.65 3.6 6 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 128 20 6144 LOFP32 25 1.65 3.6 6 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 128 20 6144 LOFP32 25 1.65 3.6 6 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 192 20 6144 LOFP32 25 1.65 3.6 6 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 192 20 6144 UFOFPN32 23 1.65 3.6 6 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 192 20 6144 UFOFPN32 23 1.65 3.6 6 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 192 20 6144 UFOFPN32 10 1.65 3.6 6 0 0 0 1 1 0 1 2 CARM COTTEX-MO+ 32 8 2048 LOFP48 37 1.65 3.6 4 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 64 8 2048 LOFP48 37 1.65 3.6 4 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 64 8 2048 LOFP64 51 1.65 3.6 4 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 64 8 2048 LOFP64 51 1.65 3.6 4 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 64 8 2048 LOFP64 51 1.65 3.6 4 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 64 8 2048 LOFP64 51 1.65 3.6 4 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 64 8 2048 LOFP64 51 1.65 3.6 4 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 64 8 2048 LOFP64 51 1.65 3.6 4 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 64 8 2048 LOFP64 51 1.65 3.6 4 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 64 8 2048 LOFP64 51 1.65 3.6 6 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 64 8 2048 LOFP64 51 1.65 3.6 6 0 0 0 1 0 1 0 1 2 CARM COTTEX-MO+ 64 8 2048 LOFP64 51 1.65 3.6 6 0 0 0 1 1 0 1 2 CARM COTTEX-MO+ 128 20 6144 LOFP64 51 1.65 3.6 6 0 0 0 1 1 0 1 2 CARM COTTEX-MO+ 128 20 6144 LOFP64 51 1.65 3.6 6 0 0 0 1 1 0 1 3 COTTEX-MO+ 128 20 6144 LOFP64 51 1.65 3.6 6 0 0 0 1 1 0 1 3 COTTEX-MO+ 128 20 6144 LOFP64 51 1.65 3.6 6 0 0 0 1 1 0 1 3 COTTEX-MO+ 128 20 6144 LOFP64 51 1.65 3.6 6 0 0 0 1 1 0 1 3 COTTEX-MO+ 128 20 6144 LOFP64 51 1.65 3.6 6 0 0 0 1 1 0 1 3 COTTEX-MO+ 128 20 6144 LOFP64 51 1.65 3.6 6 0 0 0 1 1 0 1 1 3 COTTEX-MO+ 128 20 6144 LOFP64 51 1.65 3.6 6 0 0 0 1 1 0 1 1 3 COTTEX-MO+ 128 20 6144 LOFP64 51 1.65 3.6 6 0 0 0 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1	ARM Cortex-MO+         192         20         6144         LOFP100         84         1.65         3.6         6         0         0         1         0         1         16           2 Cortex-MO+         192         20         6144         UFBGA100         84         1.65         3.6         6         0         0         1         0         1         16           2 CARM CORTEX-MO+         128         20         6144         LOFP32         25         1.65         3.6         6         0         0         1         0         1         10           2 CARM CORTEX-MO+         128         20         6144         LOFP32         25         1.65         3.6         6         0         0         1         0         1         10           2 CORTEX-MO+         192         20         6144         UFOFPN32         23         1.65         3.6         6         0         0         1         0         1         10           2 CARM CORTEX-MO+         192         20         6144         UFOFPN32         23         1.65         3.6         4         0         0         1         0         1         10	2 ARM	ARM   Cortex-MO+   192   20   6144   LOFP100   84   1.65   3.6   6   0   0   1   0   1   16   0   0   0   2   2   2   ARM   128   20   6144   LOFP32   25   1.65   3.6   6   0   0   1   0   1   0   1   10   0	Cortex-MO+   192   20   6144   LOFP100   84   1.65   3.6   6   0   0   1   0   1   16   0   0   0   2   2   2   2   2   2   2	2 CARM 192 20 6144 LOFP100 84 1.65 3.6 6 0 0 0 1 1 0 1 16 0 0 0 2 2 2 0 0 1	Cortex-Muo-   192   20   6144   LoFP100   84   1.65   3.6   6   0   0   1   0   1   16   0   0   2   2   0   2	2 CAREM 192 20 6144 LOFP100 84 1.65 3.6 6 0 0 0 1 1 0 1 16 0 0 0 2 2 0 0 2 0 0 2 0 0 2 0 0 0 1 0 0 1 1 16 0 0 0 0 2 0 0 1 0 0 0 0 0 0 0 0 0 0 0 0	2 CARMM 192 20 6144 LOFP100 84 1.65 3.6 6 0 0 0 1 0 1 0 0 1 16 0 0 0 2 2 0 0 2 0 1 3 3 2 CORRE-MOL 192 20 6144 LOFP32 25 1.65 3.6 6 0 0 0 1 0 1 0 1 16 0 0 0 2 2 2 0 0 1 0 0 0 2 2 0 0 1 0 0 0 2 0 0 1 0 0 0 0	2 CARAMA 192 20 6144 LOFP100 84 1.65 3.6 6 0 0 0 1 0 1 16 0 0 0 2 2 0 0 2 0 1 0 1 3 3 33 3 3 4 4 4 4 4 4 4 4 4 4 4 4 4	2 CAFEMAN 192 20 6144 LOFP100 84 1.65 3.6 6 00 0 1 1 0 0 1 16 0 0 0 2 2 0 0 2 0 1 3 3 [3] 4 2 CAFEMAN 192 20 6144 LOFP310 84 1.65 3.6 6 0 0 0 1 1 0 0 1 1 16 0 0 0 2 2 0 0 1 0 0 1 3 [3] 4 3 CAFEMAN 192 20 6144 LOFP310 84 1.65 3.6 6 0 0 0 0 1 0 0 1 0 0 0 0 2 2 0 0 1 0 0 0 0	2 CARRINAL 192 20 6144 LOFP100 84 1.65 3.6 6 0.0 0.1 1 0.0 1.0 1.0 1.0 0.0 2.0 2.0 0.0 1.0 3.0 3.0 3.0 4.0 1.0 1.0 1.0 1.0 1.0 1.0 1.0 1.0 1.0 1	2 CAPEM 192 20 6144 LOFP100 84 1.65 3.6 6 0 0 0 1 0 1 0 0 1 1 0 0 0 2 2 2 0 0 1 0 1	2 CARRAM 192 20 6144 UFBCATOR 2 CARRAM 192 20 6144 UFBCATOR 3 6 16 0 8 0 8 0 8 0 8 0 8 0 8 0 8 0 8 0 8 0	2 Corrigandus 192 20 6144 LOFP103 84 1.65 3.6 6 0 0 0 14 0 15 0 10 0 1 0 0 0 0 0 0 0 0 0 0 0 0 0	Corrigantified   1972   20   6144   Life   165   36   6   0   0   0   1   0   0   0   0   0   0	Content-Mone   1972   20   S144   LOPF100   84   1.65   3.6   6   0.0   0.0   1.0   0.0   1.0   0.0   0.0   1.0   0.0	2 Corrier-Monic 192 80 8144	2 Content Hole 192 80 8144 UFBGA100 84 1.65 3.6 8.0 0 0 1 0 1 0 0 1 1 0 0 1 0 0 0 0 1 0 0 1 0 0 1 0 0 0 0 0 1 0	2 COTTINAMO- 192 80 8144 LOFFICO 84 1.65 3.6 6 0 0 0 1 0 0 1 0 0 0 0 0 2 0 0 0 1 0 0 0 0	Contribation   192   20   6144   LOFFP102   24   188   186   25   6   20   20   21   20   20   20   20   20	2 Contenting 10 2 0 6 144 LOFF100 84 165 38 6 0 0 0 1 0 0 1 0 0 1 0 0 1 0 0 0 0 0 0	2 Control 10 1 2 2 6 1644   LOFP102 8 8 1 56 5 3 6 6 0 0 0 1 0 0 1 0 0 1 0 0 0 1 0 0 0 0	2 Content May 192 20 16144 LOPPING 84 1.65 3.6 6 0 0 0 1 0 0 1 0 0 1 0 0 0 0 0 0 0 0	2 Cation 1.0 1.0 2 01 1.0 1.0 1.0 1.0 1.0 1.0 1.0 1.0 1.0	Contenting 1.0 1.0 1.0 1.0 1.0 1.0 1.0 1.0 1.0 1.0	Part Content	Contination 1. 19 20 614 10 19 10 10 10 10 10 10 10 10 10 10 10 10 10	Part	Part

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	低功耗定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12妇DAC	比较器	放大器	SPI .	OHADSPI	] <sup>2</sup> 8	高速で	U(S)AKI	化以来UADI	CAN	SDIO	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	从 MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	AES	DES/TDES	HMAC
STM32L073V8T6	32	ARM Cortex-M0+	64	20	3072	LQFP100	84	1.65	3.6	6	0	0	1	0	1	16	0	0	2	2	0	2 (	0	1 3	[3	] 4	1	0	0	0	0	1	0	0 (	0	8x48/ 4x52	0	0	0	0	0	0	0	1	0	0 0	0
STM32L073V8I6	32	ARM Cortex-M0+	64	20	3072	UFBGA100	84	1.65	3.6	6	0	0	1	0	1	16	0	0	2	2	0	2 (	0	1 3	[3	] 4	1	0	0	0	0	1	0	0 (	0	8x48/ 4x52	0	0	0	0	0	0	0	1	0	0 0	0
STM32L073VBT6	32	ARM Cortex-M0+	128	20	6144	LQFP100	84	1.65	3.6	6	0	0	1	0	1	15	0	0	2	2	0	2 (	0	1 3	[3	] 4	1	0	0	0	0	1	0	0 (	0	8x48/ 4x52	0	0	0	0	0	0	0	1	0	0 0	0
STM32L073VBI6	32	ARM Cortex-M0+	128	20	6144	UFBGA100	84	1.65	3.6	6	0	0	1	0	1	16	0	0	2	2	0	2 (	0	1 3	[3	] 4	- 1	0	0	0	0	1	0	0 (	0	8x48/ 4x52	0	0	0	0	0	0	0	1	0	0 0	0
STM32L073VZT6	32	ARM Cortex-M0+	192	20	6144	LQFP100	84	1.65	3.6	6	0	0	1	0	1	16	0	0	2	2	0	2	0	1 3	[3	] 4	- 1	0	0	0	0	1	0	0 (	0	8x48/ 4x52	0	0	0	0	0	0	0	1	0	0 0	0
STM32L073VZI6	32	ARM Cortex-M0+	192	20	6144	UFBGA100	84	1.65	3.6	6	0	0	1	0	1	15	0	0	2	2	0	2 (	0	1 3	[3	] 4	- 1	0	0	0	0	1	0	0 (	0	8x48/ 4x52	0	0	0	0	0	0	0	1	0	0 0	0
STM32L083CBT6	32	ARM Cortex-M0+	128	20	6144	LQFP48	40	1.65	3.6	6	0	0	1	0	1	10	0	0	2	2	0	2	0	1 3	[3	] 4	. 1	0	0	0	0	1	0	0 (	0	4x18	0	0	0	0	0	0	0	1	1	0 0	0
STM32L083CZT6	32	ARM Cortex-M0+	192	20	6144	LQFP48	40	1.65	3.6	6	0	0	1	0	1	10	0	0	2	2	0	2 (	0	1 3	[3	] 4	1	0	0	0	0	1	0	0 (	0	4x18	0	0	0	0	0	0	0	1	1	0 0	0
STM32L083RBT6	32	ARM Cortex-M0+	128	20	6144	LQFP64	51	1.65	3.6	6	0	0	1	0	1	16	0	0	2	2	0	2 (	0	1 3	[3	] 4	1	0	0	0	0	1	0	0 (	0	8x28/ 4x32	0	0	0	0	0	0	0	1	1	0 0	0
STM32L083RZT6	32	ARM Cortex-M0+	192	20	6144	LQFP64	51	1.65	3.6	6	0	0	1	0	1	16	0	0	2	2	0	2 (	0	1 3	[3	] 4	1	0	0	0	0	1	0	0 (	0	8x28/ 4x32		0	0	0	0	0	0	1	1	0 0	0
STM32L083RZH6	32	ARM Cortex-M0+	192	20	6144	TFBGA64	51	1.65	3.6	6	0	0	1	0	1	16	0	0	2	2	0	2 (	0	1 3	[3	] 4	1	0	0	0	0	1	0	0 (	0	8x28/ 4x32	0	0	0	0	0	0	0	1	1	0 0	0
STM32L083V8T6	32	ARM Cortex-M0+	64	20	3072	LQFP100	84	1.65	3.6	6	0	0	1	0	1	16	0	0	2	2	0	2 (	0	1 3	[3	] 4	1	0	0	0	0	1	0	0 (	0	8x48/ 4x52	0	0	0	0	0	0	0	1	1	0 0	0
STM32L083V8I6	32	ARM Cortex-M0+	64	20	3072	UFBGA100	84	1.65	3.6	6	0	0	1	0	1	16	0	0	2	2	0	2 (	0	1 3	[3	] 4	1	0	0	0	0	1	0	0 (	0	8x48/ 4x52	0	0	0	0	0	0	0	1	1	0 0	0
STM32L083VBT6	32	ARM Cortex-M0+	128	20	6144	LQFP100	84	1.65	3.6	6	0	0	1	0	1	16	0	0	2	2	0	2	0	1 3	[3	] 4	1	0	0	0	0	1	0	0 (	0	8x48/ 4x52		0	0	0	0	0	0	1	1	0 0	0
STM32L083VBI6	32	ARM Cortex-M0+	128	20	6144	UFBGA100	84	1.65	3.6	6	0	0	1	0	1	16	0	0	2	2	0	2	0	1 3	[3	] 4	1	0	0	0	0	1	0	0 (	0	8x48/ 4x52	0	0	0	0	0	0	0	1	1	0 0	0
STM32L083VZT6	32	ARM Cortex-M0+	192	20	6144	LQFP100	84	1.65	3.6	6	0	0	1	0	1	16	0	0	2	2	0	2 (	0	1 3	[3	] 4	1	0	0	0	0	1	0	0 (	0	8x48/ 4x52	0	0	0	0	0	0	0	1	1	0 0	0
STM32L083VZI6	32	ARM Cortex-M0+	192	20	6144	UFBGA100	84	1.65	3.6	6	0	0	1	0	1	16	0	0	2	2	0	2 (	0	1 3	[3	] 4	1	0	0	0	0	1	0	0 (	0	8x48/ 4x52	0	0	0	0	0	0	0	1	1	0 0	0

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12·立DAC	比交器	SPI	QUADSPI	I <sup>2</sup> S	ľC	高速I°C	U(S)ART	低功耗UART	SDIO	FSMC	FMC	从USB	全速USB OTG	以太网	MDIOS	段式LCD	TFI LCD	DSI HOST	SAI	SPDIFRX	DEMI	SWPMI	TRNG	AES	DES/TDES	HMAC
							<u>'                                    </u>							'				STI	VI32L	100超	<u>.</u> 值型	- 32	MHz																					
STM32L100C6U6-A	32	ARM Cortex-M3	32	4	2048	UFQFPN48	37	1.8	3.6	8	0	0 0	0	1	14	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 0	0	0	4x16	0	0	0	0	0 0	0	0	0	0	0 0
STM32L100R8T6-A	32	ARM Cortex-M3	64	8	2048	LQFP64	51	1.8	3.6	8	0	0 0	0	1	20	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 (	0	0	4x32/ 8x28	0	0	0	0	0 0	0	0	0	0	0 0
STM32L100RBT6-A	32	ARM Cortex-M3	128	16	2048	LQFP64	51	1.8	3.6	8	0	0 0	0	1	20	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 (	0	0	4x32/ 8x28	0	0	0	0	0 0	0	0	0	0	0 0
STM32L100RCT6	32	ARM Cortex-M3	256	16	4096	LQFP64	51	1.8	3.6	8	0	0 0	0	1	20	0	0	2 2	2 0	3	0	2	2	0	3	0 0	0	0	0	1	0 (	0	0	4x32/ 8x28	0	0	0	0	0 0	0	0	0	0	0 0
																		STM3	32L15	1/152	八八	型 - :	32MHz																					
STM32L151C6T6-A	32	ARM Cortex-M3	32	16	4096	LQFP48	37	1.65	3.6	8	0	0 0	0	1	14	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 0	0	0	0	0	0	0	0	0 0	0	0	0	0	0 0
STM32L151C6U6-A	32	ARM Cortex-M3	32	16	4096	UFQFPN48	37	1.65	3.6	8	0	0 0	0	1	14	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 (	0	0	0	0	0	0	0	0 0	0	0	0	0	0 0
STM32L151C8T6-A	32	ARM Cortex-M3	64	32	4096	LQFP48	37	1.65	3.6	8	0	0 0	0	1	14	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 (	0	0	0	0	0	0	0	0 0	0	0	0	0	0 0
STM32L151C8U6-A	32	ARM Cortex-M3	64	32	4096	UFQFPN48	37	1.65	3.6	8	0	0 0	0	1	14	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 0	0	0	0	0	0	0	0	0 0	0	0	0	0	0 0
STM32L151CBT6-A	32	ARM Cortex-M3	128	32	4096	LQFP48	37	1.65	3.6	8	0	0 0	0	1	14	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 (	0	0	0	0	0	0	0	0 0	0	0	0	0	0 0
STM32L151CBU6-A	32	ARM Cortex-M3	128	32	4096	UFQFPN48	37	1.65	3.6	8	0	0 0	0	1	14	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 0	0	0	0	0	0	0	0	0 0	0	0	0	0	0 0
STM32L151R6T6-A	32	ARM Cortex-M3	32	16	4096	LQFP64	51	1.65	3.6	8	0	0 0	0	1	20	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 0	0	0	0	0	0	0	0	0 0	0	0	0	0	0 0
STM32L151R6H6-A	32	ARM Cortex-M3	32	16	4096	TFBGA64	50	1.65	3.6	8	0	0 0	0	1	19	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 0	0	0	0	0	0	0	0	0 0	0	0	0	0	0 0
STM32L151R8T6-A	32	ARM Cortex-M3	64	32	4096	LQFP64	51	1.65	3.6	8	0	0 0	0	1	20	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 0	0	0	0	0	0	0	0	0 0	0	0	0	0	0 0
STM32L151R8H6-A	32	ARM Cortex-M3	64	32	4096	TFBGA64	50	1.65	3.6	8	0	0 0	0	1	19	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 (	0	0	0	0	0	0	0	0 0	0	0	0	0	0 0
STM32L151RBT6-A	32	ARM Cortex-M3	128	32	4096	LQFP64	51	1.65	3.6	8	0	0 0	0	1	20	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 (	0	0	0	0	0	0	0	0 0	0	0	0	0	0 0
STM32L151RBH6-A	32	ARM Cortex-M3	128	32	4096	TFBGA64	50	1.65	3.6	8	0	0 0	0	1	19	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 0	0	0	0	0	0	0	0	0 0	0	0	0	0	0 0
STM32L151V8T6-A	32	ARM Cortex-M3	64	32	4096	LQFP100	83	1.65	3.6	8	0	0 0	0	1	24	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 (	0	0	0	0	0	0	0	0 0	0	0	0	0	0 0
STM32L151V8H6-A	32	ARM Cortex-M3	64	32	4096	UFBGA100	83	1.65	3.6	8	0	0 0	0	1	24	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 (	0	0	0	0	0	0	0	0 0	0	0	0	0	0 0
STM32L151VBT6-A	32	ARM Cortex-M3	128	32	4096	LQFP100	83	1.65	3.6	8	0	0 0	0	1	24	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 (	0	0	0	0	0	0	0	0 0	0	0	0	0	0 0
STM32L151VBH6-A	32	ARM Cortex-M3	128	32	4096	UFBGA100	83	1.65	3.6	8	0	0 0	0	1	24	0	0	2 2	2 0	2	0	0	2	0	3	0 0	0	0	0	1	0 (	0	0	0	0	0	0	0	0 0	0	0	0	0	0 0

产品型号	主频 (MHz)	内 核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器(低功耗定时器)	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12立DAC	放大器	SPI	QUADSPI	l²S	l²C	高速r°C	U(S)ART	低功耗UART	SDIO	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES AES	SHA	НМАС
								压	压	нн		器器	器	芜	坦	<b>是</b>   '															<u>.</u>	S .													
STM32L151CCT6	32	ARM Cortex-M3	256	32	8192	LQFP48	37	1.65	3.6	8	1	0 0	0	1	14	0	0	2 2	2	3	0	2	2	0	3	0 (	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151CCU6	32	ARM Cortex-M3	256	32	8192	UFQFPN48	37	1.65	3.6	8	1	0 0	0	1	14	0	0	2 2	2	3	0	2	2	0	3	0 (	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151UCY6	32	ARM Cortex-M3	256	32	8192	WLCSP63	51	1.65	3.6	8	1	0 0	0	1	21	0	0	2 2	2	3	0	2	2	0	3	0 (	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151RCT6	32	ARM Cortex-M3	256	32	8192	LQFP64	51	1.65	3.6	8	1	0 0	0	1	21	0	0	2 2	2	3	0	2	2	0	3	0 (	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151VCT6	32	ARM Cortex-M3	256	32	8192	LQFP100	83	1.65	3.6	8	1	0 0	0	1	25	0	0	2 2	2	3	0	2	2	0	3	0 (	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151VCH6	32	ARM Cortex-M3	256	32	8192	UFBGA100	83	1.65	3.6	8	1	0 0	0	1	25	0	0	2 2	2	3	0	2	2	0	3	0 (	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151QCH6	32	ARM Cortex-M3	256	32	8192	UFBGA132	109	1.65	3.6	8	1	0 0	0	1	40	0	0	2 2	2	3	0	2	2	0	3	0 (	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151ZCT6	32	ARM Cortex-M3	256	32	8192	LQFP144	115	1.65	3.6	8	1	0 0	0	1	40	0	0	2 2	2	3	0	2	2	0	3	0 (	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151RDT6	32	ARM Cortex-M3	384	48	12288	LQFP64	51	1.65	3.6	8	1	0 0	0	1	21	0	0 :	2 2	3	3	0	2	2	0	5	0 (	) 1	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151RDY6	32	ARM Cortex-M3	384	48	12288	WLCSP64	51	1.65	3.6	8	1	0 0	0	1	21	0	0	2 2	3	3	0	2	2	0	5	0 (	) 1	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151VDT6	32	ARM Cortex-M3	384	48	12288	LQFP100	83	1.65	3.6	8	1	0 0	0	1	25	0	0	2 2	3	3	0	2	2	0	5	0 (	) 1	1	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151QDH6	32	ARM Cortex-M3	384	48	12288	UFBGA132	109	1.65	3.6	8	1	0 0	0	1	40	0	0	2 2	3	3	0	2	2	0	5	0 (	) 1	1	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151ZDT6	32	ARM Cortex-M3	384	48	12288	LQFP144	115	1.65	3.6	8	1	0 0	0	1	40	0	0	2 2	3	3	0	2	2	0	5	0 (	) 1	1	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151VDT6-x	32	ARM Cortex-M3	384	80	16384	LQFP100	83	1.65	3.6	8	1	0 0	0	1	25	0	0	2 2	2	3	0	2	2	0	5	0 (	) 1	1	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151VDY6-x	32	ARM Cortex-M3	384	80	16384	WLCSP104	83	1.65	3.6	8	1	0 0	0	1	25	0	0	2 2	2	3	0	2	2	0	5	0 (	) 1	1	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151RET6	32	ARM Cortex-M3	512	80	16384	LQFP64	51	1.65	3.6	8	1	0 0	0	1	21	0	0	2 2	2	3	0	2	2	0	5	0 (	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151VET6	32	ARM Cortex-M3	512	80	16384	LQFP100	83	1.65	3.6	8	1	0 0	0	1	25	0	0	2 2	2	3	0	2	2	0	5	0 (	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151VEY6	32	ARM Cortex-M3	512	80	16384	WLCSP104	83	1.65	3.6	8	1	0 0	0	1	25	0	0	2 2	2	3	0	2	2	0	5	0 (	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151QEH6	32	ARM Cortex-M3	512	80	16384	UFBGA132	109	1.65	3.6	8	1	0 0	0	1	40	0	0	2 2	2	3	0	2	2	0	5	0 (	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L151ZET6	32	ARM Cortex-M3	512	80	16384	LQFP144	115	1.65	3.6	8	1	0 0	0	1	40	0	0	2 2	2	3	0	2	2	0	5	0 (	0	0	0	1	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	0	0
STM32L152C6T6-A	32	ARM Cortex-M3	32	16	4096	LQFP48	37	1.65	3.6	8	0	0 0	0	1	14	0	0	2 2	0	2	0	0	2	0	3	0 (	0	0	0	1	0	0 (	0	4x16	0	0	0	0	0	0	0	0	0 0	0	0

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	こう こうきょう こうしょう こうりょう こうりょう こうりょう こうしょう こうしん こうしん こうしん こうしん こうしん こうしん こうしん こうし	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12匂DAC	比较器	放大器	SPI	QUADSPI	-2% - C	高速l°C	U(S)ART	低功耗UART	CAN	SDIO	FSMC	FMC	Alish Alish	高速USB OTG	以太网	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES AES	SHA	НМАС
STM32L152C6U6-A	32	ARM Cortex-M3	32	16	4096	UFQFPN48	37	1.65	3.6	8		0 (			14	π 0	0	2	2	0	2	0	0 2	0	3	0	0	0	0	0	1 0	0	0	0	4x16	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152C8T6-A	32	ARM Cortex-M3	64	32	4096	LQFP48	37	1.65	3.6	8	0	0 (	0	1	14	0	0	2	2	0	2	0	0 2	0	3	0	0	0	0	0	1 0	0	0	0	4x16	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152C8U6-A	32	ARM Cortex-M3	64	32	4096	UFQFPN48	37	1.65	3.6	8	0	0 (	0	1	14	0	0	2	2	0	2	0	0 2	0	3	0	0	0	0	0	1 0	0	0	0	4x16	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152CBT6-A	32	ARM Cortex-M3	128	32	4096	LQFP48	37	1.65	3.6	8	0	0 (	0	1	14	0	0	2	2	0	2	0	0 2	0	3	0	0	0	0	0	1 C	0	0	0	4x16	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152CBU6-A	32	ARM Cortex-M3	128	32	4096	UFQFPN48	37	1.65	3.6	8	0	0 (	0	1	14	0	0	2	2	0	2	0	0 2	0	3	0	0	0	0	0	1 C	0	0	0	4x16	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152R6T6-A	32	ARM Cortex-M3	32	16	4096	LQFP64	51	1.65	3.6	8	0	0 (	0	1	20	0	0	2	2	0	2	0	0 2	0	3	0	0	0	0	0	1 0	0	0	0	4x32/ 8x28	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152R6H6-A	32	ARM Cortex-M3	32	16	4096	TFBGA64	50	1.65	3.6	8	0	0 (	0	1	19	0	0	2	2	0	2	0	0 2	0	3	0	0	0	0	0	1 0	0	0	0	4x31/ 8x27	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152R8T6-A	32	ARM Cortex-M3	64	32	4096	LQFP64	51	1.65	3.6	8	0	0 (	0	1	20	0	0	2	2	0	2	0	0 2	0	3	0	0	0	0	0	1 0	0	0	0	4x32/ 8x28	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152R8H6-A	32	ARM Cortex-M3	64	32	4096	TFBGA64	50	1.65	3.6	8	0	0 (	0	1	19	0	0	2	2	0	2	0	0 2	0	3	0	0	0	0	0	1 0	0	0	0	4x31/ 8x27	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152RBT6-A	32	ARM Cortex-M3	128	32	4096	LQFP64	51	1.65	3.6	8	0	0 (	0	1	20	0	0	2	2	0	2	0	0 2	0	3	0	0	0	0	0	1 0	0	0	0	4x32/ 8x28	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152RBH6-A	32	ARM Cortex-M3	128	32	4096	TFBGA64	50	1.65	3.6	8	0	0 (	0	1	19	0	0	2	2	0	2	0	0 2	0	3	0	0	0	0	0	1 0	0	0	0	4x31/ 8x27	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152V8T6-A	32	ARM Cortex-M3	64	32	4096	LQFP100	83	1.65	3.6	8	0	0 (	0	1	24	0	0	2	2	0	2	0	0 2	0	3	0	0	0	0	0	1 0	0	0	0	4x44/ 8x40	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152V8H6-A	32	ARM Cortex-M3	64	32	4096	UFBGA100	83	1.65	3.6	8	0	0 (	0	1	24	0	0	2	2	0	2	0	0 2	0	3	0	0	0	0	0	1 0	0	0	0	4x44/ 8x40	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152VBT6-A	32	ARM Cortex-M3	128	32	4096	LQFP100	83	1.65	3.6	8	0	0 (	0	1	24	0	0	2	2	0	2	0	0 2	0	3	0	0	0	0	0	1 0	0	0	0	4x44/ 8x40	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152VBH6-A	32	ARM Cortex-M3	128	32	4096	UFBGA100	83	1.65	3.6	8	0	0 (	0	1	24	0	0	2	2	0	2	0	0 2	0	3	0	0	0	0	0	1 0	0	0	0	4x44/ 8x40	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152CCT6	32	ARM Cortex-M3	256	32	8192	LQFP48	37	1.65	3.6	8	1	0 (	0	1	14	0	0	2	2	2	3	0	2 2	0	3	0	0	0	0	0	1 0	0	0	0	4x18	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152CCU6	32	ARM Cortex-M3	256	32	8192	UFQFPN48	37	1.65	3.6	8	1	0 (	0	1	14	0	0	2	2	2	3	0	2 2	0	3	0	0	0	0	0	1 0	0	0	0	4x18	0	0	0	0	0 (	)	0 (	0 (	0 0	0	0
STM32L152UCY6	32	ARM Cortex-M3	256	32	8192	WLCSP63	51	1.65	3.6	8	1	0 (	0	1	21	0	0	2	2	2	3	0	2 2	0	3	0	0	0	0	0	1 0	0	0	0	4x32/ 8x28	0	0	0	0	0 (	)	0 (	0 (	0 0	0	0
STM32L152RCT6	32	ARM Cortex-M3	256	32	8192	LQFP64	51	1.65	3.6	8	1	0 (	0	1	21	0	0	2	2	2	3	0	2 2	0	3	0	0	0	0	0	1 0	0	0	0	4x32/ 8x28	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152VCT6	32	ARM Cortex-M3	256	32	8192	LQFP100	83	1.65	3.6	8	1	0 (	0	1	25	0	0	2	2	2	3	0	2 2	0	3	0	0	0	0	0	1 0	0	0	0	4x44/ 8x40	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0
STM32L152VCH6	32	ARM Cortex-M3	256	32	8192	UFBGA100	83	1.65	3.6	8	1	0 (	0	1	25	0	0	2	2	2	3	0	2 2	0	3	0	0	0	0	0	1 0	0	0	0	4x44/ 8x40	0	0	0	0	0 (	0	0 (	0 (	0 0	0	0

产品	主频	ф	FLAS	RAN	EEPR	<b>\$</b>	通	最低工	最高工	16位	32位	电机空	高分辨	12년ADC	12位A	16过ADC	16位A	12∜	比	放		QU		高	U(S	低功		S	Z		W I	全東	国以	M	段	H	ISO		SPI	무	D	VS	=	עני	DES	. =
产品型号	主频 (MHz)	核	FLASH (KB)	RAM (KB)	EEPROM (B)	封 装	用。	低工作电压	高工作电压	6位定时器	2位定时器	控制定时器	率定时器	ADC转换单元	ADC通道	ADC转换单元	6位ADC通道	12坦DAC	比较器	放大器	SPI	QUADSPI	Z° Z	達r'C	U(S)ART	切耗UART	CAN	SDIO	FSIMC	FMC	<b>SUSB</b>	音速 GOD CIG	以太网	MDIOS	段式LCD	TET LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	AES	SHA S/TDES	HMAC
STM32L152ZCT6	32	ARM Cortex-M3	256	32	8192	LQFP144	115	1.65	3.6	8	1	0	0	1	40	0	0	2	2	2	3	0	2 2	0	3	0	0	0	0	0	1	0 0	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L152QCH6	32	ARM Cortex-M3	256	32	8192	UFBGA132	109	1.65	3.6	8	1	0	0	1	40	0	0	2	2	2	3	0	2 2	0	3	0	0	0	0	0	1	0 0	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L152RDT6	32	ARM Cortex-M3	384	48	12288	LQFP64	51	1.65	3.6	8	1	0	0	1	21	0	0	2	2	3	3	0	2 2	0	5	0	0	1	0	0	1	0 (	0	0	4x32/ 8x28	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L152RDY6	32	ARM Cortex-M3	384	48	12288	WLCSP64	51	1.65	3.6	8	1	0	0	1	21	0	0	2	2	3	3	0	2 2	0	5	0	0	1	0	0	1	0 0	0	0	4x32/ 8x28	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L152VDT6	32	ARM Cortex-M3	384	48	12288	LQFP100	83	1.65	3.6	8	1	0	0 0	1	25	0	0	2	2	3	3	0	2 2	0	5	0	0	1	1	0	1	0 0	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L152QDH6	32	ARM Cortex-M3	384	48	12288	UFBGA132	109	1.65	3.6	8	1	0	0	1	40	0	0	2	2	3	3	0	2 2	0	5	0	0	1	1	0	1	0 (	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L152ZDT6	32	ARM Cortex-M3	384	48	12288	LQFP144	115	1.65	3.6	8	1	0	0	1	40	0	0	2	2	3	3	0	2 2	0	5	0	0	1	1	0	1	0 (	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L152VDT6-x	32	ARM Cortex-M3	384	80	16384	LQFP100	83	1.65	3.6	8	1	0	0	1	25	0	0	2	2	2	3	0	2 2	0	5	0	0	1	1	0	1	0 0	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L152VDY6-x	32	ARM Cortex-M3	384	80	16384	WLCSP104	83	1.65	3.6	8	1	0	0	1	25	0	0	2	2	2	3	0	2 2	0	5	0	0	1	1	0	1	0 (	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L152RET6	32	ARM Cortex-M3	512	80	16384	LQFP64	51	1.65	3.6	8	1	0	0	1	21	0	0	2	2	2	3	0	2 2	0	5	0	0	0	0	0	1	0 (	0	0	4x32/ 8x28	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L152VET6	32	ARM Cortex-M3	512	80	16384	LQFP100	83	1.65	3.6	8	1	0	0	1	25	0	0	2	2	2	3	0	2 2	0	5	0	0	0	0	0	1	0 (	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L152VEY6	32	ARM Cortex-M3	512	80	16384	WLCSP104	83	1.65	3.6	8	1	0	0	1	25	0	0	2	2	2	3	0	2 2	0	5	0	0	0	0	0	1	0 (	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L152QEH6	32	ARM Cortex-M3 ARM	512	80	16384	UFBGA132	109	1.65	3.6	8	1	0	0	1	40	0	0	2	2	2	3	0	2 2	0	5	0	0	0	0	0	1	0 (	0	0	4x44/ 8x40 4x44/	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L152ZET6	32	Cortex-M3	512	80	16384	LQFP144	115	1.65	3.6	8	1	0	0	1	40	0	0	2 M22	162	2			2 2 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3			0	0	0	0	0	1	0 (	0	0	8x40	0	0	0	0	0	0	0	0	0 0	0 0	0
STM32L162RCT6	32	ARM Cortex-M3	256	32	8192	LQFP64	51	1.65	3.6	8	1	0	0 0	1	21	0	0	2	2	2			2 2			0	0	0	0	0	1	0 0	0	0	4x32/ 8x28	0	0	0	0	0	0	0	0	1 0	0 0	0
STM32L162VCT6	32	ARM Cortex-M3	256	32	8192	LQFP100	83	1.65	3.6	8	1	0	0 0	1	25	0	0	2	2	2	3	0	2 2	0	3	0	0	0	0	0	1	0 0	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	1 0	0 0	0
STM32L162VCH6	32	ARM Cortex-M3	256	32	8192	UFBGA100	83	1.65	3.6	8	1	0	0	1	25	0	0	2	2	2	3	0	2 2	0	3	0	0	0	0	0	1	0 (	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	1 0	0 0	0
STM32L162QCH6	32	ARM Cortex-M3	256	32	8192	UFBGA132	109	1.65	3.6	8	1	0	0	1	40	0	0	2	2	2	3	0	2 2	0	3	0	0	0	0	0	1	0 (	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	1 0	0 0	0
STM32L162ZCT6	32	ARM Cortex-M3	256	32	8192	LQFP144	115	1.65	3.6	8	1	0	0	1	40	0	0	2	2	2	3	0	2 2	0	3	0	0	0	0	0	1	0 (	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	1 0	0 0	0
STM32L162RDT6	32	ARM Cortex-M3	384	48	12288	LQFP64	51	1.65	3.6	8	1	0	0	1	21	0	0	2	2	3	3	0	2 2	0	5	0	0	1	0	0	1	0 (	0	0	4x32/ 8x28	0	0	0	0	0	0	0	0	1 0	0 0	0
STM32L162RDY6	32	ARM Cortex-M3	384	48	12288	WLCSP64	51	1.65	3.6	8	1	0	0	1	21	0	0	2	2	3	3	0	2 2	0	5	0	0	1	0	0	1	0 (	0	0	4x32/ 8x28	0	0	0	0	0	0	0	0	1 0	0 0	0

产 대 무 号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	低功耗定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	SPI	QUADSPI	l <sup>2</sup> S	l²C	高速rc	U(S)ART	低功耗UART	CAN	SDIO	EGMO	FMC 以USB	全速USB OTG	高速USB OTG	以太网	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	AES	DES/TDES	SHA	CONTRACT
STM32L162VDT6	32	ARM Cortex-M3	384	48	12288	LQFP100	83	1.65	3.6	8	1	0	0	0	1	25	0	0	2	2	3	3	0	2	2	0	5	0	0	1 1	1	0 1	0	0	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	1	0 (	0 0	1
STM32L162QDH6	32	ARM Cortex-M3	384	48	12288	UFBGA132	109	1.65	3.6	8	1	0	0	0	1	40	0	0	2	2	3	3	0	2	2	0	5	0	0	1 1	1	0 1	0	0	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	1	0 (	0 0	1
STM32L162ZDT6	32	ARM Cortex-M3	384	48	12288	LQFP144	115	1.65	3.6	8	1	0	0	0	1	40	0	0	2	2	3	3	0	2	2	0	5	0	0	1 1	(	0 1	0	0	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	1	0 (	0 0	
STM32L162VDY6-X	32	ARM Cortex-M3	384	80	16384	WLCSP104	83	1.65	3.6	8	1	0	0	0	1	25	0	0	2	2	2	3	0	2	2	0	5	0	0	0 0	) (	0 1	0	0	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	1	0 (	0 0	7
STM32L162RET6	32	ARM Cortex-M3	512	80	16384	LQFP64	51	1.65	3.6	8	1	0	0	0	1	21	0	0	2	2	2	3	0	2	2	0	5	0	0	0 0	) (	0 1	0	0	0	0	4x32/ 8x28	0	0	0	0	0	0	0	0	1	0 (	0 0	7
STM32L162VET6	32	ARM Cortex-M3	512	80	16384	LQFP100	83	1.65	3.6	8	1	0	0	0	1	25	0	0	2	2	2	3	0	2	2	0	5	0	0	0 0	) (	0 1	0	0	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	1	0 (	0 0	7
STM32L162VEY6	32	ARM Cortex-M3	512	80	16384	WLCSP104	83	1.65	3.6	8	1	0	0	0	1	25	0	0	2	2	2	3	0	2	2	0	5	0	0	0 0	) (	0 1	0	0	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	1	0 0	0 0	
STM32L162ZET6	32	ARM Cortex-M3	512	80	16384	LQFP144	115	1.65	3.6	8	1	0	0	0	1	40	0	0	2	2	2	3	0	2	2	0	5	0	0	0 0	) (	0 1	0	0	0	0	4x44/ 8x40	0	0	0	0	0	0	0	0	1	0 (	0 0	

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产品型号	主频 (MHz)	内核	FLASH (KB)	<b>ВАМ (КВ)</b>	EEPROM (B)	封装	通 用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12过DAC	比较器	放大器	SPI	QUADSPI	-2S	12°	U(S)ART 高速I°C	低功耗UART	CAN	SDIO	FSMC	FMC	从USB	全速USB OTG	高東IISB OTG	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	AES	DES/TDES	HMAC
																			STM	32L4x	(1人)	· ]型·	- 80 I	ИHz																						
STM32L431KBU6	80	ARM Cortex-M4	128	64	0	UFQFPN32	26	1.71	3.6	5	1	1 2	0	1	10	0	0	2	2	1	2	1	0 :	2 [	[2] 2	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	1	0 0	0 0	0
STM32L431KCU6	80	ARM Cortex-M4	256	64	0	UFQFPN32	26	1.71	3.6	5	1	1 2	0	1	10	0	0	2	2	1	2	1	0 :	2 [	[2] 2	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	1	0 0	0 0	0
STM32L431CBT6	80	ARM Cortex-M4	128	64	0	LQFP48	38	1.71	3.6	5	1	1 2	0	1	10	0	0	2	2	1	3	1	0 ;	3 [	[3] 3	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	1	0 0	0 0	0
STM32L431CCT6	80	ARM Cortex-M4	256	64	0	LQFP48	38	1.71	3.6	5	1	1 2	0	1	10	0	0	2	2	1	3	1	0 :	3 [	[3] 3	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	1	0 0	0 0	0
STM32L431CBU6	80	ARM Cortex-M4	128	64	0	UFQFPN48	38	1.71	3.6	5	1	1 2	0	1	10	0	0	2	2	1	3	1	0 :	3 [	[3] 3	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	1	0 0	0 0	0
STM32L431CCU6	80	ARM Cortex-M4	256	64	0	UFQFPN48	38	1.71	3.6	5	1	1 2	0	1	10	0	0	2	2	1	3	1	0 :	3 [	[3] 3	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	1	0 0	0 0	0
STM32L431CBY6	80	ARM Cortex-M4	128	64	0	WLCSP49	39	1.71	3.6	5	1	1 2	0	1	10	0	0	2	2	1	3	1	0	3 [	[3] 3	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	1	0 0	0 0	0
STM32L431CCY6	80	ARM Cortex-M4	256	64	0	WLCSP49	39	1.71	3.6	5	1	1 2	0	1	10	0	0	2	2	1	3	1	0	3 [	[3] 3	1	1	0	0	0	0	0	0 (	0	0	0	0	1	0	0	0	1	1	0 0	0 0	0
STM32L431RBT6	80	ARM Cortex-M4	128	64	0	LQFP64	52	1.71	3.6	5	1	1 2	0	1	16	0	0	2	2	1	3	1	0 :	3 [	[3] 3	1	1	1	0	0	0	0	0 (	0	0	0	0	1	0	0	0	1	1	0 0	0 0	0
STM32L431RCT6	80	ARM Cortex-M4	256	64	0	LQFP64	52	1.71	3.6	5	1	1 2	0	1	16	0	0	2	2	1	3	1	0 ;	3 [	[3] 3	1	1	1	0	0	0	0	0	0	0	0	0	1	0	0	0	1	1	0 0	0 0	0
STM32L431RBI6	80	ARM Cortex-M4	128	64	0	UFBGA64	52	1.71	3.6	5	1	1 2	0	1	16	0	0	2	2	1	3	1	0	3 [	[3] 3	1	1	1	0	0	0	0	0	0	0	0	0	1	0	0	0	1	1	0 0	0 0	0
STM32L431RCl6	80	ARM Cortex-M4	256	64	0	UFBGA64	52	1.71	3.6	5	1	1 2	0	1	16	0	0	2	2	1	3	1	0	3 [	[3] 3	1	1	1	0	0	0	0	0	0	0	0	0	1	0	0	0	1	1	0 0	0 0	0
STM32L431RBY6	80	ARM Cortex-M4	128	64	0	WLCSP64	52	1.71	3.6	5	1	1 2	0	1	16	0	0	2	2	1	3	1	0	3 [	[3] 3	1	1	1	0	0	0	0	0	0	0	0	0	1	0	0	0	1	1	0 0	0 0	0
STM32L431RCY6	80	ARM Cortex-M4	256	64	0	WLCSP64	52	1.71	3.6	5	1	1 2	0	1	16	0	0	2	2	1	3	1	0	3 [	[3] 3	1	1	1	0	0	0	0	0	0	0	0	0	1	0	0	0	1	1	0 0	0 0	0
STM32L431VCT6	80	ARM Cortex-M4	256	64	0	LQFP100	83	1.71	3.6	5	1	1 2	0	1	16	0	0	2	2	1	3	1	0	3 [	[3] 3	1	1	1	0	0	0	0	0	0	0	0	0	1	0	0	0	1	1	0 0	0 0	0
STM32L431VCl6	80	ARM Cortex-M4	256	64	0	UFBGA100	83	1.71	3.6	5	1	1 2	0	1	16	0	0	2	2	1	3	1	0 :	3 [	[3] 3	1	1	1	0	0	0	0	0 (	0	0	0	0	1	0	0	0	1	1	0 0	0 0	0
STM32L451CCU6	80	ARM Cortex-M4	256	160	0	UFQFPN48	38	1.71	3.6	5	1	1 2	0	1	10	0	0	1	2	1	3	1	0 4	1 [	[4] 3+	1 1	1	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	1	0 0	0 0	0
STM32L451CEU6	80	ARM Cortex-M4	512	160	0	UFQFPN48	38	1.71	3.6	5	1	1 2	0	1	10	0	0	1	2	1	3	1	0 4	1 [	[4] 3+	1 1	1	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	1	0 0	0 0	0
STM32L451RCT6	80	ARM Cortex-M4	256	160	0	LQFP64	52	1.71	3.6	5	1	1 2	0	1	16	0	0	1	2	1	3	1	0 4	1 [	[4] 3+	1 1	1	1	0	0	0	0	0	0	0	0	0	1	0	1	0	0	1	0 0	0 0	0
STM32L451RCI6	80	ARM Cortex-M4	256	160	0	UFBGA64	52	1.71	3.6	5	1	1 2	0	1	16	0	0	1	2	1	3	1	0 4	1 [	[4] 3+	1 1	1	1	0	0	0	0	0	0	0	0	0	1	0	1	0	0	1	0 0	0 0	0
STM32L451REY6	80	ARM Cortex-M4	512	160	0	WLCSP64	52	1.71	3.6	5	1	1 2	0	1	16	0	0	1	2	1	3	1	0	1 [	[4] 3+	1 1	1	1	0	0	0	0	0	0	0	0	0	1	0	1	0	0	1	0 0	0 0	0

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用io	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	低功耗定时器	高分辨率定付器 :	12立ADC转换单元	13·拉ADC報授单元	Ib化ADC通道	12位DAC	比较器	放大器	SPI	QUADSPI	I²S	I²C	高速l°C	U(S)ART	低功耗UART	CAN	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	以太网	MDIOS	<b>设式</b> G	TEI LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	AES	DES/TDES	SHA	HMAC
STM32L451REI6	80	ARM Cortex-M4	512	160	0	UFBGA64	52	1.71	3.6	5	1	1	2	0	1 1	6 0	0	1	2	1	3	1	0	4	[4]	3+1	1	1 1	0	0	0	0	0	0	)	0	0	0	1	0	1 0	) (	0 1	1 0	0	0	0
STM32L451RET6	80	ARM Cortex-M4	512	160	0	LQFP64	52	1.71	3.6	5	1	1	2	0	1 1	6 0	0	1	2	1	3	1	0	4	[4]	3+1	1	1 1	0	0	0	0	0	0	)	0	0	0	1	0	1 0	) (	0 1	1 0	0	0	0
STM32L451VCT6	80	ARM Cortex-M4	256	160	0	LQFP100	83	1.71	3.6	5	1	1	2	0	1 1	6 0	0	1	2	1	3	1	0	4	[4]	3+1	1	1 1	0	0	0	0	0	0	)	0	0	0	1	0	1 0	) 1	0 1	1 0	0	0	0
STM32L451VCl6	80	ARM Cortex-M4	256	160	0	UFBGA100	83	1.71	3.6	5	1	1	2	0	1 1	6 0	0	1	2	1	3	1	0	4	[4]	3+1	1	1 1	0	0	0	0	0	0	)	0	0	0	1	0	1 0	) ,	0 1	1 0	0	0	0
STM32L451VET6	80	ARM Cortex-M4	512	160	0	LQFP100	83	1.71	3.6	5	1	1	2	0	1 1	6 0	0	1	2	1	3	1	0	4	[4]	3+1	1	1 1	0	0	0	0	0	0	)	0	0	0	1	0	1 0	) ,	0 1	1 0	0	0	0
STM32L451VEI6	80	ARM Cortex-M4	512	160	0	UFBGA100	83	1.71	3.6	5	1	1	2	0	1 1	6 0	0	1	2	1	3	1	0	4	[4]	3+1	1	1 1	0	0	0	0	0	0	)	0	0	0	1	0	1 0	) '	0 1	1 0	0	0	0
STM32L471RET6	80	ARM Cortex-M4	512	128	0	LQFP64	51	1.71	3.6	9	2	2	2	0	3 1	6 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	0	0	0	0	0	0	)	0	0	0	2	0	1 0	) .	1 1	1 0	0	0	0
STM32L471RGT6	80	ARM Cortex-M4	1024	128	0	LQFP64	51	1.71	3.6	9	2	2	2	0	3 1	6 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	0	0	0	0	0	0	)	0	0	0	2	0	1 0	) .	1 1	1 0	0	0	0
STM32L471VET6	80	ARM Cortex-M4	512	128	0	LQFP100	82	1.71	3.6	9	2	2	2	0	3 1	6 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	1	0	0	0	0	0	)	0	0	0	2	0	1 0	) .	1 1	1 0	0	0	0
STM32L471VGT6	80	ARM Cortex-M4	1024	128	0	LQFP100	82	1.71	3.6	9	2	2	2	0	3 1	6 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	1	0	0	0	0	0	)	0	0	0	2	0	1 0	) .	1 1	1 0	0	0	0
STM32L471QEI6	80	ARM Cortex-M4	512	128	0	UFBGA132	109	1.71	3.6	9	2	2	2	0	3 1	9 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	1	0	0	0	0	0	)	0	0	0	2	0	1 0	) .	1 1	1 0	0	0	0
STM32L471QGI6	80	ARM Cortex-M4	1024	128	0	UFBGA132	109	1.71	3.6	9	2	2	2	0	3 1	9 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	1	0	0	0	0	0	)	0	0	0	2	0	1 0	) .	1 1	1 0	0	0	0
STM32L471ZET6	80	ARM Cortex-M4	512	128	0	LQFP144	114	1.71	3.6	9	2	2	2	0	3 2	4 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	1	0	0	0	0	0	)	0	0	0	2	0	1 0	)	1 1	1 0	0 0	0	0
STM32L471ZGT6	80	ARM Cortex-M4	1024	128	0	LQFP144	114	1.71	3.6	9	2	2	2	0	3 2	4 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	1	0	0	0	0	0	)	0	0	0	2	0	1 0	)	1 1	1 0	0 0	0	0
																		ST	M32L4	x2 U	SB F	S产品	线 -	80 N	Hz																						
STM32L432KBU6	80	ARM Cortex-M4	128	64	0	UFQFPN32	26	1.71	3.6	5	1	1	2	0	1 1	0 0	0	2	2	1	2	1	0	2	[2]	2	1	1 0	0	0	1	0	0	0	)	0	0	0	1	0	0 0	) .	1 1	1 0	0	0	0
STM32L432KCU6	80	ARM Cortex-M4	256	64	0	UFQFPN32	26	1.71	3.6	5	1	1	2	0	1 1	0 0	0	2	2	1	2	1	0	2	[2]	2	1	1 0	0	0	1	0	0	0	)	0	0	0	1	0	0 0	) .	1 1	1 0	0	0	0
STM32L442KCU6	80	ARM Cortex-M4	256	64	0	UFQFPN32	26	1.71	3.6	5	1	1	2	0	1 1	0 0	0	2	2	1	2	1	0	2	[2]	2	1	1 0	0	0	1	0	0	0	)	0	0	0	1	0	0 0	) .	1 1	1 0	0	0	0
STM32L452CCU6	80	ARM Cortex-M4	256	160	0	UFQFPN48	38	1.71	3.6	5	1	1	2	0	1 1	0 0	0	1	2	1	3	1	0	4	[4]	3+1	1	1 0	0	0	1	0	0	0	)	0	0	0	1	0	1 0	) (	0 1	1 0	0	0	0
STM32L452CEU6	80	ARM Cortex-M4	512	160	0	UFQFPN48	38	1.71	3.6	5	1	1	2	0	1 1	0 0	0	1	2	1	3	1	0	4	[4]	3+1	1	1 0	0	0	1	0	0	0	)	0	0	0	1	0	1 0	)	0 1	1 0	0 0	0	0
STM32L452RCT6	80	ARM Cortex-M4	256	160	0	LQFP64	52	1.71	3.6	5	1	1	2	0	1 1	6 0	0	1	2	1	3	1	0	4	[4]	3+1	1	1 1	0	0	1	0	0	0	)	0	0	0	1	0	1 0	)	0 1	1 0	0 0	0	0
STM32L452RCI6	80	ARM Cortex-M4	256	160	0	UFBGA64	52	1.71	3.6	5	1	1	2	0	1 1	6 0	0	1	2	1	3	1	0	4	[4]	3+1	1	1 1	0	0	1	0	0	0	)	0	0	0	1	0	1 0	) [	0 1	1 0	0	0	0
																					$\overline{}$			_											_		_	_	_								

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	低功耗定时器	高分牌客官寸器	12位ADC超道	16位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	SPI	QUADSPI	<u>-</u> % -	高速で	U(S)ART	低功耗UART	CAN	SDIO	FSMC	FMC	从USB	全速USB OTG	高東lise OTG 以太网	MDIOS	段式LCD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES	SHA	HMAC
STM32L452RET6	80	ARM Cortex-M4	512	160	0	LQFP64	52	1.71	3.6	5	1	1	2	) 1	1 16	0	0	1	2	1	3	1	0 4	[4	] 3+	1 1	1	1	0	0	1	0	0 0	0	0	0	0	1	0	1	0 (	0 1	1 0	0	0	0
STM32L452REI6	80	ARM Cortex-M4	512	160	0	UFBGA64	52	1.71	3.6	5	1	1	2	) 1	1 16	0	0	1	2	1	3	1	0 4	[4	] 3+	1 1	1	1	0	0	1	0	0 0	0	0	0	0	1	0	1	0 (	0 1	1 0	0	0	0
STM32L452REY6	80	ARM Cortex-M4	512	160	0	WLCSP64	52	1.71	3.6	5	1	1	2	) 1	1 16	0	0	1	2	1	3	1	0 4	[4	] 3+	1 1	1	1	0	0	1	0	0	0	0	0	0	1	0	1	0 (	0 1	1 0	0	0	0
STM32L452VCT6	80	ARM Cortex-M4	256	160	0	LQFP100	83	1.71	3.6	5	1	1	2	) 1	l 16	0	0	1	2	1	3	1	0 4	[4	] 3+	1 1	1	1	0	0	1	0	0 0	0	0	0	0	1	0	1	0 (	0 1	1 0	0	0	0
STM32L452VCl6	80	ARM Cortex-M4	256	160	0	UFBGA100	83	1.71	3.6	5	1	1	2	) 1	1 16	0	0	1	2	1	3	1	0 4	[4	] 3+	1 1	1	1	0	0	1	0	0 0	0	0	0	0	1	0	1	0 (	0 1	1 0	0	0	0
STM32L452VET6	80	ARM Cortex-M4	512	160	0	LQFP100	83	1.71	3.6	5	1	1	2	) 1	1 16	0	0	1	2	1	3	1	0 4	[4	] 3+	1 1	1	1	0	0	1	0	0 0	0	0	0	0	1	0	1	0 (	0 1	1 0	0	0	0
STM32L452VEI6	80	ARM Cortex-M4	512	160	0	UFBGA100	83	1.71	3.6	5	1	1	2	) 1	1 16	0	0	1	2	1	3	1	0 4	[4	] 3+	1 1	1	1	0	0	1	0	0 0	0	0	0	0	1	0	1	0 (	0 1	1 0	0	0	0
																	ST	M32L	4x3 l	JSB F	S & L	CD 7	<sup>÷</sup> 品线	- 80	MHz																					
STM32L433CBT6	80	ARM Cortex-M4	128	64	0	LQFP48	38	1.71	3.6	5	1	1	2	) 1	1 10	0	0	2	2	1	3	1	0 3	0	3	1	1	0	0	0	1	0 (	0 0	0	4x19	0	0	1	0	0	0 -	1 1	1 0	0	0	0
STM32L433CBU6	80	ARM Cortex-M4	128	64	0	UFQFPN48	38	1.71	3.6	5	1	1	2	) 1	1 10	0	0	2	2	1	3	1	0 3	0	3	1	1	0	0	0	1	0 (	0 0	0	4x19	0	0	1	0	0	0 -	1 1	1 0	0	0	0
STM32L433CBY6	80	ARM Cortex-M4	128	64	0	WLCSP49	39	1.71	3.6	5	1	1	2	) 1	1 10	0	0	2	2	1	3	1	0 3	0	3	1	1	0	0	0	1	0	0 0	0	4x19	0	0	1	0	0	0 -	1 1	1 0	0	0	0
STM32L433CCT6	80	ARM Cortex-M4	256	64	0	LQFP48	38	1.71	3.6	5	1	1	2	) 1	1 10	0	0	2	2	1	3	1	0 3	0	3	1	1	0	0	0	1	0	0 0	0	4x19	0	0	1	0	0	0	1 1	1 0	0 0	0	0
STM32L433CCU6	80	ARM Cortex-M4	256	64	0	UFQFPN48	38	1.71	3.6	5	1	1	2	) 1	1 10	0	0	2	2	1	3	1	0 3	0	3	1	1	0	0	0	1	0	0 0	0	4x19	0	0	1	0	0	0	1 1	1 0	0 0	0	0
STM32L433CCY6	80	ARM Cortex-M4	256	64	0	WLCSP49	39	1.71	3.6	5	1	1	2	) 1	1 10	0	0	2	2	1	3	1	0 3	0	3	1	1	0	0	0	1	0	0 0	0	4x19	0	0	1	0	0	0 -	1 1	1 0	0 0	0	0
STM32L433RBT6	80	ARM Cortex-M4	128	64	0	LQFP64	52	1.71	3.6	5	1	1	2	) 1	1 10	0	0	2	2	1	3	1	0 3	0	3	1	1	1	0	0	1	0	0 0	0	4x32/ 8x28	0	0	1	0	0	0 -	1 1	1 0	0 0	0	0
STM32L433RBI6	80	ARM Cortex-M4	128	64	0	UFBGA64	52	1.71	3.6	5	1	1	2	) 1	1 16	0	0	2	2	1	3	1	0 3	0	3	1	1	1	0	0	1	0	0 0	0	4x32/ 8x28	0	0	1	0	0	0	1 1	1 0	0 0	0	0
STM32L433RBY6	80	ARM Cortex-M4	128	64	0	WLCSP64	52	1.71	3.6	5	1	1	2	) 1	1 16	0	0	2	2	1	3	1	0 3	0	3	1	1	1	0	0	1	0 (	0 0	0	4x32/ 8x28	0	0	1	0	0	0 -	1 1	1 0	0 0	0	0
STM32L433RCT6	80	ARM Cortex-M4	256	64	0	LQFP64	52	1.71	3.6	5	1	1	2	) 1	1 16	0	0	2	2	1	3	1	0 3	0	3	1	1	1	0	0	1	0	0 0	0	4x32/ 8x28	0	0	1	0	0	0 -	1 1	1 0	0 0	0	0
STM32L433RCI6	80	ARM Cortex-M4	256	64	0	UFBGA64	52	1.71	3.6	5	1	1	2	) 1	1 16	0	0	2	2	1	3	1	0 3	0	3	1	1	1	0	0	1	0	0 0	0	4x32/ 8x28	0	0	1	0	0	0 -	1 1	1 0	0 0	0	0
STM32L433RCY6	80	ARM Cortex-M4	256	64	0	WLCSP64	52	1.71	3.6	5	1	1	2	) 1	1 16	0	0	2	2	1	3	1	0 3	0	3	1	1	1	0	0	1	0	0 0	0	4x32/ 8x28	0	0	1	0	0	0 -	1 1	1 0	0 0	0	0
STM32L433VCT6	80	ARM Cortex-M4	256	64	0	LQFP100	83	1.71	3.6	5	1	1	2	) 1	1 16	6 0	0	2	2	1	3	1	0 3	0	3	1	1	1	0	0	1	0	0 0	0	4x44/ 8x40	0	0	1	0	0	0 -	1 1	1 0	0 0	0	0
STM32L433VCI6	80	ARM Cortex-M4	256	64	0	UFBGA100	83	1.71	3.6	5	1	1	2	) 1	1 10	6 0	0	2	2	1	3	1	0 3	0	3	1	1	1	0	0	1	0	0 0	0	4x44/ 8x40	0	0	1	0	0	0 -	1 1	1 0	0 0	0	0
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产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通 用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机空制定时器低功耗定时器	高分辨率定时器	12位ADC转换单元	12位ADC通道	16位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	SPI	OHADED!	r°C	高速r°C	U(S)ART	低功耗UART	CAN	SUID	FMC	从USB	全速USB OTG	高速USB OTG	以太网	段式CD	TFT LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	DES/TDES	SHA	HMAC
STM32L443CCT6	80	ARM Cortex-M4	256	64	0	LQFP48	38	1.71	3.6	5	1	1 2	0	1	10	0	0	2	2	1	3 1	0	3	0	3	1	1 (	0 0	0	1	0	0	0 0	4x1	9 0	0	1	0	0	0	1	1 1	1 0	0	0
STM32L443CCU6	80	ARM Cortex-M4	256	64	0	UFQFPN48	38	1.71	3.6	5	1	1 2	0	1	10	0	0	2	2	1	3 1	0	3	0	3	1	1 (	0 0	0	1	0	0	0 0	4x1	9 0	0	1	0	0	0	1	1 1	1 0	0	0
STM32L443CCY6	80	ARM Cortex-M4	256	64	0	WLCSP49	39	1.71	3.6	5	1	1 2	0	1	10	0	0	2	2	1	3 1	0	3	0	3	1	1 (	0 0	0	1	0	0	0 0	4x1	9 0	0	1	0	0	0	1	1 1	1 0	0	0
STM32L443RCT6	80	ARM Cortex-M4	256	64	0	LQFP64	52	1.71	3.6	5	1	1 2	0	1	16	0	0	2	2	1	3 1	0	3	0	3	1	1	1 0	0	1	0	0	0 0	4x3 8x2		0	1	0	0	0	1	1 1	1 0	0	0
STM32L443RCI6	80	ARM Cortex-M4	256	64	0	UFBGA64	52	1.71	3.6	5	1	1 2	0	1	16	0	0	2	2	1	3 1	0	3	0	3	1	1	1 0	0	1	0	0	0 0	4x3 8x2		0	1	0	0	0	1	1 1	1 0	0	0
STM32L443RCY6	80	ARM Cortex-M4	256	64	0	WLCSP64	52	1.71	3.6	5	1	1 2	0	1	16	0	0	2	2	1	3 1	0	3	0	3	1	1	1 0	0	1	0	0	0 0	4x3 8x2		0	1	0	0	0	1	1 1	1 0	0	0
STM32L443VCT6	80	ARM Cortex-M4	256	64	0	LQFP100	83	1.71	3.6	5	1	1 2	0	1	16	0	0	2	2	1	3 1	0	3	0	3	1	1	1 0	0	1	0	0	0 0	4x4 8x4		0	1	0	0	0	1	1 1	1 0	0	0
STM32L443VCI6	80	ARM Cortex-M4	256	64	0	UFBGA100	83	1.71	3.6	5	1	1 2	0	1	16	0	0	2	2	1	3 1	0	3	0	3	1	1	1 0	0	1	0	0	0 0	4x4 8x4		0	1	0	0	0	1	1 1	1 0	0	0
																		<b>STM3</b>	2L4x	5 USI	B OTG	产品	线 - 8	O MI	lz																				
STM32L475RCT6	80	ARM Cortex-M4	256	128	0	LQFP64	51	1.71	3.6	9	2	2 2	0	3	16	0	0	2	2	2	3 1	0	3	0	3+2	1	1	1 0	0	0	1	0	0 0	0	0	0	2	0	1	0	1	1 0	0	0	0
STM32L475RET6	80	ARM Cortex-M4	512	128	0	LQFP64	51	1.71	3.6	9	2	2 2	0	3	16	0	0	2	2	2	3 1	0	3	0	3+2	1	1	1 0	0	0	1	0	0 0	0	0	0	2	0	1	0	1	1 0	0	0	0
STM32L475RGT6	80	ARM Cortex-M4	1024	128	0	LQFP64	51	1.71	3.6	9	2	2 2	0	3	16	0	0	2	2	2	3 1	0	3	0	3+2	1	1	1 0	0	0	1	0	0 0	0	0	0	2	0	1	0	1	1 0	0	0	0
STM32L475VCT6	80	ARM Cortex-M4	256	128	0	LQFP100	82	1.71	3.6	9	2	2 2	0	3	16	0	0	2	2	2	3 1	0	3	0	3+2	1	1	1 1	0	0	1	0	0 0	0	0	0	2	0	1	0	1	1 0	0	0	0
STM32L475VET6	80	ARM Cortex-M4	512	128	0	LQFP100	82	1.71	3.6	9	2	2 2	0	3	16	0	0	2	2	2	3 1	0	3	0	3+2	1	1	1 1	0	0	1	0	0 0	0	0	0	2	0	1	0	1	1 0	0	0	0
STM32L475VGT6	80	ARM Cortex-M4	1024	128	0	LQFP100	82	1.71	3.6	9	2	2 2	0	3	16	0	0	2	2	2	3 1	0	3	0	3+2	1	1	1 1	0	0	1	0	0 0	0	0	0	2	0	1	0	1	1 0	0 0	0	0
																	STIM	32L4	x6 U	SB O	ΓG & L	.CD产	品线	- 80	MHz																				
STM32L476RCT6	80	ARM Cortex-M4	256	128	0	LQFP64	51	1.71	3.6	9	2	2 2	0	3	16	0	0	2	2	2	3 1	0	3	[3]	3+2	1	1	1 0	0	0	1	0	0 0	8x2 4x3	3/ 2 0	0	2	0	1	0	1	1 0	0	0	0
STM32L476RET6	80	ARM Cortex-M4	512	128	0	LQFP64	51	1.71	3.6	9	2	2 2	0	3	16	0	0	2	2	2	3 1	0	3	[3]	3+2	1	1	1 0	0	0	1	0	0 0	8x2 4x3		0	2	0	1	0	1	1 0	0	0	0
STM32L476RGT6	80	ARM Cortex-M4	1024	128	0	LQFP64	51	1.71	3.6	9	2	2 2	0	3	16	0	0	2	2	2	3 1	0	3	[3]	3+2	1	1	1 0	0	0	1	0	0 0	8x2 4x3		0	2	0	1	0	1	1 0	0	0	0
STM32L476JEY6	80	ARM Cortex-M4	512	128	0	WLCSP72	57	1.71	3.6	9	2	2 2	0	3	16	0	0	2	2	2	3 1	0	3	[3]	3+2	1	1	1 0	0	0	1	0	0 0	8x2 4x3		0	2	0	1	0	1	1 0	0	0	0
STM32L476JGY6	80	ARM Cortex-M4	1024	128	0	WLCSP72	57	1.71	3.6	9	2	2 2	0	3	16	0	0	2	2	2	3 1	0	3	[3]	3+2	1	1	1 0	0	0	1	0	0 (	8x2 4x3	3/ 2 0	0	2	0	1	0	1	1 0	0 0	0	0
STM32L476MEY6	80	ARM Cortex-M4	512	128	0	WLCSP81	65	1.71	3.6	9	2	2 2	0	3	16	0	0	2	2	2	3 1	0	3	[3]	3+2	1	1	1 0	0	0	1	0	0 (	8x3 4x3	0/2 0	0	2	0	1	0	1	1 0	0	0	0

产品型号	主频 (MHz)	内 核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用10	最低工作电压	最高工作电压	16位定时器	32位定时器	电机控制定时器	氏力毛定付器 	高分解释足寸器 -	12立ADC转换单元	16位ADC转换单元	16位ADC通道	12位DAC	比较器	放大器	SPI	QUADSPI	l²S	l²C	高速rc	U(S)ART	低功耗UART	CAN	FSMC	FMC	从USB	全速USB OTG	高速USB OTG	以太网	MDIOS	段式LCD	TET LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DGMI	SWPMI	TRNG	DES/TDES AES	SHA	НМАС
STM32L476MGY6	80	ARM Cortex-M4	1024	128	0	WLCSP81	65	1.71	3.6	9	2	2	2	)	3 1	6 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	0	0	0	1	0	0		8x30/ 4x32	0	0	2	0	1 (	0	1	1 (	0 0	0	0
STM32L476VCT6	80	ARM Cortex-M4	256	128	0	LQFP100	82	1.71	3.6	9	2	2	2	)	3 1	6 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	1	0	0	1	0	0		8x40/ 4x44	0	0	2	0	1 (	0	1	1	0 0	0	0
STM32L476VET6	80	ARM Cortex-M4	512	128	0	LQFP100	82	1.71	3.6	9	2	2	2	)	3 1	6 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	1	0	0	1	0	0		8x40/ 4x44	0	0	2	0	1 (	0	1	1	0 0	0	0
STM32L476VGT6	80	ARM Cortex-M4	1024	128	0	LQFP100	82	1.71	3.6	9	2	2	2	)	3 1	6 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	1	0	0	1	0	0		8x40/ 4x44	0	0	2	0	1 (	0	1	1	0 0	0	0
STM32L476QEI6	80	ARM Cortex-M4	512	128	0	UFBGA132	109	1.71	3.6	9	2	2	2	)	3 1	9 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	1	0	0	1	0	0		8x40/ 4x44	0	0	2	0	1 (	0	1	1 (	0 0	0	0
STM32L476QGI6	80	ARM Cortex-M4	1024	128	0	UFBGA132	109	1.71	3.6	9	2	2	2	)	3 1	9 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	1	0	0	1	0	0	(1)	8x40/ 4x44	0	0	2	0	1 (	0	1	1	0 0	0	0
STM32L476ZET6	80	ARM Cortex-M4	512	128	0	LQFP144	114	1.71	3.6	9	2	2	2	0	3 2	4 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	1	0	0	1	0	0		8x40/ 4x44	0	0	2	0	1 (	0	1	1 (	0 0	0	0
STM32L476ZGT6	80	ARM Cortex-M4	1024	128	0	LQFP144	114	1.71	3.6	9	2	2	2	0	3 2	4 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	1	0	0	1	0	0		8x40/ 4x44	0	0	2	0	1 (	0	1	1 (	0 0	0	0
STM32L486RGT6	80	ARM Cortex-M4	1024	128	0	LQFP64	51	1.71	3.6	9	2	2	2	)	3 1	6 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	0	0	0	1	0	0		8x28/ 4x32	0	0	2	0	1 (	0	1	1	1 0	0	0
STM32L486JGY6	80	ARM Cortex-M4	1024	128	0	WLCSP72	57	1.71	3.6	9	2	2	2	)	3 1	6 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	0	0	0	1	0	0		8x28/ 4x32	0	0	2	0	1 (	0	1	1	1 0	0	0
STM32L486VGT6	80	ARM Cortex-M4	1024	128	0	LQFP100	82	1.71	3.6	9	2	2	2	)	3 1	6 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	1	0	0	1	0	0	11	8x40/ 4x44	0	0	2	0	1 (	0	1	1	1 0	0	0
STM32L486QGI6	80	ARM Cortex-M4	1024	128	0	UFBGA132	109	1.71	3.6	9	2	2	2	)	3 1	9 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	1	0	0	1	0	0		8x40/ 4x44	0	0	2	0	1 (	0	1	1	1 0	0	0
STM32L486ZGT6	80	ARM Cortex-M4	1024	128	0	LQFP144	114	1.71	3.6	9	2	2	2	)	3 2	4 0	0	2	2	2	3	1	0	3	[3]	3+2	1	1 1	1	0	0	1	0	0	(1)	8x40/ 4x44	0	0	2	0	1 (	0	1	1	1 0	0	0
STM32L496RET6	80	ARM Cortex-M4	512	320	0	LQFP64	52	1.71	3.6	9	2	2	2	0	3 1	6 0	0	2	2	2	3	1	0	4	[4]	3+2	1	2 1	0	0	0	1	0	0		8x40/ 4x44	0	0	2	0	1	1	1	1	0 0	0	0
STM32L496RGT6	80	ARM Cortex-M4	1024	320	0	LQFP64	52	1.71	3.6	9	2	2	2	0 :	3 1	6 0	0	2	2	2	3	1	0	4	[4]	3+2	1	2 1	0	0	0	1	0	0	11	8x40/ 4x44	0	0	2	0	1 1	1	1	1 (	0 0	0	0
STM32L496VET6	80	ARM Cortex-M4	512	320	0	LQFP100	83	1.71	3.6	9	2	2	2	) :	3 1	6 0	0	2	2	2	3	1	0	4	[4]	3+2	1	2 1	1	0	0	1	0	0		8x40/ 4x44	0	0	2	0	1 1	1	1	1 (	0 0	0	0
STM32L496VGT6	80	ARM Cortex-M4	1024	320	0	LQFP100	83	1.71	3.6	9	2	2	2	)	3 1	6 0	0	2	2	2	3	1	0	4	[4]	3+2	1	2 1	1	0	0	1	0	0		8x40/ 4x44	0	0	2	0	1 1	1	1	1 (	0 0	0	0
STM32L496VGY6	80	ARM Cortex-M4	1024	320	0	WLCSP100	83	1.71	3.6	9	2	2	2	)	3 1	6 0	0	2	2	2	3	1	0	4	[4]	3+2	1	2 1	1	0	0	1	0	0	(1)	8x40/ 4x44	0	0	2	0	1 '	1	1	1	0 0	0	0
STM32L496QEI6	80	ARM Cortex-M4	512	320	0	UFBGA132	110	1.71	3.6	9	2	2	2	)	3 1	9 0	0	2	2	2	3	1	0	4	[4]	3+2	1	2 1	1	0	0	1	0	0		8x40/ 4x44	0	0	2	0	1 1	1	1	1 (	0 0	0	0
STM32L496QGI6	80	ARM Cortex-M4	1024	320	0	UFBGA132	110	1.71	3.6	9	2	2	2	)	3 1	9 0	0	2	2	2	3	1	0	4	[4]	3+2	1	2 1	1	0	0	1	0	0		8x40/ 4x44	0	0	2	0	1 '	1	1	1	0 0	0	0
STM32L496ZET6	80	ARM Cortex-M4	512	320	0	LQFP144	115	1.71	3.6	9	2	2	2	0	3 2	4 0	0	2	2	2	3	1	0	4	[4]	3+2	1	2 1	1	0	0	1	0	0		8x40/ 4x44	0	0	2	0	1 '	1	1	1 (	0 0	0	0

产品型号	主频 (MHz)	内核	FLASH (KB)	RAM (KB)	EEPROM (B)	封装	通用IO	最低工作电压	最高工作电压	16位定时器	32位定时器	电机空制定时器	低功耗定付器 	高分辨率定时器	12位ADC转换单元	12位ADC通道	16过ADC转换单元	16位ADC通道	12妇DAC	比较器	放大器	SPI	QUADSPI	l <sup>2</sup> S	l²C	高速r°C	U(S)ART	低功耗UART	CAN	SDIO	FONC	从USB	全速USB OTG	高速USB OTG	以太网	MDIOS	段式LCD	TFI LCD	DSI HOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	TRNG	AES	DES/TDES	SHA	HMAC
STM32L496ZGT6	80	ARM Cortex-M4	1024	320	0	LQFP144	115	1.71	3.6	9	2	2	2	0	3	24	0	0	2	2	2	3	1	0	4	[4]	3+2	1	2	1	0	0	1	0	0	0	8x40/ 4x44	0	0	2	0	1	1	1	1	0	0	0	0
STM32L496AEI6	80	ARM Cortex-M4	512	320	0	UFBGA169	136	1.71	3.6	9	2	2	2	0	3	24	0	0	2	2	2	3	1	0	4	[4]	3+2	1	2	1	O	0	1	0	0	0	8x40/ 4x44	0	0	2	0	1	1	1	1	0	0	0	0
STM32L496AGI6	80	ARM Cortex-M4	1024	320	0	UFBGA169	136	1.71	3.6	9	2	2	2	0	3	24	0	0	2	2	2	3	1	0	4	[4]	3+2	1	2	1	0	0	1	0	0	0	8x40/ 4x44	0	0	2	0	1	1	1	1	0	0	0	0
STM32L4A6RGT6	80	ARM Cortex-M4	1024	320	0	LQFP64	52	1.71	3.6	9	2	2	2	0	3	16	0	0	2	2	2	3	1	0	4	[4]	3+2	1	2	1 (	) (	0	1	0	0	0	8x40/ 4x44	0	0	2	0	1	1	1	1	1	0	1	1
STM32L4A6VGT6	80	ARM Cortex-M4	1024	320	0	LQFP100	83	1.71	3.6	9	2	2	2	0	3	16	0	0	2	2	2	3	1	0	4	[4]	3+2	1	2	1 '	C	0	1	0	0	0	8x40/ 4x44	0	0	2	0	1	1	1	1	1	0	1	1
STM32L4A6VGY6	80	ARM Cortex-M4	1024	320	0	WLCSP100	83	1.71	3.6	9	2	2	2	0	3	16	0	0	2	2	2	3	1	0	4	[4]	3+2	1	2	1 '	C	0	1	0	0	0	8x40/ 4x44	0	0	2	0	1	1	1	1	1	0	1	1
STM32L4A6QGI6	80	ARM Cortex-M4	1024	320	0	UFBGA132	110	1.71	3.6	9	2	2	2	0	3	19	0	0	2	2	2	3	1	0	4	[4]	3+2	1	2	1	C	0	1	0	0	0	8x40/ 4x44	0	0	2	0	1	1	1	1	1	0	1	1
STM32L4A6ZGT6	80	ARM Cortex-M4	1024	320	0	LQFP144	115	1.71	3.6	9	2	2	2	0	3	24	0	0	2	2	2	3	1	0	4	[4]	3+2	1	2	1 '	0	0	1	0	0	0	8x40/ 4x44	0	0	2	0	1	1	1	1	1	0	1	1
STM32L4A6AGI6	80	ARM Cortex-M4	1024	320	0	UFBGA169	136	1.71	3.6	9	2	2	2	0	3	24	0	0	2	2	2	3	1	0	4	[4]	3+2	1	2	1	O	0	1	0	0	0	8x40/ 4x44	0	0	2	0	1	1	1	1	1	0	1	1

#### 缩写和封装

#### 缩写

ADC LCD Analog-to-digital converter Liquid crystal display ART Auto-reload timer LIN Local interconnect network **ATAPI** AT attachment packet interface LVD Low voltage detection AWU Auto wake-up from halt MAC Multiply accumulator BI PD MC Byte level protocol decoder Motor control BOD Brown-out detector MFT Multifunction timer MMC CAN Controller area network MultiMediaCard CAPCOM Capture compare NMI Non-maskable interrupt CSS OSG Oscillator safeguard Clock security system DALL Digital addressable lighting interface PCA Programmable counter array DDC PDR Power-down reset Data display channel DiSEqC Digital satellite equipment control PHW Programmable halt wake-up Direct memory access PEC Peripheral event controller DMA DSC Dual supply control PID Programmable logic device DTC Data transfer coprocessor PLL Phase locked loop ETM Embedded trace macrocell POR Power-on reset Programmable voltage detector FMI External memory interface PVD **HDLC** High-level data link control **PVR** Programmable voltage regulator Pulse width modulation In-application programming **PWM** IAP IC/OC Input capture/output compare **ROP** Readout protection ICP RTC programming Real-time clock timer SAL Serial Audio Interface IR Infrared Infrared data association SC IrDA Smartcard ISP In-situ programming SCI Serial communication interface Inter-integrated circuit SCR 12C Smartcard reader Inter-IC sound **SDIO** 12S Secure digital input output

**SDMMC** 

SMI

Secure Digital / Multi Media Card

Serial memory interface

SPI Serial peripheral interface SSC Single-cycle switching support SSP Synchronous serial port TBU Time base unit TLI Top level interrupt Universal asynchronous receiver UART transmitter **USART** Universal sync/async receiver transmitter USB Universal Serial Bus

#### 封装

WDG

WWDG

DIP : Dual in-line package LCC : Leaded chip carrier

PDIP Shrink: Shrink Plastic Dual In-line

Watchdog timer

Window watchdog timer

Package

PQFP : Plastic quad flat package

SO : Small outline

LQFP : Low-profile quad flat package

PBGA : Plastic ball grid array
DFN : Dual flat no-lead
QFN : Quad flat no-lead

WLCSP: Wafer-Level Chip-Scale Package

#### STM32 & STM8产品型号



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