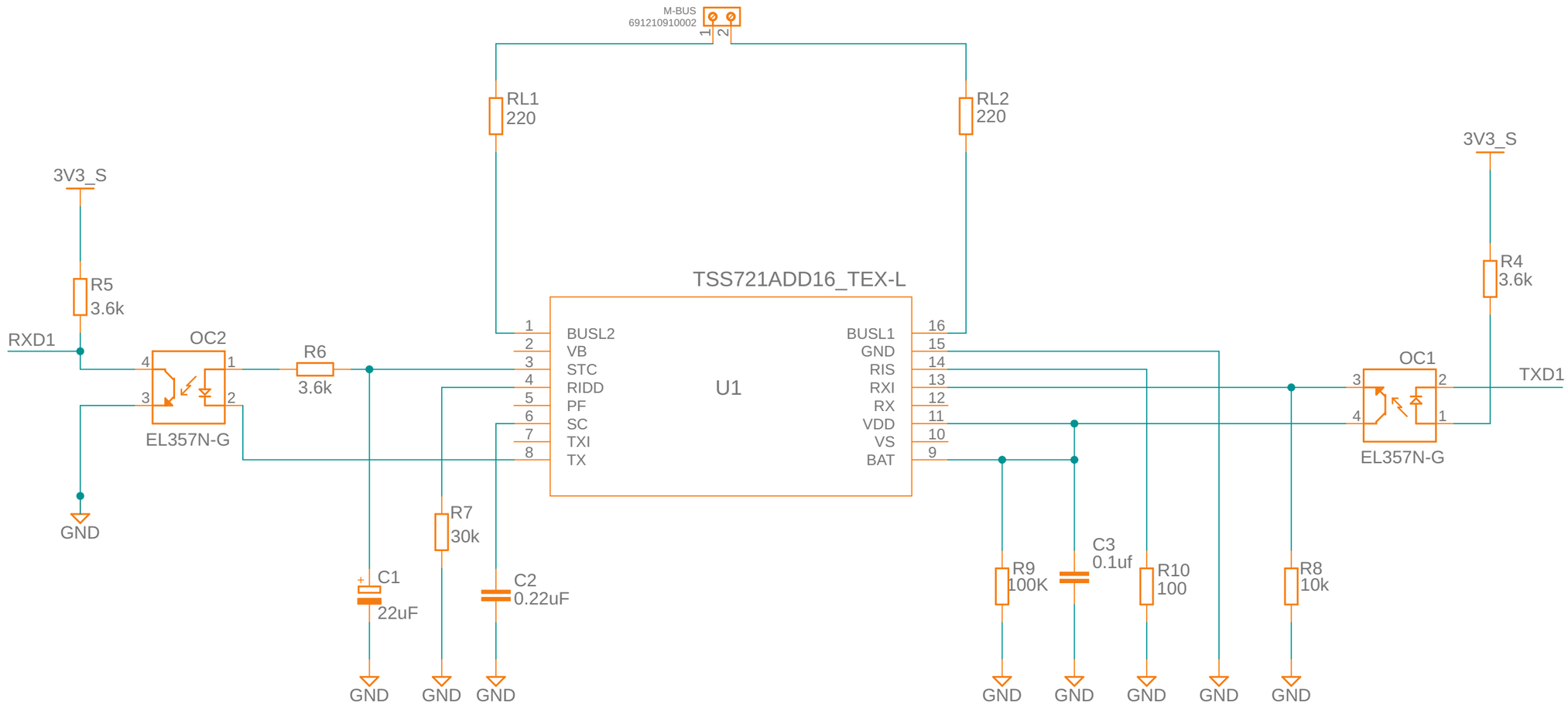

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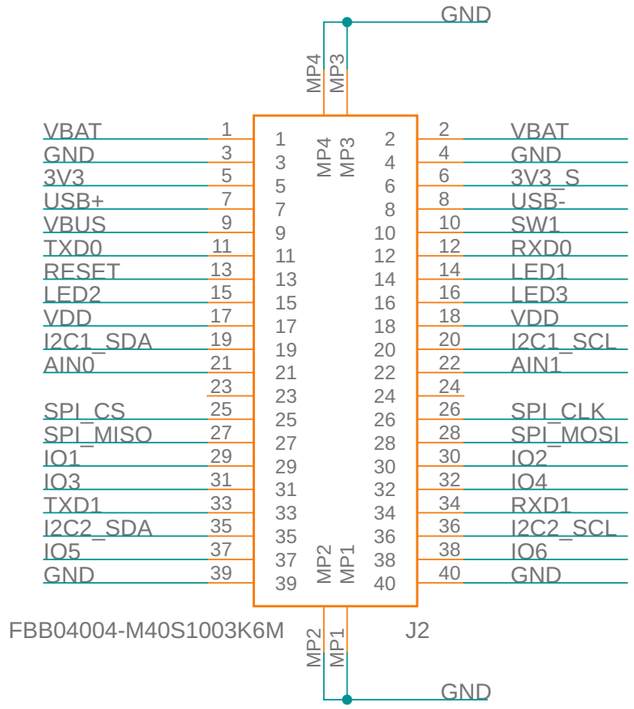
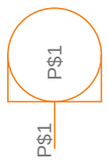
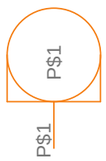


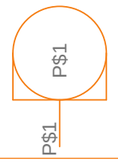
Hole for IO support



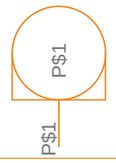
Hole for IO support



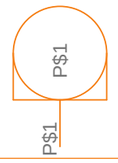
Hole for IO fix



Hole for IO fix



Hole for IO fix



Pin Number	Pin Name	Description
1	VBAT	Power supply from battery
2	VBAT	Power supply from battery
3	GND	Ground
4	GND	Ground
5	3V3	3.3V power supply
6	3V3_S	3.3V power supply, controlled by CPU module
7	USB+	USB D+
8	USB-	USB D-
9	VBUS	5V input for USB
10	SW1	Switch connection
11	TXD0	MCU UART0 TX signal
12	RXD0	MCU UART0 RX signal
13	RESET	Reset switch, for MCU reset
14	LED1	LED for battery charge indicator
15	LED2	LED for custom used
16	LED3	LED for custom used
17	VDD	GPIO voltage of MCU module
18	VDD	GPIO voltage of MCU module
19	I2C1_SDA	#1 I2C data signal
20	I2C1_SCL	#2 I2C clock signal
21	AIN0	Analog input for ADC
22	AIN1	Analog input for ADC
23	NC	Not connect
24	NC	Not connect
25	SPI_CS	SPI chip select signal
26	SPI_CLK	SPI clock
27	SPI_MISO	SPI MISO signal
28	SPI_MOSI	SPI MOSI signal
29	IO1	Gerneral purpose IO
30	IO2	Used for 3V3_S enable
31	IO3	Gerneral purpose IO
32	IO4	Gerneral purpose IO
33	TXD1	MCU UART1 TX signal
34	RXD1	MCU UART1 RX signal
35	I2C2_SDA	#2 I2C data signal
36	I2C2_SCL	#2 I2C clock signal
37	IO5	Gerneral purpose IO
38	IO6	Gerneral purpose IO

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