Duration Calculus

1. Symbols:	$true, false, =, <, >, \le, \ge, f, g,$	X, Y, Z,	x, y, z	
1.1 Predicate Sy	mbols: $true, false, =, <, >, \leq, \geq$			$\mathbb{R}^n \to \mathbb{B}$
1.2 Function Syn	nbols: +, -			$\mathbb{R}^n o \mathbb{R}$

1.3 State variables and Domain values: State variable $X \to \mathcal{D}(X) = \{d_1, \dots, d_n\}$ $\mathcal{I}: Obs \to (\operatorname{Time} \to \mathcal{D})$

 $\mathcal{I}(X): \mathrm{Time} \to \mathcal{D}(X)$

 $\mathcal{I}(X)(t) \in \mathcal{D}(X)$ denotes value X has at time $t \in \mathrm{Time}$ 1.4 Global Variables: x, y, z $\mathcal{V} : \mathrm{GVar} \to \mathbb{R}$

2. State Assertions: $P := 0 \mid 1 \mid X = d \mid \neg P_1 \mid P_1 \land P_2$

3. Terms: $\theta := x \mid \ell \mid \int P \mid f(\theta_1, \dots, \theta_n)$

Rigid Term: no l, nor \int operators

3.1 Semantics $\mathcal{I}\llbracket\theta\rrbracket: \operatorname{Val} \times \operatorname{Intv} \to \mathbb{R}$ $\mathcal{I}\llbracket\theta\rrbracket(\mathcal{V}, [b, e])$

5. Abbreviations:

 $\theta_1 + \theta_2 = +(\theta_1, \theta_2)$

 $\Diamond F := true; F; true \\ \Box F = \neg \Diamond \neg F$

7. Laws of the DC Integral operator:
$$\models ((\int P = r_1); (\int P = r_2)) \Rightarrow \int P = (r_1 + r_2) \\ \models \lceil \neg P \rceil \Rightarrow \int P = 0 \\ \models \int P \leq l \\ \models \lceil \rceil \Rightarrow \int P = 0$$

3. Terms - Info

$$\begin{array}{lll} \mathcal{V}(x) = 20 & \theta = x \cdot \int L & [b,e] = [0.5,3.25] \\ \Rightarrow \mathcal{I}\llbracket\theta\rrbracket \left(\mathcal{V}, [0.5,3.25]\right) = \mathcal{I}\llbracketx \cdot \int L\rrbracket \left(\mathcal{V}, [0.5,3.25]\right) = \mathcal{I}\llbracket\widehat{\cdot}(x,\int L)\rrbracket \left(\mathcal{V}, [0.5,3.25]\right) = \\ \widehat{\cdot} \left(\mathcal{I}\llbracketx\rrbracket \left(\mathcal{V}, [0.5,3.25]\right), \mathcal{I}\llbracket\int L\rrbracket \left(\mathcal{V}, [0.5,3.25]\right)\right) = \\ \widehat{\cdot} \left(20,\int_{0.5}^{3.25} L_{\mathcal{I}}(t) \, \mathrm{d}t\right) = \widehat{\cdot} (20,1.25) = 20 \cdot 1.25 = 25 \end{array}$$

Validity, Realizability, satisfyability

Holds: For a certain interpretation, evaluation, in a specific interval $\mathcal{I}, \mathcal{V}, [b, e] \models F \Leftrightarrow \mathcal{I} \llbracket F \rrbracket (\mathcal{V}, [b, e]) = tt$

Satisfies: For all interpretations and evaluations within a certain interval set $\mathcal{I}, \mathcal{V} \models F \Leftrightarrow \forall [b,e] \in \text{Intervals} : \mathcal{I}, \mathcal{V}, [b,e] \models F$

Realises: For a certain Interpretation and for all intervals

 $\mathcal{I} \models F \Leftrightarrow \forall \mathcal{V} \in \text{Valuations} : \mathcal{I}, \mathcal{V} \models F$

Valid:

 $\models F \Leftrightarrow \forall \mathcal{I}: \mathcal{I} \models F$ Satisfiable \Leftarrow Realisable \Leftarrow Valid

Decidability Results for DC

	Discrete Time	Continuous Time		Discrete Time	Continuous Time
RDC	1. decidable	deci dable	= ? ([P]; [P])	.,,	Yes,
$RDC + \ell = r$	decidable for $r \in \mathbb{N}$	undecidable for $r \in \mathbb{R}^+$	$\Longrightarrow \lceil P \rceil$	Yes	smallest $e - b = 2$
$RDC + \int P_1 = \int P_2$	un de ci da b le	un deci da ble			
$RDC + \ell = x, \forall x$	un de ci da b le	2. undecidable	\models ? $\lceil P \rceil \Longrightarrow$	Yes	No,
DC	undecidable	un deci da b le	$(\lceil P \rceil; \lceil P \rceil)$	100	smallest $e - b = 1$

RDC in Discrete Time: $F := \lceil P \rceil \mid \neg F_1 \mid F_1 \lor F_2 \mid F_1; F_2$,

where P is a state assertion with boolean observables only

Idea: Give a procedure to construct a formula F,

a regular language $\mathcal{L}(F)$, such that $\mathcal{I}[0, n] \models F$

iff $w \in \mathcal{L}(F)$, where w describes \mathcal{I} on [0, n]

Then F is satisfiable in discrete time iff $\mathcal{L}(F)$ is not empty

 \Rightarrow Emptyness problems are decidable for regular languages

$$\begin{split} \Sigma(F) = & \{ (X \wedge Y \wedge Z) \,, (X \wedge Y \wedge \neg Z) \,, (X \wedge \neg Y \wedge Z) \,, \\ & (X \wedge \neg Y \wedge \neg Z) \,, (\neg X \wedge Y \wedge Z) \,, (\neg X, \wedge Y \wedge \neg Z) \,, \\ & (\neg X \wedge \neg Y \wedge Z) \,, (\neg X \wedge \neg Y \wedge \neg Z) \} \end{split}$$

Disjunctive Normal Form (DNF):

$$P = (X \land \neg Y) \iff P = (X \land \neg Y \land Z) \lor (X \land \neg Y \land \neg Z)$$

 $RDC + \ell = x, \forall x \text{ in Continuous Time:}$

$$F ::= \lceil P \rceil \mid \neg F_1 \mid F_1 \vee F_2 \mid F_1; F_2 \mid \ell = 1 \mid \ell = x \mid \forall x \bullet F_1$$

Reduce divergence of two counter-machines to realisability from 0. $\mathcal{M} = (Q, q_0, q_{fin}, Prog)$

The (!) computation of \mathcal{M} is a finite sequence of the form $K_0 = (q_0, 0, 0) \vdash K_1 \vdash K_2 \ldots \vdash (q_{fin}, n_1, n_2)$

or an infinite sequence of the form

 $K_0 = (q_0, 0, 0) \vdash K_1 \vdash K_2 \dots$

A single configuration K of $\mathcal M$ can be encoded in an interval of length 4

Being an encoding interval can be characterised by a DC formula Being an encoding of the run can be characterised by a DC Formula $\mathcal{F}(\mathcal{M})$ Then \mathcal{M} diverges iff $\mathcal{F}(\mathcal{M}) \land \neg \Diamond \lceil q_{fin} \rceil$ is realisable from 0.

DC Standard Form

(Timed) leads to: $F \xrightarrow{\theta} \lceil P \rceil : \Leftrightarrow (F \land \ell = \theta) \rightarrow \lceil P \rceil$

(Timed) up to:
$$F \xrightarrow{\leq \theta} \lceil P \rceil : \Leftrightarrow (F \land \ell \leq \theta) \rightarrow \lceil P \rceil$$

Followed-by-initially: $F \xrightarrow[]{} \lceil P \rceil : \iff \neg (F; \lceil \neg P \rceil)$

(Timed) up-to-initally: $F \xrightarrow{\leq \theta} _0 \lceil P \rceil : \iff (F \land l \leq \theta) \longrightarrow _0 \lceil P \rceil$

Initialisation: $\lceil \rceil \vee \lceil P \rceil; true$

DC Implementables

1. Initialisation: $[] \vee [\pi]; true$

Initially, the control automaton is in phase π .

2. Sequencing: $\lceil \pi \rceil \longrightarrow \lceil \pi \vee \pi_1 \vee \ldots \vee \pi_n \rceil$

When the control automaton is in π , it subsequently stays in π or moves to one of π_1, \ldots, π_n .

3. Progress: $[\pi] \xrightarrow{\theta} [\neg \pi]$

After the control automaton stayed in phase π for θ time units, it subsequently leaves this phase, thus progress

4. Synchronisation: $[\pi \land \varphi] \xrightarrow{\theta} [\neg \pi]$

After the control automation stayed for θ time units in phase π with the condition φ being true, it subsequently leaves this phase.

5. Bounded stability: $\lceil \neg \pi \rceil$; $\lceil \pi \land \varphi \rceil \xrightarrow{\leq \theta} \lceil \pi \lor \pi_1 \lor \ldots \lor \pi_n \rceil$ If the control automaton

6. Unbound stability: $\lceil \neg \pi \rceil$; $\lceil \pi \land \varphi \rceil \longrightarrow \lceil \pi \lor \pi_1 \lor \ldots \lor \pi_n \rceil$ If the control automaton

7. Bounded inital stability: $\lceil \pi \land \varphi \rceil \xrightarrow{\leq \theta}_0 \lceil \pi \lor \pi_1 \lor \ldots \lor \pi_n \rceil$ If the control automaton

8. Unbounded inital stability: $\lceil \pi \wedge \varphi \rceil \longrightarrow_0 \lceil \pi \vee \pi_1 \vee \ldots \vee \pi_n \rceil$

If the control automaton

Difference between ABC A B

C D

E F

HIER NOCH IRGENDWAS ZU DC IMPLEMENTABLES

Networks of Timed Automata Timed Automaton $A = (L, B, X, I, E, \ell_{ini})$ Edges $E = (\ell, \alpha, \varphi, Y, \ell')$ 1. Handshake Edges: $(\ell_1, \alpha, \varphi_1, Y_1, \ell'_1) \in E_1$ $(\ell_2, \alpha, \varphi_2, Y_2, \ell_2') \in E_2$ Locations: $L = \{ \text{off, light, bright} \}$ 2. Asynchronous Edges: $((\ell_1, \ell_2), \tau, \varphi_1 \land \varphi_2, Y_1 \cup Y_2, (\ell'_1, \ell'_2)) \in E$ Alphabet: $B = \{press\}$ Clocks: $X = \{x\}$ If $(\ell_1, \alpha, \varphi_1, Y_1, \ell'_1) \in E_1$ then $\forall \ell_2 \in E_2 \bullet ((\ell_1, \ell_2), \alpha, \varphi, Y_1, (\ell'_1, \ell_2)) \in E$ $I = \{ \text{off} \mapsto \text{true}, \text{light} \mapsto \text{true}, \text{bright} \mapsto \text{true} \}$ Invariants: $E = \{(\text{off, press?, true, } \{x\}, \text{ light)}, (\text{light, press?, } x > 0, \emptyset, \text{ off}), \}$ Edges: 3. Channel Hiding: (Introduces local channels) (light, press?, x<3, Ø, bright), (bright, press?, true, Ø, off)} $(\ell, \alpha, \varphi, Y, \ell') \in E'$ Inital location: $\ell_{ini} = off$ 4. Closed Networks: Hiding all channel transitions of given channel 1. Clock Constraints: $\varphi := x \sim c \mid x - y \sim c \mid \varphi_1 \land \varphi_2 \ , (\sim \in \{<, ><, \geq, \leq\})$ 2. Clock Valuations: $\mathcal{V}: X \to \text{Time}$, assigning each clock $x \in X$ the current time $\nu(x)$ all channel yields a closed network) 2.1 Time Shift: $(\nu + t)(x) = \nu(x) + t$ 5. Operational Semantics of Networks: $V: \{x \mapsto 3.0\} \Rightarrow (V + 0.27) = \nu(x) + 0.27 = 3.0 + 0.27 = 3.27$ 5.1 Local Transitions: $\langle \vec{\ell}, \nu \rangle \xrightarrow{\alpha} \langle \vec{\ell}', \nu \rangle$ $\langle \vec{\ell}, \nu \rangle \xrightarrow{\tau} \langle \vec{\ell}', \nu \rangle$ if there is an $i \in \{1, \ldots, n\}$ such that $(\ell_i, \alpha, \varphi, Y, \ell_i) \in E, \quad \alpha \in B_{?!}$ 3.1 Time or Delay Transition: $\langle \ell, \nu \rangle \xrightarrow{t} \langle \ell, \nu + t \rangle$ iff $\forall t \in \text{Time} : \nu + t \models I(\ell)$ $(\ell_i, b!, \varphi_i, Y_i, \ell_i) \in E_i$ Some Time elapses respecting invariants, locations unchanged $\vec{\ell}_i' = \vec{\ell} \left[\ell_i \coloneqq \ell_i' \right]$ 3.2 Action or Discrete Transition: $\langle \ell, \nu \rangle \xrightarrow{\alpha} \langle \ell', \nu' \rangle$ iff $\nu \models \varphi, \nu' = \nu \, [Y := 0], \nu' \models I(\ell')$ $\nu' = \nu [Y \coloneqq 0]$ $\nu \models \varphi_i \land \varphi_j$ An Action occurs, location, clocks may change / reset, time does not elapse $\nu' \models I(\ell'_i)$ $\vec{\ell}_i' = \vec{\ell}[\ell_i := \ell_i'][\ell_j := \ell_j']$ 3.3 Transition Sequences: $\langle \ell_0, \nu_0 \rangle \xrightarrow{\lambda_1} \langle \ell_1, \nu_1 \rangle \xrightarrow{\lambda_2} \dots$ with $\langle \ell_0, \nu_0 \rangle \in C_{ini}$ $\lambda \in B \lor \lambda \in \text{Time } [-0.1 \text{cm}] \text{ (finite or infinite)}$ 5.3 Delay Transitions: $\langle \vec{\ell}, \nu \rangle \xrightarrow{t} \langle \vec{\ell}, \nu + t \rangle$ if $\forall t \in [0, t]$ $\nu + t' \models \bigwedge_{k=1}^{n} I_k(\ell_k)$ $\nu' = \nu \left[Y_i \cup Y_i \coloneqq 0 \right]$ $\nu' \models I(\ell'_i) \land I(\ell'_i)$ 4. Reachability: A configuration $\langle \ell, \nu \rangle$ is called reachable (in \mathcal{A}), iff there is a transition sequence of the form: Location Reachability / The Region Automata $\langle \ell_0, \nu_0 \rangle \xrightarrow{\lambda_1} \langle \ell_1, \nu_1 \rangle \xrightarrow{\lambda_2} \langle \ell_2, \nu_2 \rangle \xrightarrow{\lambda_3} \dots \xrightarrow{\lambda_n} \langle \ell_n, \nu_n \rangle = \langle \ell, \nu \rangle$ Given: A timed automata A and one of its location & 5. Time Stamped Configurations: $\langle \ell, \nu \rangle$, t is a timed-stamped configuration Question: Is ℓ reachable? That is, if there is a transition sequence of the form 5.1 Time Stampe,d Delay Transition: iff $t' \in \text{Time and } \langle \ell, \nu \rangle, t \xrightarrow{t'} \langle \ell, \nu + t' \rangle$ $\langle \ell, \nu \rangle, t \xrightarrow{t'} \langle \ell, \nu + t' \rangle, t + t'$ 5.2 Time Stamped Action Transition: $\langle \ell_{ini}, \nu_0 \rangle \xrightarrow{\lambda_1} \langle \ell_1, \nu_1 \rangle \xrightarrow{\lambda_2} \langle \ell_2, \nu_2 \rangle \xrightarrow{\lambda_3} \dots \xrightarrow{\lambda_n} \langle \ell_n, \nu_n \rangle$ with $\ell_n = \ell$ is the labelled transition system. $\langle \ell, \nu \rangle, t \xrightarrow{\alpha} \langle \ell', \nu' \rangle, t$ iff $\alpha \in B_{?!}$ and $\langle \ell, \nu \rangle, t \xrightarrow{\alpha} \langle \ell', \nu' \rangle$ Note: Decidability is not soo obvious: 6. Computation Path: (=Sequence of time-stamped configurations (infinite/max finite)) $\xi = \langle \ell_0, \nu_0 \rangle, t_0 \xrightarrow{\lambda_1} \langle \ell_1, \nu_1 \rangle, t_1 \xrightarrow{\lambda_2} \dots$ starting in $\langle \ell_0, \nu_0 \rangle$, with $\langle \ell_0, \nu_0 \rangle \in$ 7. Time Locks and Zeno-Behaviour: $\langle \ell, x = 0 \rangle, 0 \xrightarrow{2} \langle \ell, x = 2 \rangle, 2$ tors in C(A). A location is reachable in $t_A \cdot A$ $\langle \ell, x=0 \rangle, 0 \xrightarrow{0.1} \langle \ell, x=0.1 \rangle, 0.1 \xrightarrow{0.01} \langle \ell, x=0.11 \rangle, 0.11 \xrightarrow{0.001} \dots$ A Configuration $\langle \ell, \nu \rangle$ is called timelock, iff no delay transition with t > 02. Time abstract Transition System $\mathcal{U}(\mathcal{A})$: from $\langle \ell, \nu \rangle$ is possible (or any location change possible due to Invariant of location + guard of edge/transition) $\begin{array}{c} \langle \ell, x=0 \rangle, 0 \xrightarrow{\frac{1}{2}} \langle \ell, x=\frac{1}{2} \rangle, \frac{1}{2} \xrightarrow{\frac{1}{4}} \dots \xrightarrow{\frac{1}{2^n}} \langle \ell, x=\frac{2^n-1}{2^n} \rangle, \frac{2^n-1}{2^n} \\ \underline{\textbf{8. Real-Time Sequence:}} \ t_0, t_1, t_2, \dots, t_i \in \mathrm{Time \ for \ } i \in \mathbb{N}_0 \end{array}$ 3. Region Automaton $\mathcal{R}(\mathcal{A})$: is called Real-Time Sequence iff it has the following properties: classes 8.1 Monotonicity: $\forall i \in \mathbb{N}_0 : t_i \leq t_{i+1}$ 8.2 Non-Zeno Behaviour: $\forall t \in \overline{\text{Time}}, \exists i \in \mathbb{N}_0 : t < t_i$ indistinguishable by A.
$$\begin{split} \mathcal{R}(\mathcal{A}) &= \left(\operatorname{Conf} \left(\mathcal{R}(\mathcal{A}) \right) (\,, B_{?!}, \left\{ \overset{\alpha}{\Rightarrow}_{\mathcal{R}(\mathcal{A})} \mid \alpha \in B_{?!} \right\}, C_{ini} \right) \\ \text{where } \operatorname{Conf}(\mathcal{R}(\mathcal{A})) &= \left\{ \langle \ell, [\nu] \rangle, \ell \in L, \nu : X \to \operatorname{Time}, \nu \models I(\ell) \right\} \end{split}$$
9. Run: Starting in $\langle \ell, \nu \rangle$, t_0 , a run is an infinite computation path, where $t_{i \in \mathbb{N}_0}$ is a Real-Time Sequence. $(\xi = \text{run of } A)$ iff $\xi = \text{computation path of } A)$ 4. The number of Regions: $2(c+2)^{|X|} \cdot (4c+3)^{\frac{1}{2}|X| \cdot (|X|-1)} = \text{upper bound number}$ Putting all together $A = (L, B, X, I, E, \ell_{ini})$ There are finitely many locations in L (By definition) There are finitely many regions ⇒ So Conf(R(A)) is finite (By construction)

If $(\ell_2, \alpha, \varphi_2, Y_2, \ell_2') \in E_2$ then $\forall \ell_1 \in E_1 \bullet ((\ell_1, \ell_2), \alpha, \varphi, Y_2, (\ell_1, \ell_2')) \in E_1$ iff $(\ell, \alpha, \varphi, Y, \ell') \in E \land \alpha \notin \{press!, press?\}$ \Rightarrow Transitions are thus either internal actions τ or delay transitions (Hiding

5.2 Synchronisation Transition:

if there are $i, j \in \{1, ..., n\}, i \neq j$ and $b \in B_i \cup B_j$, such that $(\ell_i, b?, \varphi_i, Y, \ell_i) \in E_i$

Clocks range over real numbers, thus infinitely many configurations At each configurations uncountably many transitions \xrightarrow{t} may originate

Recall: $\varphi := x \sim c \mid x - y \sim c \mid \varphi \wedge \varphi$ $x, y \in X, c \in \mathbb{Q}_0^+$, and $\gamma \in \{<, >, \leq, \geq\}$

1. Observe clock constraints: Let t_A be the least common multiple of the denominaiff ℓ is reachable in \mathcal{A}

Let $\langle \ell, \nu \rangle, \langle \ell', \nu' \rangle \in \operatorname{Conf}(\mathcal{A})$ be configurations of \mathcal{A} and $\alpha \in B_{?!}$, an action, then $\langle \ell, \nu \rangle \stackrel{\alpha}{\Longrightarrow} \langle \ell', \nu' \rangle$ iff there exists $t \in \text{Time}$, such that $\langle \ell, \nu \rangle \stackrel{t}{\Longrightarrow} \circ \stackrel{\alpha}{\Longrightarrow} \langle \ell', \nu \rangle$

Distinguish Clock valuations: if $c_x \geq 1$ then there are $(2c_x + 2)$ equivalence $\{\{0\}, (0,1), \{1\}, (1,2), \{2\}, \dots, \{c_x\}, (c_x, \infty)\}.$

If $\nu_1(x)$ and $\nu_2(x)$ are in the same equivalence class, then ν_1 and ν_2 are

 $\alpha \in B_{?!} : \langle \ell, [\nu] \rangle \xrightarrow{\alpha}_{\mathcal{R}(\mathcal{A})} \langle \ell', [\nu'] \rangle \text{iff } \langle \ell, \nu \rangle \xrightarrow{\alpha} \langle \ell', \nu' \rangle \left(\widehat{=} \langle \ell, \nu \rangle \xrightarrow{t} \circ \xrightarrow{\alpha} \langle \ell', \nu' \rangle \right)$

It is decideable whether there exists a sequence

 $\begin{array}{l} \langle \ell_{ini}, [\nu_{ini}] \rangle \stackrel{\Delta}{\Longrightarrow}_{\mathcal{R}(\mathcal{A})} \langle \ell_{1}, [\nu_{1}] \rangle \stackrel{\Delta}{\Longrightarrow}_{\mathcal{R}(\mathcal{A})} \dots \stackrel{\Delta}{\Longrightarrow}_{\mathcal{R}(\mathcal{A})} \langle \ell_{n}, [\nu_{n}] \rangle, \\ \text{such that } \langle \ell_{n}, [\nu_{n}] \rangle = \langle \ell_{1}, [\nu] \rangle \end{array}$

Note: We just observed that $\mathcal{R}(A)$ loses some information about the clock valuations that are possible in a region.

Region and Zones

A (clock) zone is a set $z \subseteq (X \to Time)$ of valuations of clocks X such that there exists $\varphi \in \Phi(X)$ with $\nu \in z$ iff $\nu \models \varphi$.

1. Let time elapse

2. Intersect with Invariant of ℓ

3. Intersect with guard

4. Reset clocks

5. Intersect with Invariant of ℓ'

Pro's and Con's:

Zone-based: + avoids blowup by number of clocks and size of clock constraints through symbolic representation of clocks - confinde wrt. size of discrete

Region-based: + Less dependent on size of discrete state space - exponential in number of clocks

Extended Timed Automata $A = (L, C, B, U, X, V, I, E, \ell_{ini})$ $(\ell, \alpha, \varphi, \vec{r}, \ell')$

Comitted Locations: $C \subseteq L$ Urgend Channels: $U \subseteq \overline{B}$ Set of Datavariables: $\overline{\mathcal{V}}$

Urgend: Being in this location blocks time passing

Committed: Being in this location the next edge must change the committed location 1. Data Variables: When modelling controllers as timed automata, it is sometimes

desirable to have (local and shared) non-clock variables. E.g. number of open

2. Urgent Locations: Enforce local immediate progress (In t = 0 time)

3. Committed Locations: Enforce atomic immediate progress (Direct) As long as data variables are finite the extension doesn't harm the decidability

Automatic Verification of DC Properties for Timed Automata

Testability

Timed Büchi Automata $\mathcal{A} = (\Sigma, S, S_0, X, E, F)$

Alphabet: Σ Finite Set of States: SSet of Start States: $S_0 < S$ Finite Set of Clocks: $reve{X}$

Set of Transitions: $E = (s, s', a, \lambda, \delta)$

Set of Accepting States: \hat{F}

1. Time sequence: $\tau = \tau_1, \tau_2, \dots$ is an infinite sequence of time values $\tau_i \in \mathbb{R}_0^+$ satisfying the following constraints:

Monotonicity: τ increases strictly monotonically, i.e., $\tau_i < \tau_{i+1} \forall i \geq 1$

Progress: For every $t \in \mathbb{R}_0^+$, there is some i > 1 such that $\tau_i > t$

2. Timed Word: is a pair (σ, τ) over a an alphabet Σ , where $\sigma = \sigma_1, \sigma_2, \ldots \in \Sigma^{\omega}$ is an infinite word over Σ and τ is a time sequence

3. Timed Language: is a set of timed words over Σ over an alphabet Σ

4. Clock constraints: $\delta := x \le c \mid c \le x \mid \neg \delta \mid \delta_1 \wedge \delta_2$

5. (Accepting) TBA Run: A run r, denoted by (\bar{s}, \bar{v}) of a TBA over a timed word (σ,τ) is an infinite sequence $r:\langle s_o,\nu_0\rangle \xrightarrow{\sigma_1} \langle s_1,\nu_1\rangle \xrightarrow{\sigma_2} \ldots$ and is called (an) accepting (run) iff $\inf(r)\cap F\neq 0$ (Der Final-State wird ∞ -oft besucht

 $\rightarrow \{s_2, s_3\} \cup s_2 \neq 0$

6. Language of TBA For a TBA \mathcal{A} , the language $\mathcal{L}(\mathcal{A})$ of time wrds it accepts defined to be the set $\{(\sigma,\tau)|\mathcal{A}$ has an accepting run over (σ,τ) . For short: $\mathcal{L}(\mathcal{A})$ is the language of A.