RAK811 Customer Develop Manual V1.1(For CoIDE)

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1. Overview

RAK811 module integrates semtech SX1276 and stm32L, support customer to do their applications use the internal stm32. And we offer an open source project which implement the loraWAN and loraP2P applications, that customer can easy develop own applications base the lora driver.

RAK811 module support 868/915M loraWAN band, hardware support 860MHz-1020MHz, and can choose RF out from HF or PA_Boost pin, max tx power can reach 19dbm. Module hardware has also be certificated by FCC/KCC/CE.

Current we supply the open source project build IAR Embedded Workbench for ARM (EWARM) tool chain V7.70.1+ SWD Debug.

1.1 system block diagram

The block diagram of module is described in the figure below.

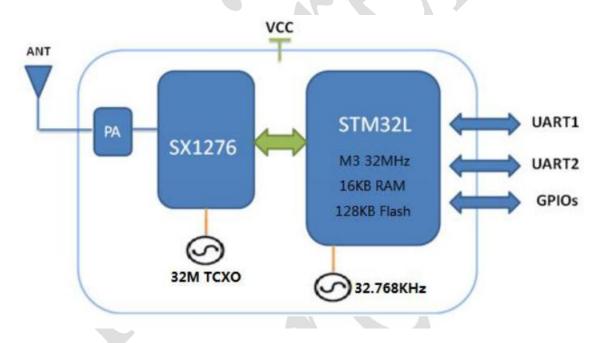


Figure 1-1 System Diagram

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2. Module Description

2.1 Pin Outline

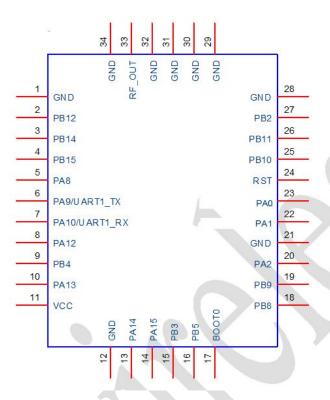


Figure 3-1 Module Pin outline

2.2 Pin definition

Table 4-1: Pin Definition

NO	Name	Type	Description
1	GND	-	Ground connections
2	PB12	I/O	B part for GPIO port
3	PB14	I/O	B part for GPIO port
4	PB15	I/O	B part for GPIO port
5	PA8	I/O	A part for GPIO port
6	PA9/UART1_TX	О	UART1 Interface
7	PA10/UART1_RX	I	UART1 Interface
8	PA12	О	A part for GPIO port
9	PB4	I	B part for GPIO port
10	PA13	I/O	A part for GPIO port
11	VCC	P	Main power voltage source input
12	GND	_	Ground connections



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13	PA14	I/O	A part for GPIO port
14	PA15	I/O	A part for GPIO port
15	PB3	I/O	B part for GPIO port
16	PB5	I/O	B part for GPIO port
17	BOOT0	I	Boot mode GPIO enable pin, high active
18	PB8	I/O	B part for GPIO port
19	PB9	I/O	B part for GPIO port
20	PA2	I/O	A part for GPIO port
21	GND	_	Ground connections
22	PA1	I	A part for GPIO port
23	PA0	О	A part for GPIO port
24	RST	I	Reset trigger input, low active
25	PB10	I/O	B part for GPIO port
26	PB11	I/O	B part for GPIO port
27	PB2	I/O	B part for GPIO port
28	GND	_	Ground connections
29	GND	-	Ground connections
30	GND	_	Ground connections
31	GND	_	Ground connections
32	GND		Ground connections
33	RF_OUT	I/O	RF I/O port
34	GND	_	Ground connections

3. Develop environment

3.1 WiseNode-Lora EVB

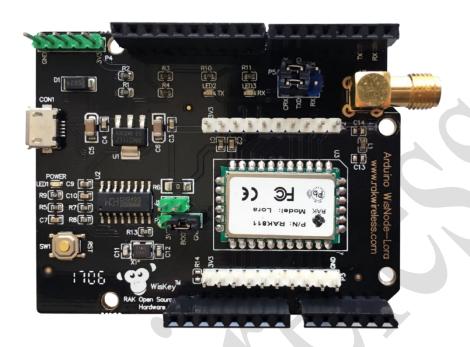


Figure 3-1 WiseNode overview

Function	Name	Description	
Module	U3	RAK811 Lora module	
External Interface	Micro USB (CON1)	Power Supply; DC 5V Input,USB to TTL communication interface	
Key	RST	Module Reset Key	
Leading Foot	P1	Boot Switch Pin, When Boot Pin Switch to 3.3V Module will into the Boot Mode	
	P4	SWD Debug Pin	
	P5	Uart Switch Pin	
Power Test	J1	Module Power Test Pin	
LED Indicator	LED1 (Power)	Power Indicator Light	

USBTO232 window7/8/10 driver:

http://docs.rakwireless.com/en/RAK811%20LoRa%20Module/Tools/CH340%20Drive.rar

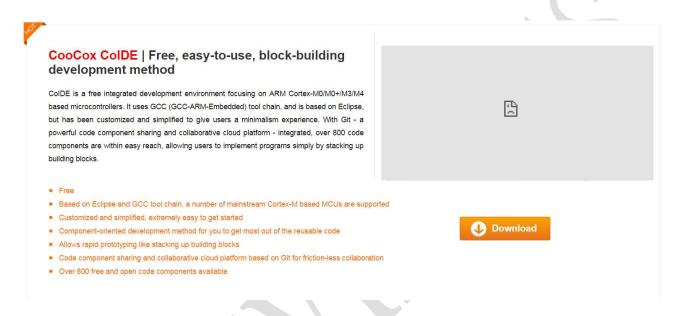
The WisNode-LoRa Schematic diagram is:

http://docs.rakwireless.com/en/RAK811%20LoRa%20Module/Hardware%20Design/WisNode-LoRa%20Sche matic 20161216.pdf

3.2 Download CoIDE

CoIDE is a free integrated development environment focusing on ARM Cortex-M0/M0+/M3/M4 based microcontrollers. It uses GCC (GCC-ARM-Embedded) tool chain, and is based on Eclipse, but has been customized and simplified to give users a minimalism experience. With Git - a powerful code component sharing and collaborative cloud platform - integrated, over 800 code components are within easy reach, allowing users to implement programs simply by stacking up building blocks.

The download link is: http://www.coocox.org/



3.3 Demo Project

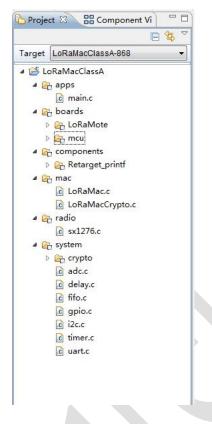


Figure 3-2 Project overview

apps

-mcu.c application code

boards

- -LoRaMote
 - -cmsis stm32lxx platform system initial
 - -STM32L1xx_HAL_Driver stm32lxx platform peripheral driver
 - -board.c... peripheral initial and stm32 related pins operate
- -mcu
 - -stm32 stm32lxx platform system interrupted

components

-Retarget_printf printf function configuration

mac

-LoRaMac.c LoRaMacCrypto.c lora mac driver

radio

-sx1276.c support the semtech sx1276 driver system

-crypto lora transmit security use AES and cmac check

-adc.c delay.c... delay ,timer, support and uart,adc,i2c,gpio Interface rewrite

Project github project: https://github.com/RAKWireless/RAK811_LoRaMac_CoIDE

3.4 Example ClassA

Test the example class A to connect IoraWAN gateway with OTAA way, default 868 band. You just need change the DevEui (if need), AppEui and AppKey match with your gateway.

Check the config in Comissioning.h.

Figure 3-3 loraWAN config

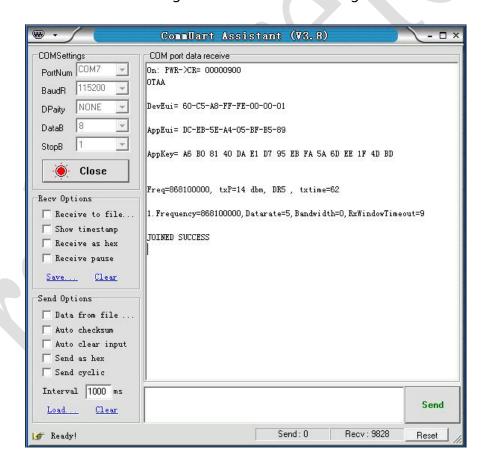


Figure 3-4 loraWAN join&send

3.5 Example PingPong

Test the example pingpong to communicate with two lora module, one as master and another as slave.

Change the follow code:

bool isMaster = false; //true

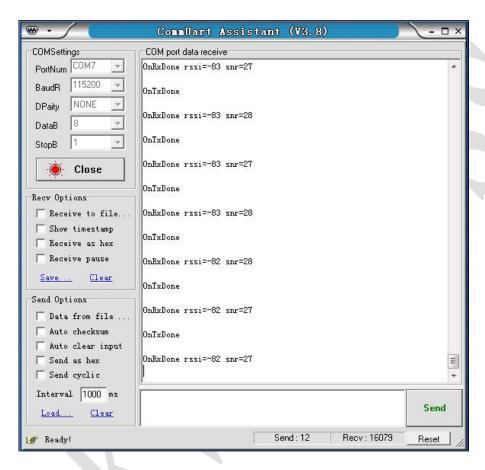


Figure 3-5 loraP2P send&recv

4. Contact information

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5. Change Note

Version	Date	Change
V1.0	2017-03-16	First release
V1.1	2017-08-14	For CoIDE

