

### GTU Electronics Engineering

## ELEC 331 Electronic Circuits 2

#### Fall Semester

Instructor: Assist. Prof. Önder Şuvak

# $\begin{array}{c} {\rm HW~2} \\ {\rm Questions~and~Answers} \end{array}$

Updated October 20, 2017 - 13:37

Assigned:

Due:

**Answers Out:** 

Late Due:

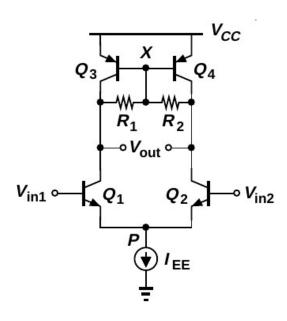
### Contents

itle Page	1
ontents	1
uestion 1	2
Question	2
Solution	3
uestion 2	10
Question	10
Solution	11
uestion 3	14
Question	14
Solution	15
uestion 4	17
Question	17
Solution	18
uestion 5	21
Question	21
Solution	22
uestion 6	24
Question	24
Solution	

#### **BJT Differential Amplifier Design**

#### **Razavi 10.31**

**31.** The circuit of Fig. 10.68 must provide a gain of 50 with  $R_1=R_2=5~{\rm k}\Omega.$  If  $V_{A,n}=5~{\rm V}$  and  $V_{A,p}=4$  V, calculate the required tail current.



**Figure 10.68** 

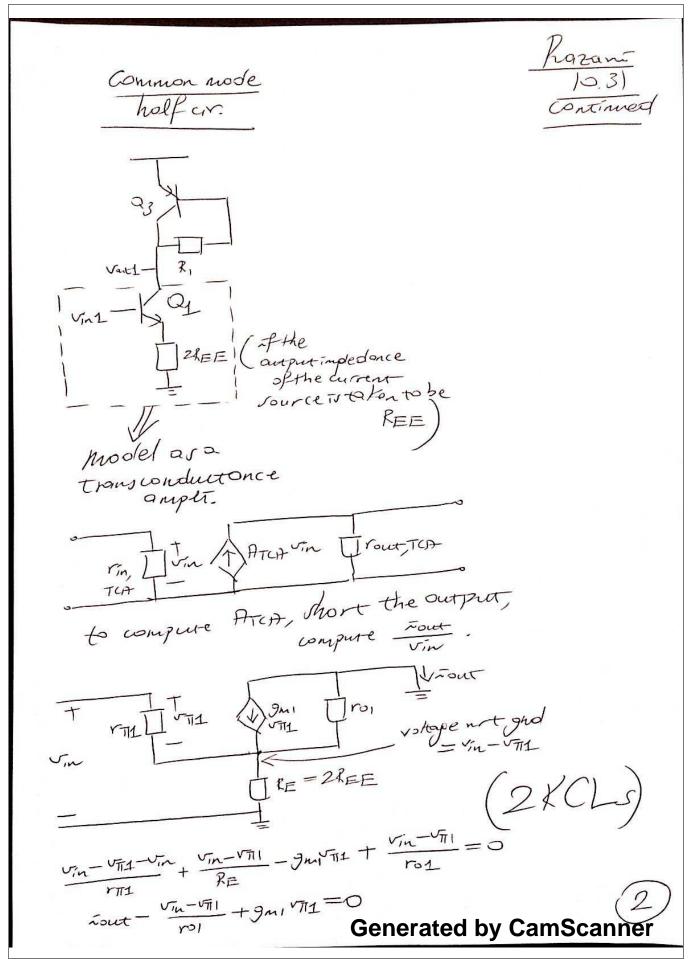
**Necessary Knowledge and Skills:** BJT small signal analysis, differential mode half circuit analysis, output impedance calculation, voltage gain calculation, parameter selection for design specification

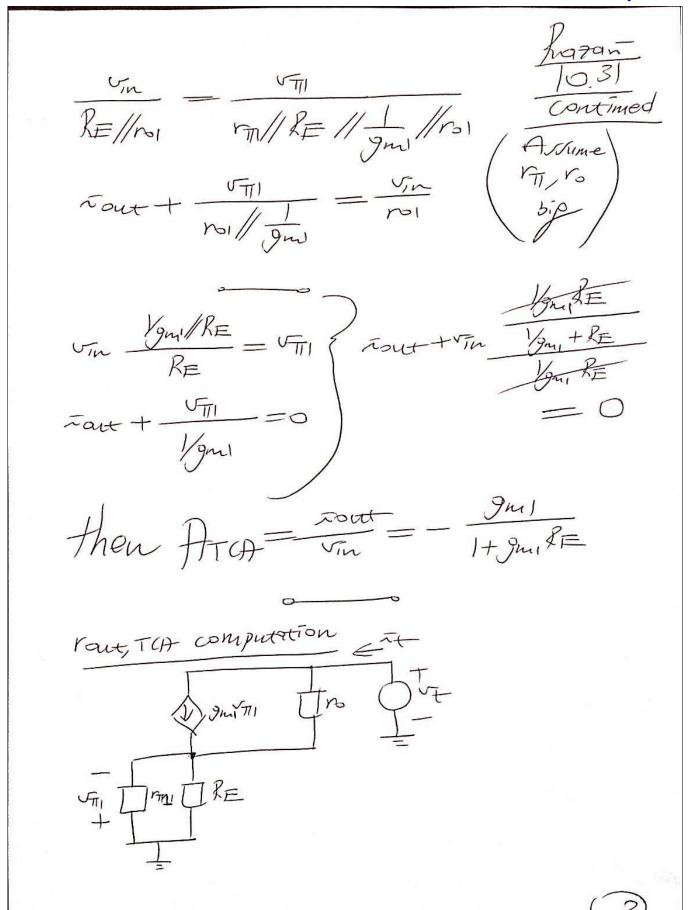
Razanio 10.31 Differential mode half cir Rup = TE = Rillros then Jours = - gm, [R, //r3 //r3] (common emitter config.)

Similarly (with sinz = -vin1) vin1

Tout = vaut1-vout2 = -9mi[Rillros/101]

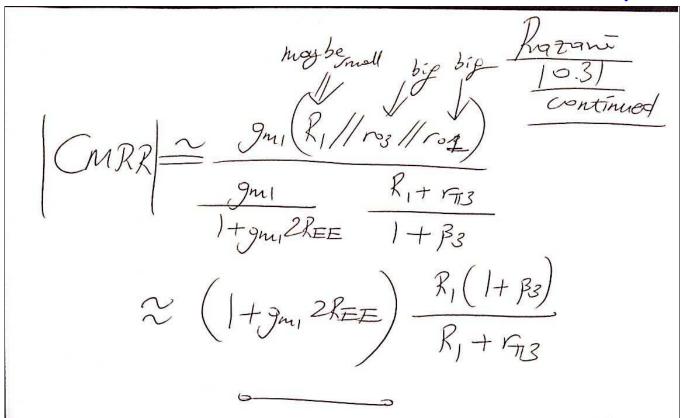
and vin1-vinz vin1 - vin2 Generated by CamScanner





Regard

$$|D.3|$$
 $|D.3|$ 
 $|D$ 



Design question  $I_{c,q_1}=I_{c,q_2}=I_{c,q_3}=I_{c,q_4}=I_{EE}$ of the base currents of Q3 and Q4 are repligible.  $g_{\text{My}} = \frac{I_{\text{C,Q1}}}{V_{\text{T}}} = \frac{I_{\text{EE}}}{2V_{\text{T}}}$ R = 5kN $r_{03} = \frac{V_{A,P}}{I_{C,Q3}} = \frac{4V}{I_{EE/D}} = \frac{8}{I_{FE}}$  $r_{01} = \frac{V_{A,r}}{I_{r}} = \frac{5V}{I_{FF}/2} = \frac{10}{I_{FF}}$  $\begin{aligned} \left| G_{a,n} \right| &= g_{m_1} \left[ R_1 / / n_3 / / n_0 \right] \\ &= \frac{I_{EE}}{2V_T} \left[ \frac{5k}{I_{EE}} / \frac{8}{I_{EE}} \right] = 50 \\ &= \frac{1}{2V_T} \left[ \frac{5k}{I_{EE}} / \frac{8}{I_{EE}} \right] = \frac{1}{2V_T} \left[ \frac{1}{V_T} \right] = \frac{1}{2V_T} \left[ \frac{1}{V_$ colungte IEE!



MOS Amplifier Razavi 10.51

**51.** A student who has a single-ended voltage source constructs the circuit shown in Fig. 10.75, hoping to obtain differential outputs. Assume perfect symmetry but  $\lambda = 0$  for simplicity.

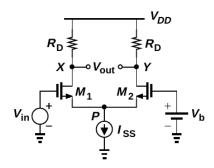
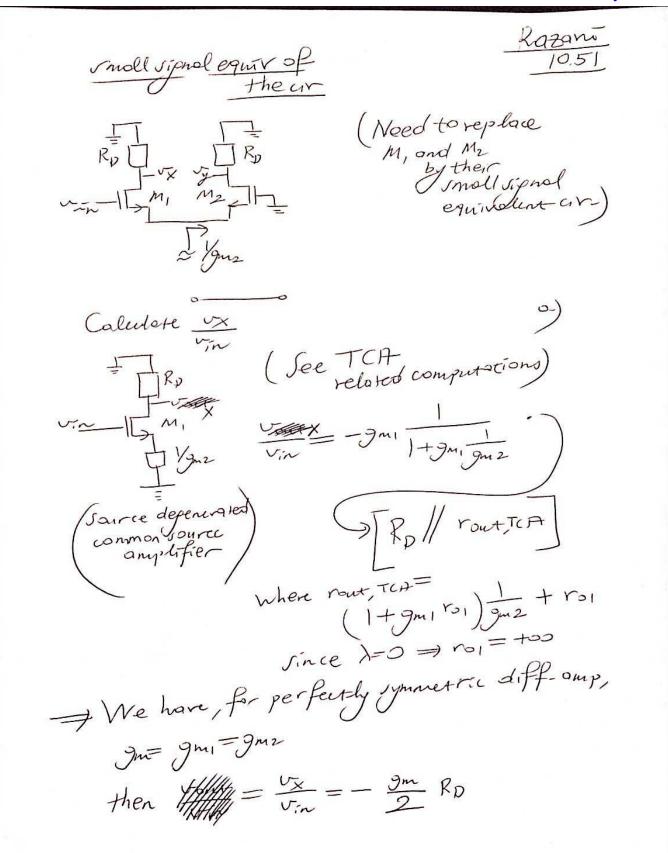


Figure 10.75

- (a) Viewing  $M_1$  as a common-source stage degenerated by the impedance seen at the source of  $M_2$ , calculate  $v_X$  in terms of  $v_{in}$ .
- (b) Viewing  $M_1$  as a source follower and  $M_2$  as a common-gate stage, calculate  $v_Y$  in terms of  $v_{in}$ .
- (c) Add the results obtained in (a) and (b) with proper polarities. If the voltage gain is defined as  $(v_X v_Y)/v_{in}$ , how does it compare with the gain of differentially-driven pairs?

**Necessary Knowledge and Skills:** Common source/gate amplifiers, small signal equivalent MOS, differential pair gain and other calculations, performance comparisons



With diff. driven pairs

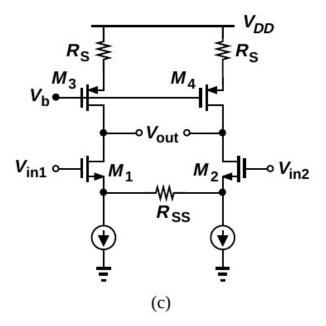
(on the other hand)

$$\frac{10.51}{continued}$$
 $\frac{10.51}{continued}$ 
 $\frac{10.51}{continued}$ 

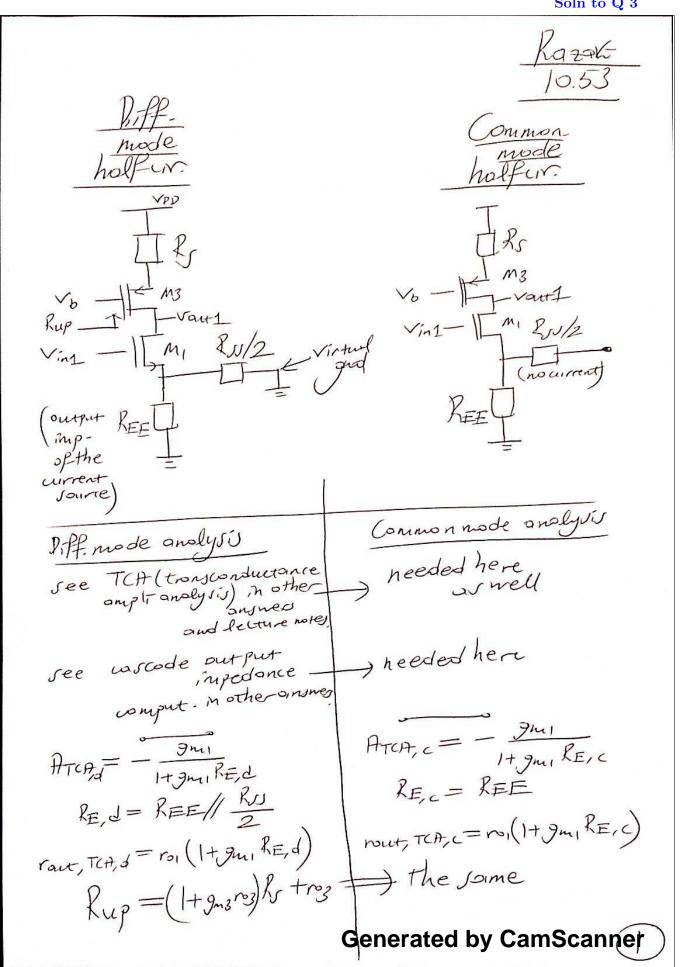
#### **MOS Differential Pair**

Razavi 10.53 c

**53.** Calculate the differential voltage gain of the circuits depicted in Fig. 10.77. Assume perfect symmetry and  $\lambda > 0$ . You may need to compute the gain as  $A_v = -G_m R_{out}$  in some cases.



**Necessary Knowledge and Skills:** MOS small signal analysis, output impedance calculation, common/differential mode half circuits, cascode structure, virtual ground in differential mode, voltage gain computation



	Ω
Diff-mode	Common Continued mode
Vout = ATCA, d [rout, TCA, d/ Rup]	Vait, 1 = ATCA, C Vin 1 (rout, TCH, c// Rup)
if Rss << REE	(desired) (*)
Common mode gon 1 - 3m (REE /	Jepan 2 2 / 3 / 3 / 3 / 3 / 3 / 3 / 3 / 3 / 3
9mi 1+gmi REE	this ratio is smaller than 1
Oltopether 1+gmi REE 1+gmi Rs	See (*)  Generated by CamScanner

#### **BJT Differential Amplifier with Parasitics**

**Razavi 10.55** 

**55.** Due to a manufacturing error, a parasitic resistance,  $R_P$ , has appeared in the circuit of Fig. 10.78. Calculate the voltage gain.

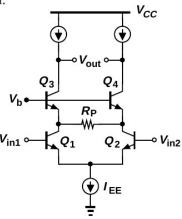
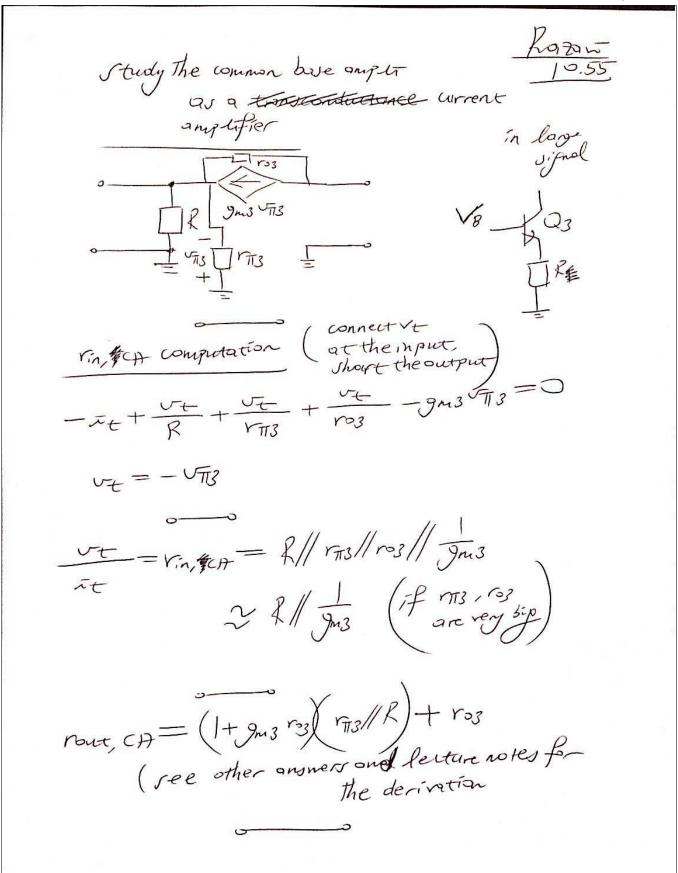


Figure 10.78

**Necessary Knowledge and Skills:** BJT small signal analysis, differential mode half circuit analysis, voltage gain computation, virtual ground in differential mode



The computation (connect tain at the imput, short the output)

That 
$$\frac{10.55}{R}$$
 continued

The trip output the output continued

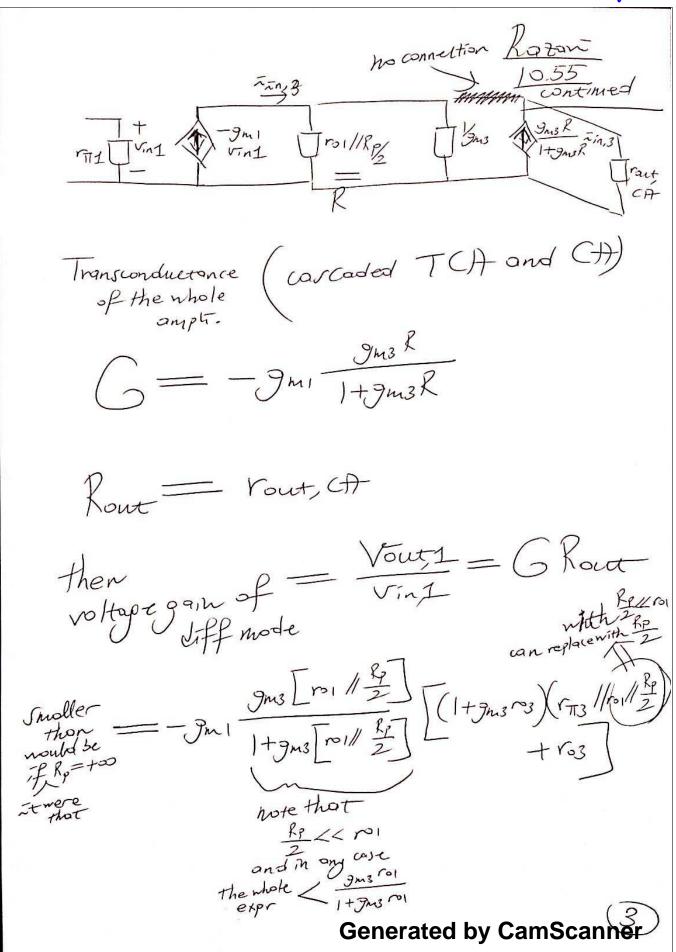
The trip output the output continued

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The trip output trip



#### MOS Diff. Amp. (Mismatch)

Sedra 7.14

**7.14** A design error has resulted in a gross mismatch in the circuit of Fig. P7.14. Specifically,  $Q_2$  has twice the W/L ratio of  $Q_1$ . If  $v_{id}$  is a small sine-wave signal, find:

- (a)  $I_{D1}$  and  $I_{D2}$ .
- (b)  $V_{OV}$  for each of  $Q_1$  and  $Q_2$ .
- (c) The differential gain  $A_d$  in terms of  $R_D$ , I, and  $V_{OV}$ .

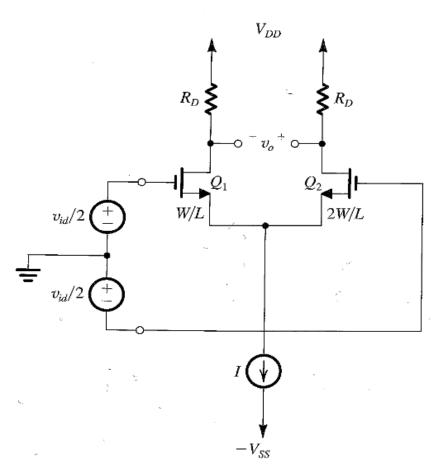


FIGURE P7.14

**Necessary Knowledge and Skills:** MOS differential, transistor mismatch, differential gain computation

$$T_{D} = \frac{1}{2} m_{n} (o_{X} \frac{W}{L} (V_{S} - V_{C})^{2}) \frac{\int \delta dr_{0}}{\int J_{L}^{2}}$$

$$\ln DC \Rightarrow V_{G1} = V_{G2} = OV$$

$$V_{S1} - V_{I2} = V_{S} (G_{D} m_{NO})$$

$$T = I_{D1} + I_{D2}$$

$$= \frac{1}{2} m_{n} (o_{X} \frac{W}{L} (O - V_{S} - V_{C})^{2})$$

$$= \frac{3}{2} m_{n} (o_{X} \frac{W}{L} (V_{S} - V_{C})^{2})$$

$$= \frac{3}{3} m_{n} (o_{X} \frac$$

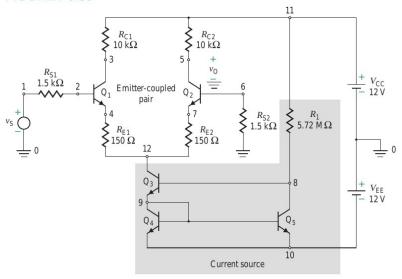
Note that virtual and in transistor Jeans 7.14 his match is questionoisle => needs further analysis Cont. Accept for now that virt, Indexists. いりーラからを見り 502 = 7m2(-52) PD  $\frac{|V_{02}-V_{01}|}{|V_{0}|} = \frac{1}{|V_{0}|} \left[ \frac{4I}{3V_{0V}} \frac{1}{2} v_{0} R_{0} - \left( -\frac{2I}{3V_{0V}} \frac{1}{2} v_{0} R_{0} \right) \right]$  $\frac{1}{\sqrt{3}} \frac{1}{\sqrt{3}} \sqrt{3} \sqrt{3} \sqrt{3}$ 

#### BJT Differential Amp. With Current Source

**Rashid 9.39** 

9.39 A differential amplifier is shown in Fig. P9.39. The transistors are identical. Assume  $V_{\rm BE}=0.7$  V,  $V_{\rm T}=26$  mV,  $\beta_{\rm F}=50$ , and  $V_{\rm A}=40$  V. Calculate the values of  $A_{\rm d}$ ,  $R_{\rm id}$ ,  $A_{\rm c}$ ,  $R_{\rm ic}$ , and CMRR.

#### FIGURE P9.39



**Necessary Knowledge and Skills:** Current source output current and impedance computation, BJT differential pair, BJT small signal analysis, common/differential mode half circuit analysis, voltage gain computations in common/differential modes, CMRR calculation

Collular the tail current

$$V_{11} = 12 V$$
 $V_{10} = -12 V$ 
 $V_{10} = -12 V$ 
 $V_{10} = -12 V$ 
 $V_{11} = -12 V$ 
 $V_{12} = -12 V$ 
 $V_{12} = -12 V$ 
 $V_{12} = -12 V$ 
 $V_$ 

