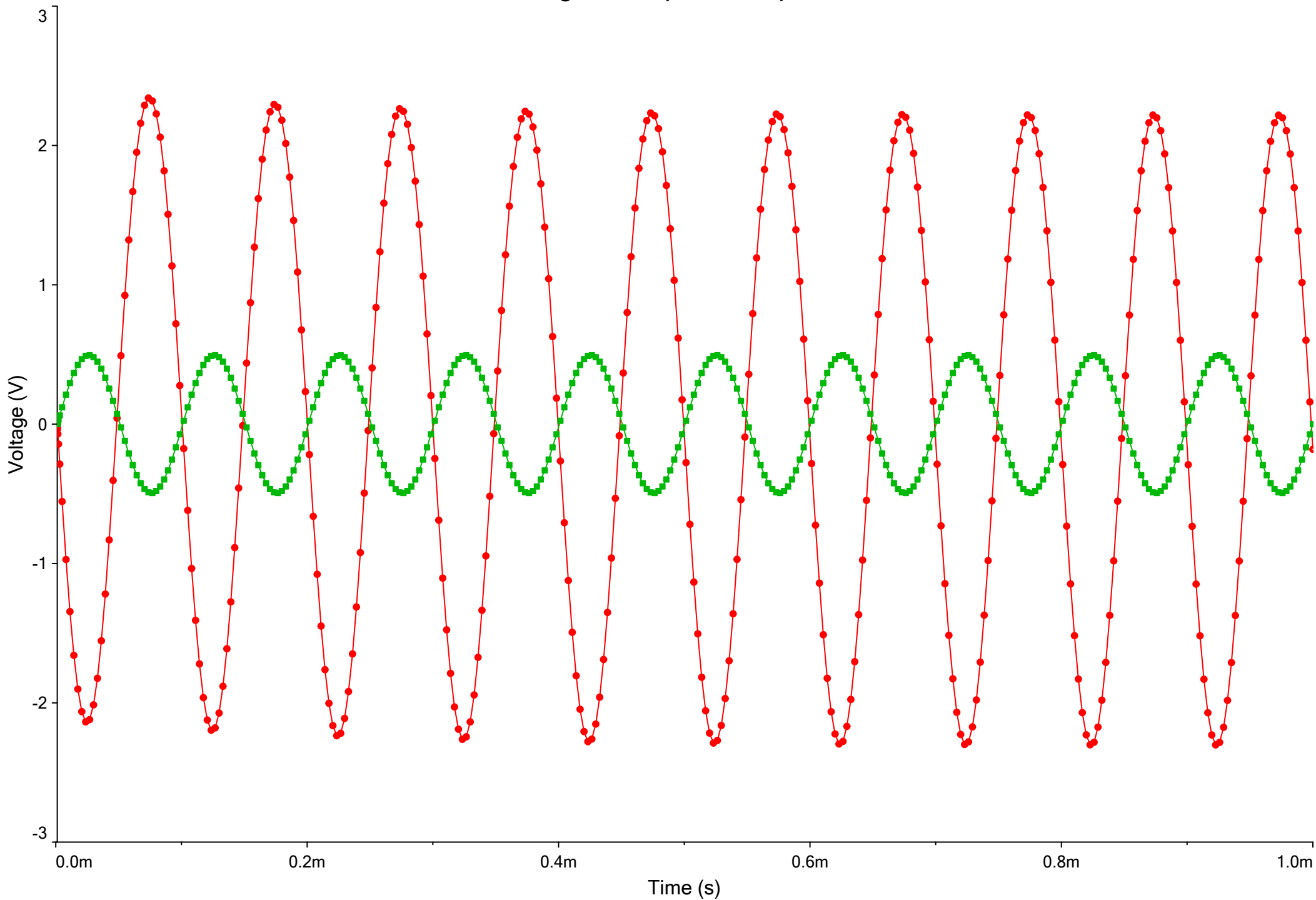


Designed Amplifier Output



☒ V(5) ☒ V(3)
☒ V(out) ☒ V(in)