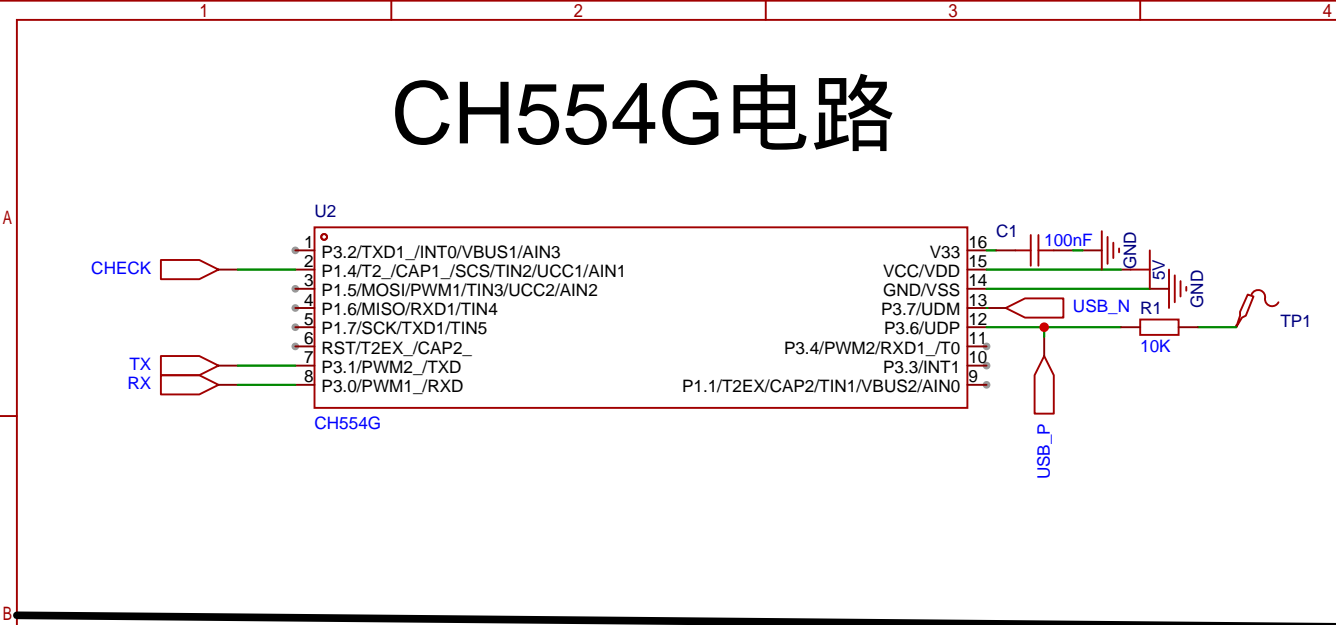


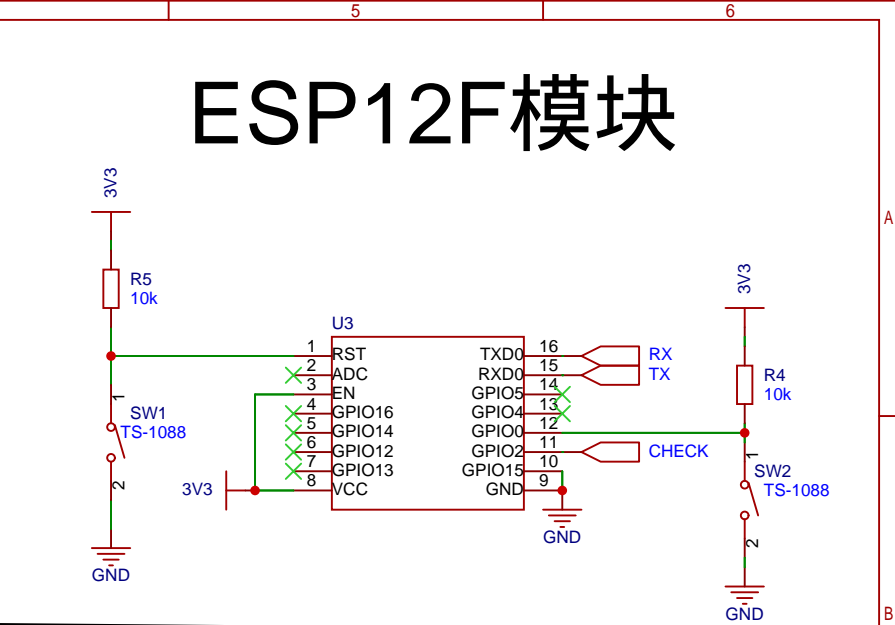
[illegible]

ESP12F模块

The diagram shows the wiring for an ESP12F module (U3). The module's pins are connected as follows:

- Pin 1 (RST):** Connected to a 3V3 supply through a 10k resistor (R5) and a switch (SW1, TS-1088).
- Pin 2 (ADC):** Connected to GND.
- Pin 3 (EN):** Connected to a 3V3 supply.
- Pin 4 (GPIO16):** Connected to GND.
- Pin 5 (GPIO14):** Connected to GND.
- Pin 6 (GPIO12):** Connected to GND.
- Pin 7 (GPIO13):** Connected to GND.
- Pin 8 (VCC):** Connected to a 3V3 supply.
- Pin 9 (GPIO15):** Connected to GND.
- Pin 10 (GND):** Connected to GND.
- Pin 11 (CHECK):** Connected to a 3V3 supply through a 10k resistor (R4) and a switch (SW2, TS-1088).
- Pin 12 (GPIO0):** Connected to GND.
- Pin 13 (GPIO4):** Connected to GND.
- Pin 14 (GPIO5):** Connected to GND.
- Pin 15 (RXD0):** Connected to a TX pin.
- Pin 16 (TXD0):** Connected to an RX pin.

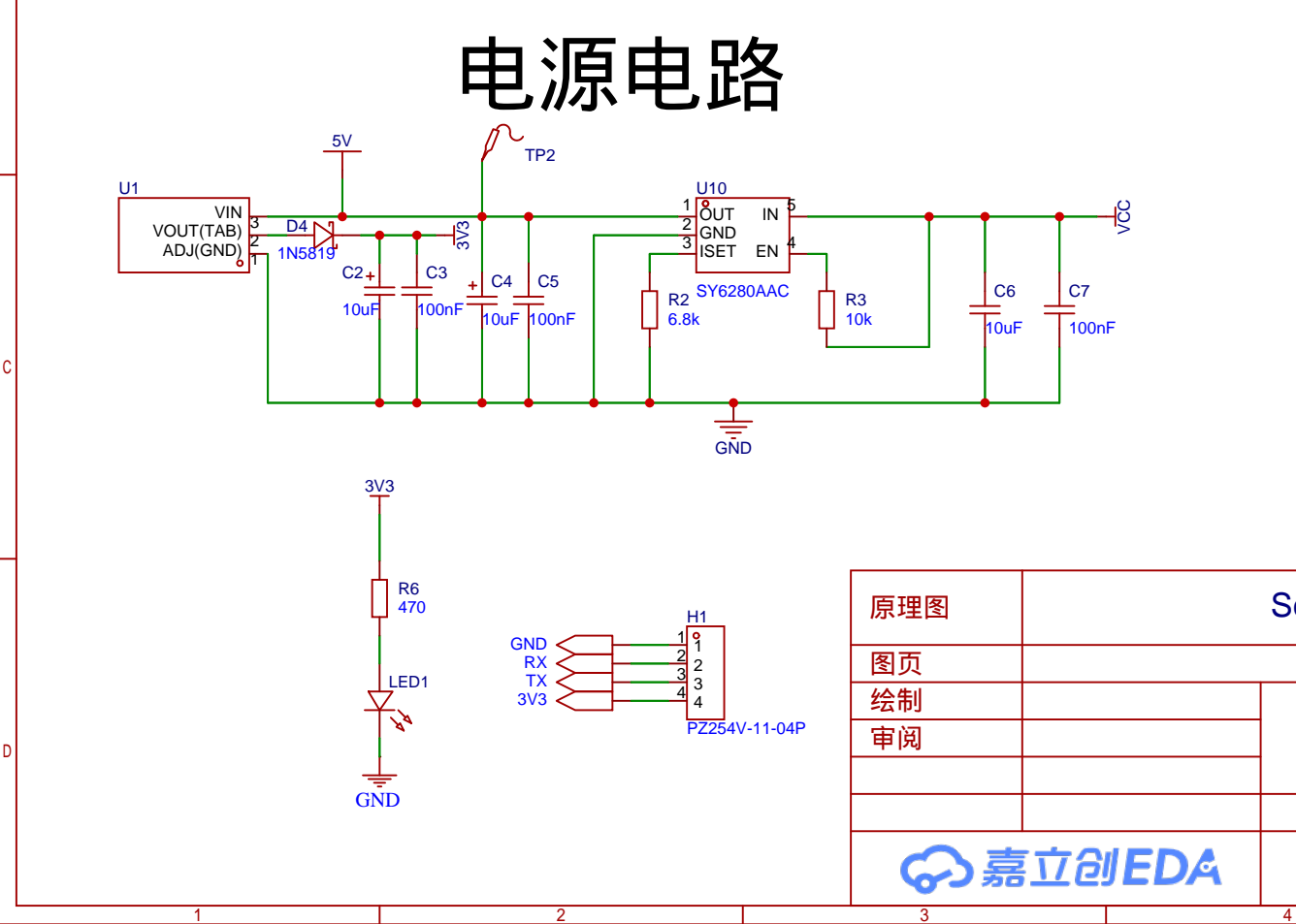
The module is powered by a 3V3 supply.



电源电路

The diagram illustrates a power supply circuit. A 5V input is connected to a 1N5819 diode (D4) and a 3V3 voltage divider (R2, R3) for a SY6280AAC regulator (U10). The regulator's output is filtered by capacitors C2 through C7. A 3V3 LED driver (R6) and an LED (LED1) are also shown. A connector H1 (PZ254V-11-04P) is used for GND, RX, TX, and 3V3 signals.

原理图	S	
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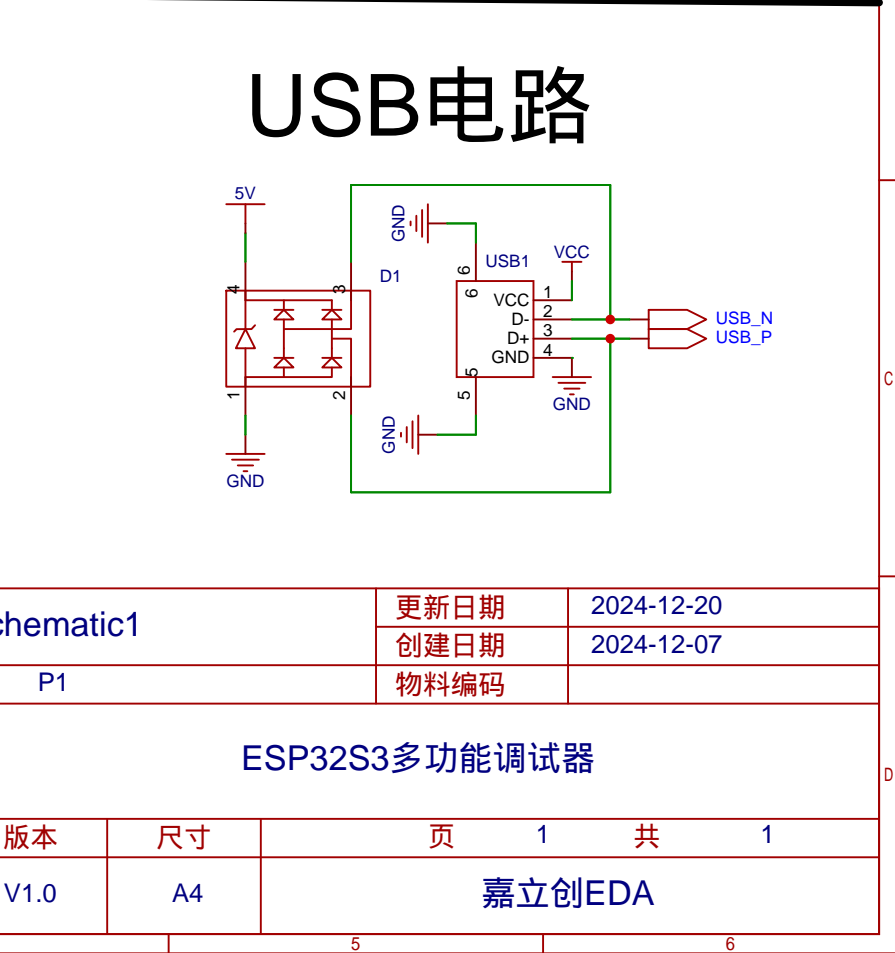



USB电路

chematic1	更新日期	2024-12-20
	创建日期	2024-12-07
	物料编码	

ESP32S3多功能调试器

版本	尺寸	页	1	共	1
V1.0	A4	嘉立创EDA			



原理图	Schematic1			更新日期	2024-12-20
				创建日期	2024-12-07
图页	P1			物料编码	
绘制		ESP32S3多功能调试器			
审阅					
		版本	尺寸	页	1 共 1
		V1.0	A4	嘉立创EDA	