

# SPECIFICATION

## 液晶屏规格书

Revision: 1.0

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## 1. GENERAL DESCRIPTION

The TK043F1508 is a 480(RGB)x800 dot-matrix TFT module. This module can be easily accessed by micro-processor-unit (MPU) via parallel 80-system interface and RGB interface, and is suitable for small mobile products as digital cell phone and MP4.

## 2. FEATURES

Display Mode	TFT LCD module
	Active matrix TFT ,Transmissive type
Display Format	RGB Stripe
Color	16.7M color
Input Data	System parallel interface or SPI+RGB
Viewing Direction	Full viewing
Backlight	White LED
Driver IC	NT35510

## 3. MECHANICAL SPECIFICATION

Item	Specifications	Unit
Dimensional outline	61.26(W)×104.5(L) ×2.3(D) (exclude FPC,include included D.S.T)	mm
Number of Pixel	480 x 800	Pixel
LCD A.A	56.16(W)×93.6 (L)	mm
Pixel Pitch	0.117 (W)×0.117(L)	mm

Note: 1 pixel = 3 dots = Red dot +Green dot +Blue dot

Issue date:2017/12/07

Customer's Approval

Customer

Date

\*FOR REFERENCE ONLY\*

The drawing shows the LCM11A070 module with dimensions in mm. Key dimensions include: overall width 61.26±0.15, LCD width 59.56±0.2, PDL width 58.16, AA width 56.16, pull tape width 30.63, and overall height 104.5±0.2. The module is labeled with '480×RGB×800DITS' and 'SCALE "A" 40:1'. A detailed view of the FPC connector shows dimensions like 12.6±0.05, 0.3±0.040, and 0.2±0.03. A pin list table is provided at the bottom right.

No	PIN NAME
1	IN(ING)
2	PMARK
3	SUI
4	RS
5	WR/SCL
6	RD
7	CS
8	RESET
9	IMQ
10	IMT
11	GND
12	DB23
13	DB22
14	DB21
15	DB20
16	DB19
17	DB18
18	DB17
19	DB16
20	DB15
21	DB14
22	DB13
23	DB12
24	DB11
25	DB10
26	DB9
27	DB8
28	DB7
29	DB6
30	DB5
31	DB4
32	DB3
33	DB2
34	DB1
35	D80
36	DE
37	PCLK
38	HSYNC
39	VSNC
40	IDVCC
41	VCC
42	GND
43	LEDA
44	LEDB
45	GND

## 5. MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Note
Supply voltage	$V_{DD}$	-0.24	3.6	V	
Input Voltage	$V_{IN}$	-0.24	$V_{DD}+0.24$	V	
Operating temperature	$T_{OP}$	-20	70	°C	
Storage temperature	$T_{ST}$	-30	80	°C	
Humidity	---	-	90%	RH	MAX60°C

## 6.ELECTRICAL CHARACTERISTICS

Item		Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage		$V_{DD}$	-	2.7	2.8	2.9	V
		IOVCC		1.7	1.8	1.9	V
Input Voltage	H level	$V_{IH}$	-	0.7xVCC	---	VCC	V
	L level	$V_{IL}$		0	---	0.3xVCC	V
Supply current		$I_{DD}$	Without LED	---	40.5	---	mA
		$I_{sleep}$		----	30	----	uA

## 7.BACKLIGHT CHARACTERISTICS

Item	Symbol	Min.	Typical	Max.	Unit
Current (One LED)	$I_f$	---	18	25	mA/Pcs
Number of LED ★1	---	8			Piece
Connection mode	S	Serial			---
LCM Surface Luminance ★2( $I_f = 18$ mA)	$L_s$	250	290	----	cd/m <sup>2</sup>
LCM Surface brightness uniform★3	$L_D$	80	----	----	%

★1 BACKLIGHT Block diagram :



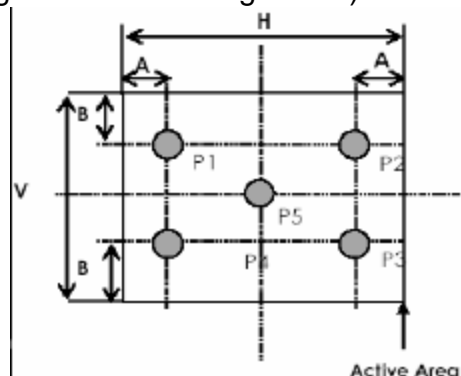
★2 Definition of Luminance:

From the LCD surface 50cm vertical suvery the center point , use BM-7 at field 1° when all pixels displaying white.

★3 Uniform measure condition :

(1)Measure 5 point. Measure location is show below :

(2)Uniform = (Min. brightness / Max. brightness)×100%



A:1/4 H

B:1/4 V

H,V:Active Area

Measurement device is TOPCON luminance meter BM-7

## 8. MODULE FUNCTION DESCRIPTION

### 8.1 PIN DESCRIPTION

Pin No	Symbol	Function
1	IM3(NC)	TBD
2	FMARK	Out a frame head pulse signal
3	SDI	Serial data input pin and used for RGB interface mode.
4	RS	“Low”: command. “High”: display data.
5	SCL	Write control pin for the DBI interface. When RGB interface is selected, this pin is used as serial clock pin.
6	RD	LCD station read enable. “Low” active.
7	CS	Chip select. “Low” active.
8	RESET	System reset. “Low” active.
9	IM0	interface select pin
10	IM1	
11	GND	System ground.
12-35	DB23-DB00	Data bus.
36	DE	Data enable signal in RGB interface mode.
37	PCLK	DOT clock signal in RGB interface mode.
38	HSYNC	Horizontal sync. signal in RGB interface mode.
39	VSYSN	Vertical sync. signal in RGB interface mode.
40	IOVCC	Power supply to interface pins
41	VCC	Power supply to liquid crystal power supply analog circuit.
42	GND	System ground.
43	LEDK	Cathode for back light power supply.
44	LEDA	Anode for back light power supply.
45	GND	System ground.

**REMARK:**

Select the MPU system interface mode:

IM1	IM0	MPU-interface mode	DB pin in use
0	0	8080 8-BIT	DB[7:0]
0	1	8080 16-BIT	DB[15:0]
1	0	8080 24-BIT	DB[23:0]
1	1	RGB 24-BIT	DB[23:0]

## **8.2 APPLICATION CIRC**

Please consult our technical department for detail information.

## **8.3 INITIAL CODE**

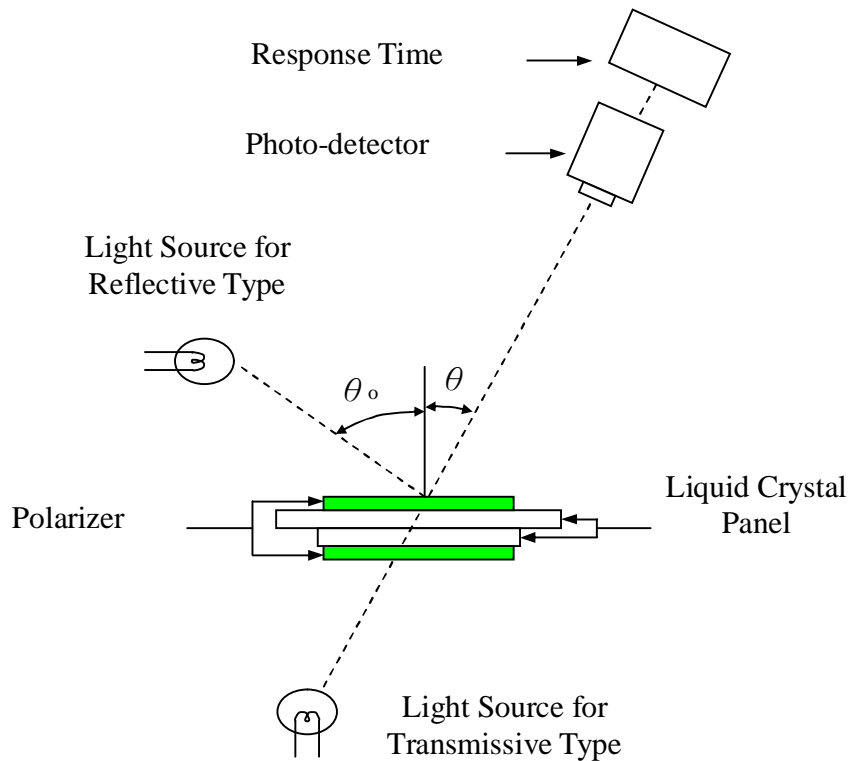
Please consult our technical department for detail information.



## 9. ELECTRO-OPTICAL CHARACTERISTICS

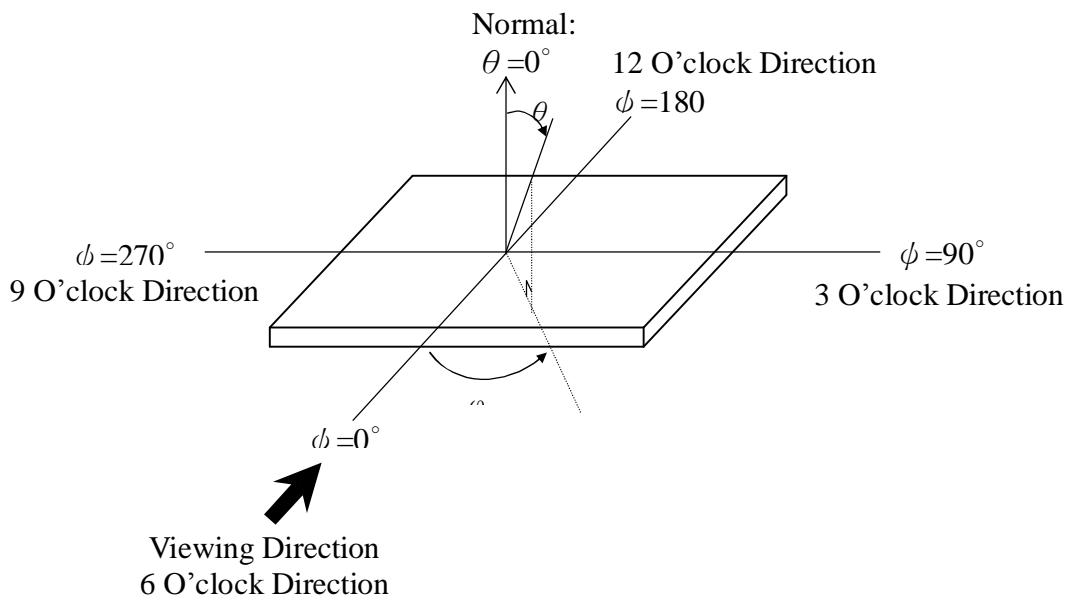
Electro-Optical Characteristics									
Item	Symbol		Condition	Temp.	Min.	Typ.	Max.	Units	Note
Viewing Angle Range	θ		ψ= 0° ψ= 90° ψ= 180° ψ= 270° (CR ≥ 10)	25℃	----	70	----	degree	Note 2
					----	70	----		
					----	70	----		
					----	70	----		
Response Time	Rise Time (Tr)		θ=ψ= 0° θ <sub>0</sub> = 25°	25℃	----	20	----	msec	Note 1,4
	Fall Time (Tf)				----	15	----		
Module Chromaticity	White	x	θ=ψ= 0°	25℃	0.26	0.2940	0.30	---	Note 3
		y			0.30	0.3288	0.34		
	Red	x			----	TBD	----		
		y			----		----		
	Green	x			----		----		
		y			----		----		
	Blue	x			----		----		
		y			----		----		
Module Contrast Ratio	CR		θ=ψ= 0°	25℃		650		---	Note3, 5

### Note 1: Electro-Optical Characteristics Test Method.



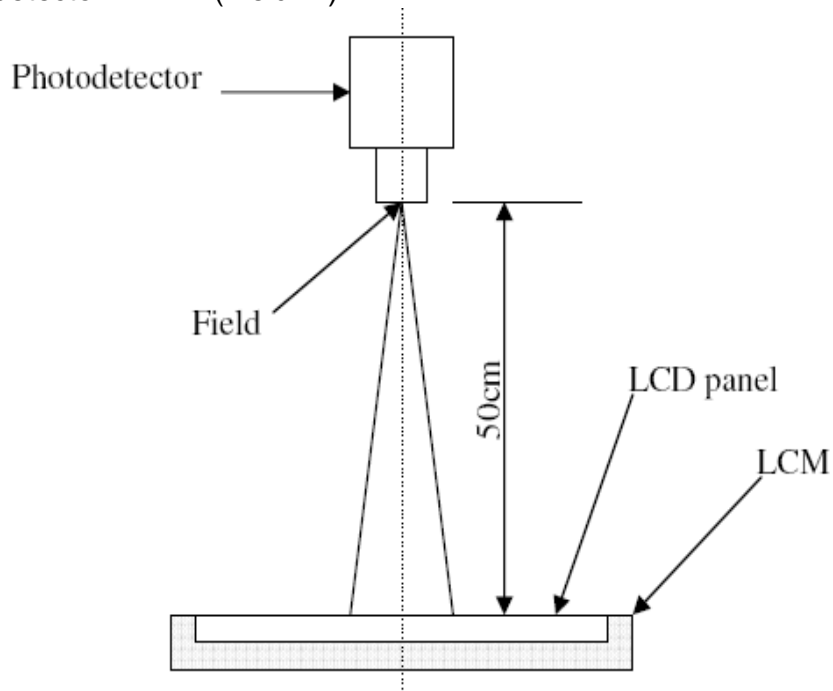
### Note 2: Definition of Viewing Angle.

Viewing angle is the angle at which the contrast ratio is greater than 2, for TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface.



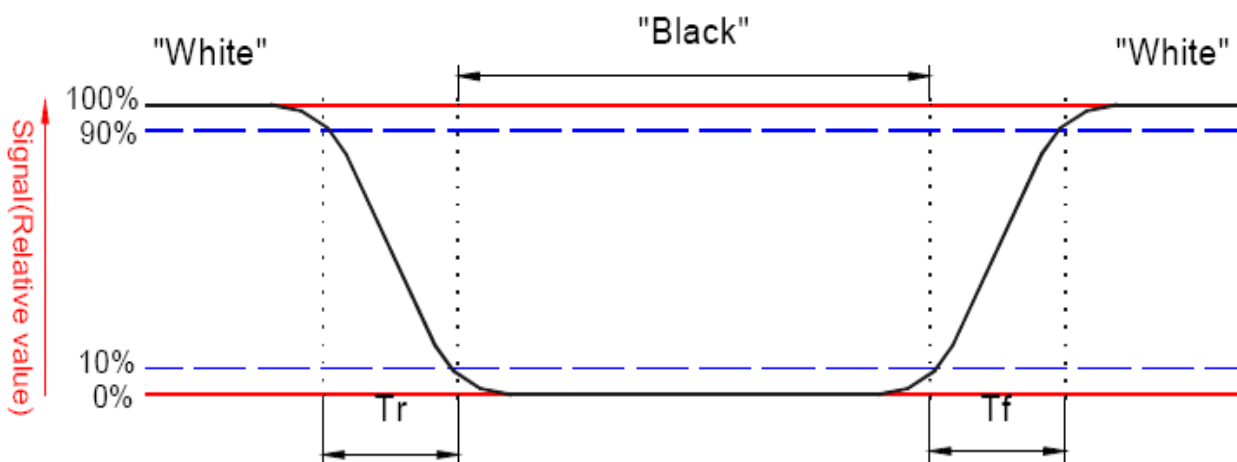
### Note 3: Optical measurement equipment setup

- Measurement should be executed in a stable, windless, and dark room. After lighting the backlight for 30mins.
- Environment condition : Common air conditioner cleanness  $T_a=25\pm5$   
Humidity= $60\pm15\%$
- Distance : 50cm
- Photodetector : BM-7 (Field 1°)



#### Note 4: Definition of Optical Response Time

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below:



#### Note 5: Definition of Contrast Ratio (CR).

Contrast ratio is calculated with the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector output when LCD is at "Black" state}}$$

## 10. RELIABILITY

### 10.1. MTBF

The LCD module shall be designed to meet a minimum MTBF value of 50000 hours with normal. (25°C in the room without sunlight)

### 10.2. Test condition

NO.	ITEM	CONDITION	CRITERION
1	High Temperature Non-Operating Test	80°C * 240Hrs	<ul style="list-style-type: none"> <li>No defect of operational functions in room temperature are allowable.</li> <li>IDD of LCM should be below specification.</li> </ul>
2	Low Temperature Non-Operating Test	-30°C * 240Hrs	
3	High Temperature/Humidity Operating Test	50°C * 90%RH * 240 Hrs	
4	High Temperature Operating Test	70°C * 240Hrs	
5	Low Temperature Operating Test	-20°C * 240Hrs	
6	Thermal Shock Test	-30°C (30Min) ↔ 80(30Min)* 10 Cycles	
7	ESD Test	Air discharge:±6KV Contact discharge:±4KV	

Notes:

- Judgments should be made after exposure in room temperature for two hours.
- The pure water is used for the high temperature / humidity test.
- The sample above is individually for every reliability tests condition.

## 11. INSPECTION CRITERIA

### 1. AQL(Acceptable Quality Level)

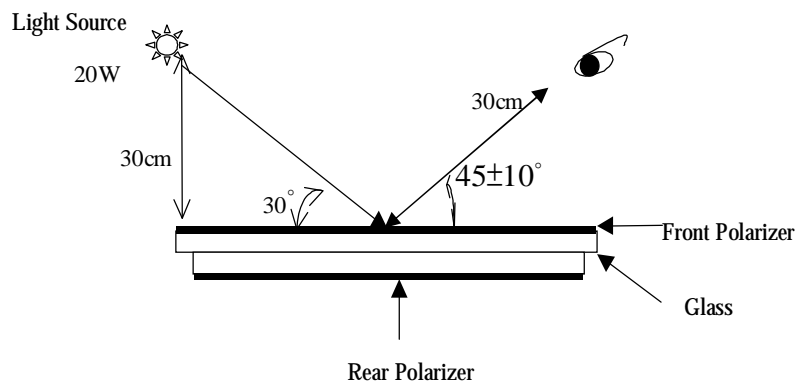
AQL of major and minor defect

	MAJOR DEFECT	MINOR DEFECT	MAJOR+MINOR
APPEARANCE	0.40%	1.0%	1.0%
ELECTRIC-OPTICAL	0.15%	0.15%	0.15%

### 2. Basic conditions for inspection

The LCM face to us, According to the criteria of luminance measurement instruction, About an angle of incidence 30,a distance of 30 cm with normal eye.with an angle of 45 degree to check the products without uncovering the film!

(As shown below).

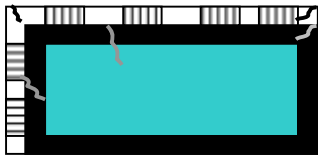
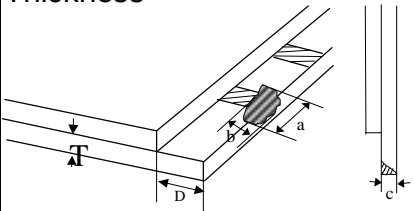
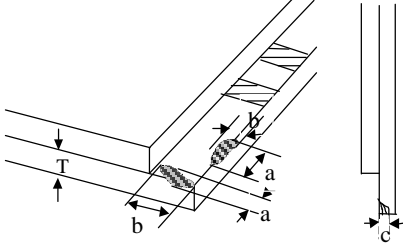
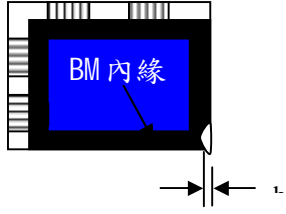


### 3. Inspection item and criteria

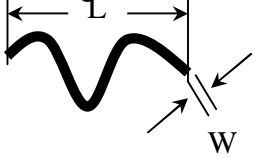
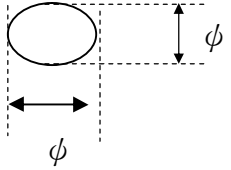
#### 3.1 Visual inspection criterion in immobility

##### 3.1.1 Glass defect

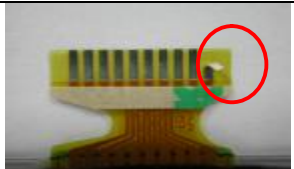
No	Defect item	Criteria	Remark
1	Dimension Unconformity (Major defect)	By Engineering Drawing	

No	Defect item	Criteria	Remark
2	Cracks (Major defect)	1) Not-extended crack according to the limit sample 2) Extended crack when $C \leq T$ and the crack touch $\leq 1/3$ sealant width is OK	
3	Glass extrude the conductive area (minor defect)	a: disregards and no influence assemblage 1) $b \leq 1/3$ Pin width (non bonding area) <b>【Accept】</b> 2) bonding area $\leq 0.5\text{mm}$ <b>【Accept】</b>	a: Length, b: Width
4	Pin-side, conductive area damaged (minor defect)	(a c: disregards) $b \leq 1/3$ of effective length for bonding electrode <b>【Accept】</b>	a: Length, b: Width, c: Thickness 
5	Pin-side, non-conductive area damaged (minor defect)	1) Damage area don't touch the ITO (Including contraposition mark, except scribing mark) <b>【Accept】</b> 2) $c < T$ $b \leq BM 1/3$ of width <b>【Accept】</b> 3) $c = T$ b not touch the seal glue <b>【Accept】</b> 4) a disregards	a: Length, b: Width, c: Thickness 
6	Non-pin-side damage (minor defect)	$c < T$ 1) b exceeds $1/3$ BM <b>【Reject】</b> $c = T$ b not touch the seal glue <b>【Accept】</b>	c: Thickness b: width of damage 

### 3.1.2 LCD appearance defect (View area)

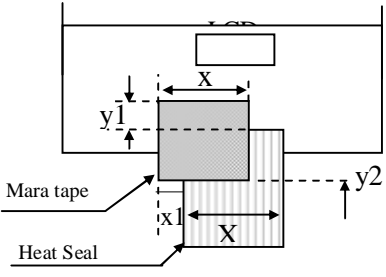
No	Defect item	Criteria		Remark
1	Fiber、glass cratch、polarizer scratch/folded (minor defect)	Specification	Allowable	note1: L: Length, W: Width note2: disregard if out of AA 
		$W \leq 0.03\text{mm}$	disregard	
		$0.03\text{mm} < W \leq 0.05\text{mm}$ ; $L \leq 3.0\text{mm}$	2	
		$0.05\text{mm} < W \leq 0.1\text{mm}$ ; $L \leq 3.0\text{mm}$	1	
		$W > 0.1\text{mm}$ ; $L > 3.0\text{mm}$	0	
2	Polarizer bubble、 concave and convex (minor defect)	$\psi \leq 0.2\text{mm}$	disregard	note 1: $\psi = (L+W)/2$ ; L: Length, W: Width note2: disregard if out of AA
		$0.2\text{mm} < \psi \leq 0.3\text{mm}$	2	
		$0.3\text{mm} < \psi \leq 0.5\text{mm}$	1	
		$0.5\text{mm} < \psi$	0	
3	Black dots、dirty dots、 impurities、eyewinker (Major defect)	$\psi \leq 0.10\text{mm}$	disregard	note 1: disregard if out of AA note2: Inspection by RGB pattern 
		$0.1\text{mm} < \psi \leq 0.15\text{mm}$	2	
		$0.15 < \psi \leq 0.2\text{mm}$	1	
4	Polarizer prick (Major defect)	$\psi \leq 0.1\text{mm}$	disregard	note1: $\psi = (L+W)/2$ ; L= Length, W=Width note2: the distance between two dots $> 5\text{mm}$
		$0.1\text{mm} < \psi \leq 0.25\text{mm}$	3	
		$\psi > 0.25\text{mm}$	0	

### 3.1.3 .FPC

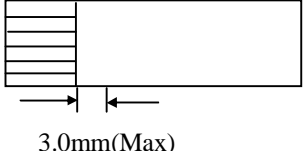
No	Defect item	Criteria		Remark
1	Copper screen peel (Major defect)	Copper screen peel 【Reject】		
2	No release tape or peel (Major defect)	No release tape or peel 【Reject】		
3	Dirty dot and impurity of FPC for customer using side (minor defect)	Specification	Allowable	note1: Cannot have stride ITO impurities
		$\psi \leq 0.25\text{mm}$	2	
		$\psi > 0.25$	0	

### 3.1.4 Black tape & Mara tape

No	Defect item	Criteria	Remark
----	-------------	----------	--------

No	Defect item	Criteria	Remark
1	FPC or H/S black tape shift (minor defect)	1.shift spec: 1)glue to the polarize <b>【Reject】</b> 2) IC bare <b>【Reject】</b> 2. left-and-right spec: 1) exceed of FPC edge or H-S edge <b>【Reject】</b> 2)IC bare <b>【Reject】</b>	
2	No black tape (Major defect)	No black tape <b>【Reject】</b>	
3	Tape position mistake (minor defect)	Not by engineering drawing <b>【Reject】</b>	
4	Mara tape defect (minor defect)	Peel before pulling the protecting film. <b>【Reject】</b>	

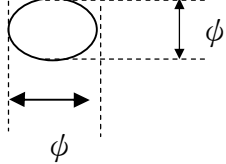
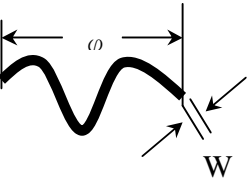
### 3.1.5 Silicon and Tuffy glue

No	Defect item	Criteria	Remark
1	Quantity of silicon (minor defect)	Uncover the ITO and circuit area. <b>【Reject】</b>	note: compared by engineering drawing.
2	Tuffy glue (minor defect)	1. Uncover the reveal copper area <b>【Reject】</b> 2. Cover layer 0.3mm(Min) ~ 3.0mm(Max) <b>【accept】</b>	note:if customer has special requirement , refer to the technical document. 
3	Depth of glue covering (minor defect)	Depth of glue covering overtop front Polarizer <b>【Reject】</b>	Except of the special requirement.

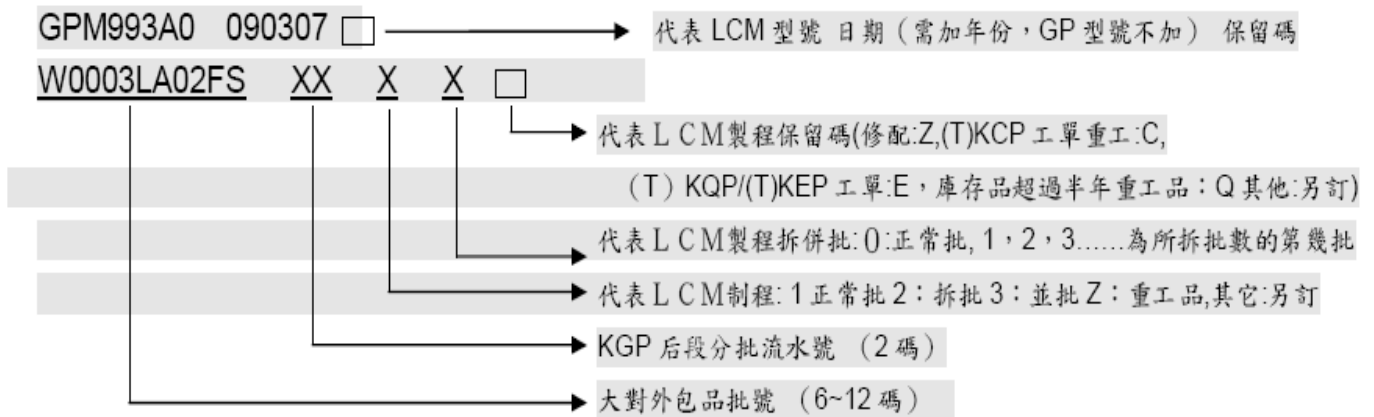
### 3.2 Electrical criteria

No	Defect item	Criteria	Remark
1	No display (Major defect)	No display <b>【Reject】</b>	
2	Missing line (Major defect)	Missing line <b>【Reject】</b>	



No	Defect item	Criteria	Remark	
3	Seg-com light and dark (Major defect)	Seg-com light and dark 【Reject】		
4	No display in immobility (Major defect)	No display in immobility 【Reject】		
5	Flicker of Pattern (Major defect)	Flicker of Pattern 【Reject】		
6	Over current (Major defect)	Over current 【Reject】		
7	Voltage out of specification (Major defect)	Voltage out of specification 【Reject】		
8	Pattern blur ,error code (Major defect)	Pattern blur ,error code 【Reject】		
9	Dark light, Flicker (Major defect)	Dark light, Flicker 【Reject】		
10	Black/White dots 、 Dirty dots、 eyewinker  (Major defect)	Specification	Allowable	Note1: disregard if out of AA note2: Inspection by RGB pattern 
		$\psi \leq 0.10\text{mm}$	disregard	
		$0.10\text{mm} < \psi \leq 0.15\text{mm}$	2	
		$0.15\text{mm} < \psi \leq 0.2\text{mm}$	1	
11	Fiber、 glass cratch、 polarizer scratch/folded  (minor defect)	$W \leq 0.03\text{mm}$	disregard	note1: L: Length, W: Width note2: disregard if out of AA 
		$0.03\text{mm} < W \leq 0.05\text{mm} ;$ $L \leq 3.0\text{mm}$	2	
		$0.05\text{mm} < W \leq 0.1\text{mm};$ $L \leq 3.0\text{mm}$	1	
		$W > 0.1\text{mm} ; L > 3.0\text{mm}$	0	

## 12. ILLUSTRATION OF LCD DATE CODE



## 13. PRECAUTIONS FOR USE

### 13.1 SAFETY

- (1) Do not swallow any liquid crystal, even if there is no proof that liquid crystal is poisonous.
- (2) If the LCD panel breaks, be careful not to get liquid crystal to touch your skin.
- (3) If skin is exposed to liquid crystal, wash the area thoroughly with alcohol or soap.

### 13.2 STORAGE CONDITIONS

- (1) Store the panel or module in a dark place where the temperature is  $23\pm5^{\circ}\text{C}$  and the humidity is below  $45\pm 20\%\text{RH}$ .
- (2) Store in anti-static electricity container.
- (3) Store in clean environment, free from dust, active gas, and solvent.
- (4) Do not place the module near organics solvents or corrosive gases.
- (5) Do not crush, shake, or jolt the module.

### 13.3 HANDLING PRECAUTIONS

- (1) Avoid static electricity, which can damage the CMOS LSI.
- (2) The polarizin plate of the display is very fragile. so, please handle if very carefully.
- (3) Do not give external shock.
- (4) Do not apply excessive force on the surface.
- (5) Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- (6) Do not use ketonics solvent & Aromatic solvent, use with a soft cloth soaked with a cleaning naphtha solvent.

- (7) Do not operate it above the absolute maximum rating.
- (8) Do not remove the panel or frame from the module.

#### **13.4 WARRANTY**

The period is within twelve months since the date of shipping out under normal using and storage conditions.

## 14. MARK AND PACKAGING

KS GIANPLUS OPTOELECTRONICS TECHNOLOGY CO., LTD.			
KG M1066A0 包裝示意圖		DCN	A4
DWG: KG M1066A0 包裝 A	P 1 OF 1	VERSION	A

( 1 ) TRAY型號：KPL0M1066A01-1A <div><div>② 第②面</div><div>① 正面</div></div> <div>TRAY</div>	( 2 ) 疊放次序為 <div><div>①</div><div>②</div><div>③</div><div>④</div></div> <div></div>	( 3 ) 用tape膠帶將tray盤固定 <div></div>	( 4 ) tray盤用EPE包裹 <div></div>
( 5 ) <div></div>	( 6 ) <div></div>	( 7 ) 包裝數量：製品 104 pcs / 1小箱 1個TRAY裝產品 8 pcs 13個實裝產品TRAY，1個空TRAY <div></div> 完成小箱包裝	( 8 ) 在大箱中加入瓦楞紙墊和護圍 <div></div>
( 9 ) <div></div> 4小箱裝入一大外箱	(10) 大箱在封箱前加墊瓦楞紙 <div></div>	(11) 52個實裝產品TRAY，4個空TRAY 包裝數量：製品 416 pcs / 1大箱 <div></div> 完成包裝	

包裝次序(1)-(11)

非保稅產品內，  
外箱使用帶  
GIANPLUS  
LOGO的膠帶

NOTE:

- 內、外箱必須用膠帶封箱
- 小箱中如有空隙，
- 用適當EPE或氣泡膜填充

## 15. FACTORY

## 16. REVISION HISTORY

Rev NO.	Revise record	Rev Date
1.0	Original revision	2017/12/07