

Subject

Date

1

0 - loadA src

8 - JAT

2 1 - STA dest

9 - LDCC src

2 - XOR src

A - IBME vec

3 3 - SBC src

B - IBME vec

4 - ROR

C - LDI

4 5 - HLT

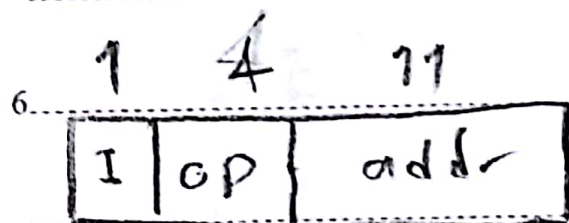
D - STTI

6 - JMP vec

E - LOR src

5 7 - ADC src

F - LAND src



I = indirect

Alu op = Alu op

Alu op	Alu op	vec	src
0 - 0	0 - 0	vec	src

1 XOR 1 10

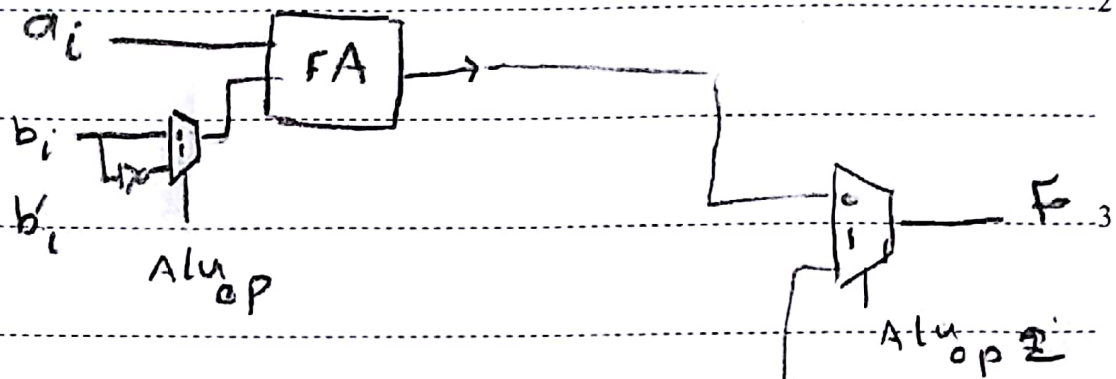
2 sub 0 - 1

3 ror 1 11

4 or 1 01

5 and 1 00

Add & sub

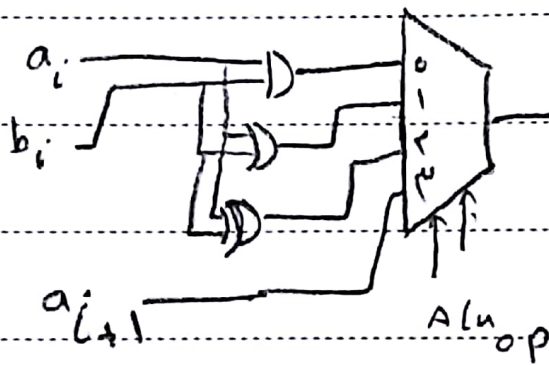


xOR

ror

And

oR



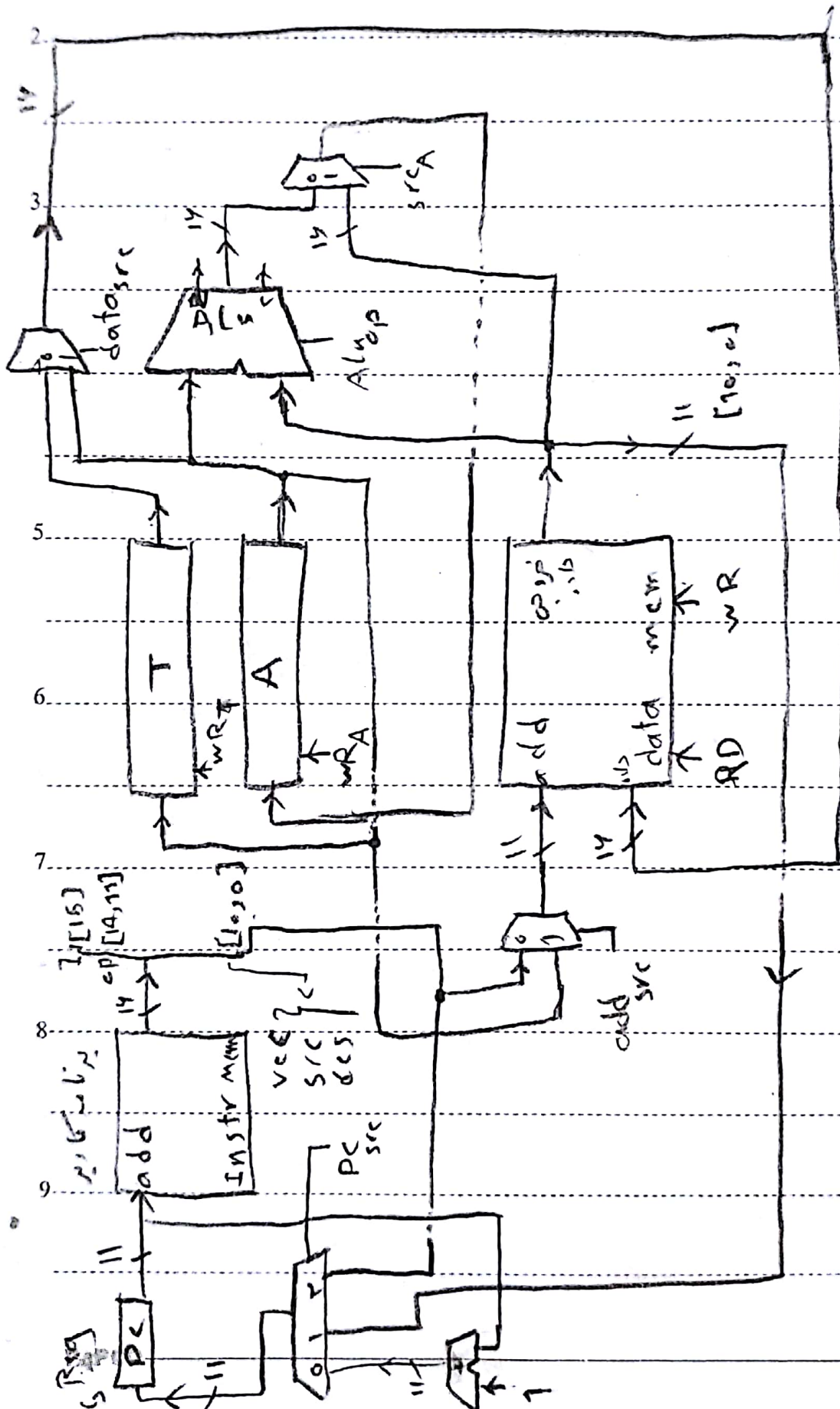
برای بیت آخر $L_{314} = carry$ می باشد.

Subject

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1

طراحی single cycle



Subject

Date

طراحی واحد کنترل پردازنده

LD	STA	STI	IBNE	TAT	XOR	JMP	
00	00	00	02'	00	00	10	PC _{src}
1	0	0	0	0	1	0	WR _A
1	—	—	—	—	0	—	src _A
0	0	0	0	1	0	0	WR _T
—	—	—	—	—	110	—	ALU _{OP}
1	0	0	1	0	1	0	RD
0	1	1	0	0	0	0	WR
0	0	1	0	—	0	—	add _{src6}
—	0	1	—	—	—	—	data _{src}

Subject

Date

1

OP

↓

dec
F E D C B A n v y d e c y 1



WRA

ادامہ طراح واحد کٹر

3

1 1 1 1

PC SRC₀

4

PC SRC₁

5

6

7

8

9