Scientific Python @SciPyTip · 7 мая

Got a suggestion for a tip? Got a relevant project you want to plug? Let me know. bit.ly/NbTt4

Sergey Sobko
@IwasakiRussia

@SciPyTip I'm creating a lib for simulating digital logic circuits (e.g. produce Simulink models from Py code). GH: profitware/circuitrylib

🌐 Показать перевод

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РЕТВИТОВ

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ИЗБРАННЫХ

1:42 - 8 мая 2014 г.

Ответить @SciPyTip

Christopher Felton @FeltonChris · 8 мая

@IwasakiRussia this already exists :), see #myhld myhdl.org

Sergey Sobko @IwasakiRussia · 8 мая

@FeltonChris thank you. MyHDL project looks great and I would have used it as a core if not started my own ;)

Sergey Sobko @IwasakiRussia · 8 мая

@FeltonChris if I have free time I'll write an adapter to MyHDL. The main goal of projects is slightly different as far as I can see.

Christopher Felton @FeltonChris · 8 мая

@IwasakiRussia it is never too late you switch. You probably you have more momentum if you did!

Christopher Felton @FeltonChris · 8 мая

@IwasakiRussia As a weekend project way back I did something similar to your project, bit.ly/RrBx6X

Christopher Felton @FeltonChris · 8 мая

@IwasakiRussia but I haven't had the time or motivation to complete it.

Sergey Sobko @IwasakiRussia · 8 мая

@FeltonChris you're right! As I haven't started internal simulation part yet (as not necessary for my coursework) I'll entrust this to MyHDL

Sergey Sobko @IwasakiRussia · 8 мая

@FeltonChris thus the only thing to throw out is this: github.com/profitware/cir...

Наверх ↑