### 115V N-Channel Enhancement Mode MOSFET

Voltage

115 V

Rdson

7.6m ohm

#### Feature:

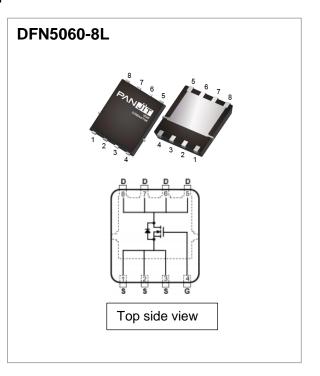
- $R_{DS(ON)}$ ,  $V_{GS}@10V$ ,  $I_D@20A<7.6m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS}@4.5V$ ,  $I_D@10A<10.5m\Omega$
- High switching speed
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### **Mechanical Data**

- Case: DFN5060-8L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0028 ounces, 0.08 grams

### **Application**

- PD Quick Charger SSR
- Isolation converter SSR



# **Absolute Maximum Ratings** (T<sub>A</sub> = 25 °C unless otherwise specified)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V <sub>DS</sub>	115	V	
Gate-Source Voltage		V <sub>GS</sub>	+20/ -12		
Continue Current	Tc=25°C		91.4	^	
Continue Current	T <sub>C</sub> =100°C	I <sub>D</sub>	57.8	Α	
ulsed Drain Current (Note 1) T <sub>C</sub> =25°C		I <sub>DM</sub>	365	Α	
Single Pulse Avalanche Current (Note 6)		I <sub>AS</sub>	37	Α	
Single Pulse Avalanche Energy (Note 6)		E <sub>AS</sub>	68	mJ	
Power Dissipation	Tc=25°C	PD	125	W	
	T <sub>C</sub> =100°C	PD	50		
Operating Junction and & Storage Temperature Range		T <sub>J</sub> ,T <sub>STG</sub>	-55~150	°C	

### **Thermal Characteristics**

PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNITS
	Junction-to-Case (Drain)	$R_{\theta JC}$	0.9	1	°C/W
	Junction-to-Compound (Top)	$R_{\theta JT}$	19.3	23	°C/W
	Junction-to-Ambient	R <sub>θJA</sub>	41	50	°C/W

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# **Electrical Characteristics** (T<sub>A</sub> = 25 °C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	115			V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1.2	1.7	2.5		
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A		6.1	7.6	mΩ	
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A		8.4	10.5		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =115V, V <sub>GS</sub> =0V			1	uA	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =+20V, V <sub>DS</sub> =0V			100	nA	
Dynamic (Note 7)							
Total Gate Charge	Qg	V 60V I 50A	-	77	105		
Gate-Source Charge	Qgs	V <sub>DS</sub> =60V, I <sub>D</sub> =50A, V <sub>GS</sub> =10V (Note 1,2)	-	25	-	nC	
Gate-Drain Charge	Qgd		-	7.7	-		
Input Capacitance	Ciss	\/ CO\/ \/ O\/	-	4740	-		
Output Capacitance	Coss	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V,	-	338	-	pF	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	36	-		
Turn-On Delay Time	td(on)	V <sub>DD</sub> =60V, I <sub>D</sub> =50A,	-	34	-		
Turn-On Rise Time	tr		-	111	-	ns	
Turn-Off Delay Time	td(off)	$V_{GS}=10V, R_{G}=6\Omega$ (Note 1,2)	-	116	-		
Turn-Off Fall Time	tf	(14010-1,2)	-	119	-		
Gate Resistance	Rg	VDS=Open, F=1MHz		1.6		Ω	
Drain-Source Diode							
Maximum Continuous Drain-Source		Tc =25 °C			407	^	
Diode Forward Current	Is		-	-	107	Α	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>GS</sub> =0V	-	0.68	1	V	
Reverse Recovery Time	Trr	VR =100V, Is= 10A		62.7		ns	
Reverse Recovery Charge	Qrr	di/dt = 100A/us, Tj=25 °C (Note 1.2)		98		nC	

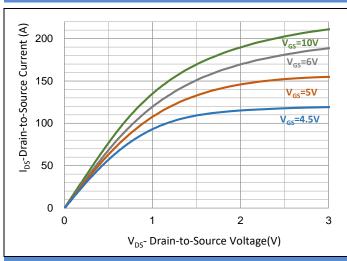
#### NOTES:

- 1. Pulse width<300us, Duty cycle<2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> =25°C.
- 4. The maximum current rating is package limited.
- 5. R<sub>BJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
- 6. The test condition is L=0.5mH,  $I_{AS}$ =37A,  $V_{DD}$ =70V,  $V_{GS}$ =10V,  $R_{G}$ =25ohm, Starting  $T_{J}$ =25°C
- 7. Guaranteed by design, not subject to production testing.



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#### **TYPICAL CHARACTERISTIC CURVES**



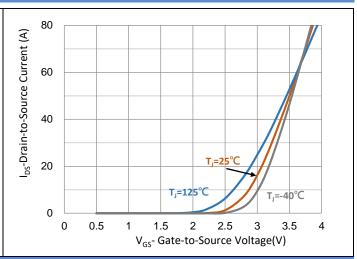
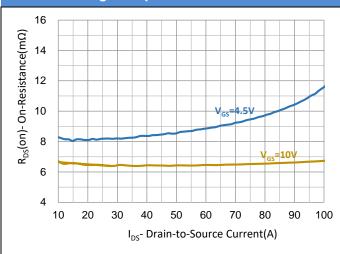


Fig.1 Output Characteristics



### Fig.2 Transfer Characteristics

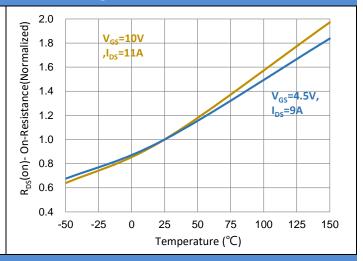


Fig.3 On-Resistance vs. Drain Current

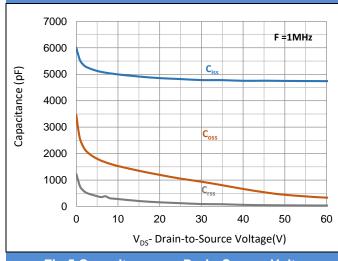


Fig.4 On-Resistance vs. Junction temperature

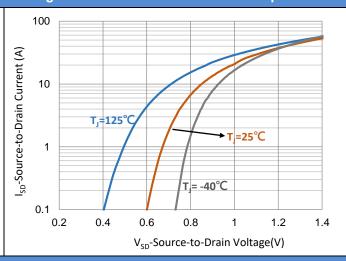


Fig.5 Capacitance vs. Drain-Source Voltage

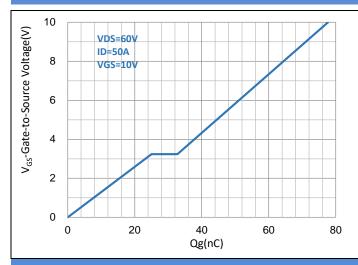
Fig.6 Source-Drain Diode Forward Voltage



# **Preliminary**

# PSMQC076N12LS1

#### **TYPICAL CHARACTERISTIC CURVES**



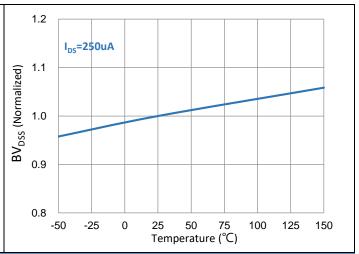
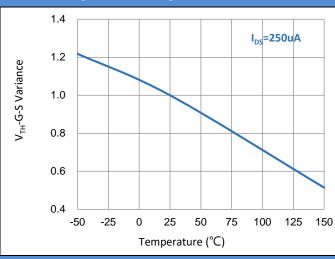


Fig.7 Gate-Charge Characteristics

Fig.8 Breakdown Voltage Variation vs. Temperature



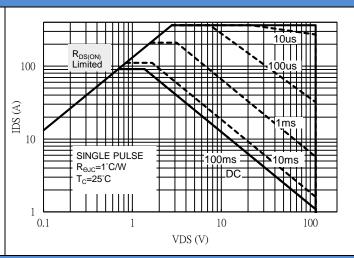
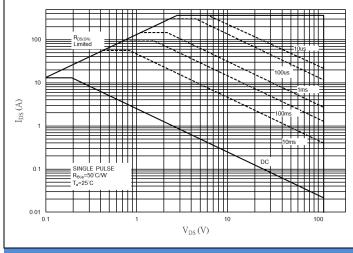


Fig.9 Threshold Voltage Variation with Temperature

Fig.10 Maximum Safe Operating Area



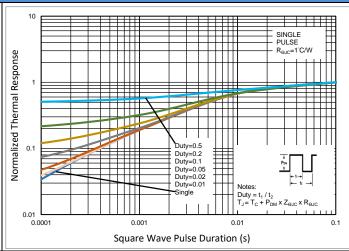


Fig.11 Maximum Safe Operating Area

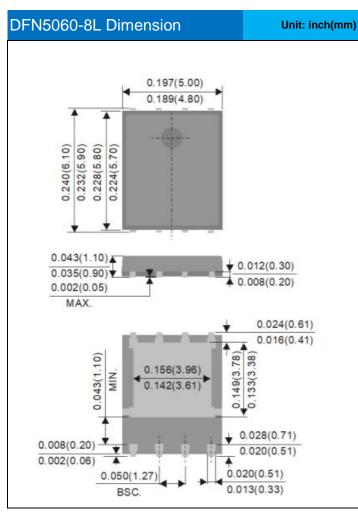
Fig.12 Normalized Transient Thermal Impedance

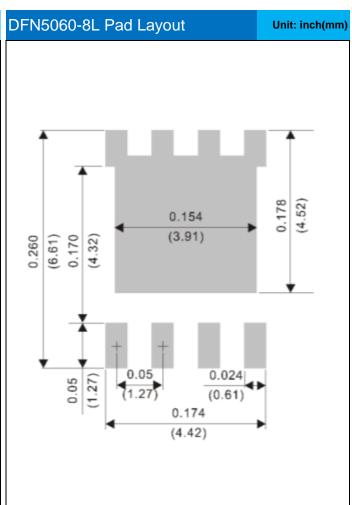


### **Ordering Information:**

Ordering Code No.	Package Type	Packing Type	Marking	Version
PSMQC076N12LS1_R2_00001	DFN5060-8L	3000pcs / 13" reel	076N12LS	Halogen free

## **Packaging Information & Mounting Pad Layout**





# **Preliminary**

# PSMQC076N12LS1

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