Title

PF315-085

Subtitle

MIT Lincoln Laboratory - RTDS HIL Mapping Box



Project

PF315-085

Date

July 20, 2015

Revision

REV1.4

Project Manager

Alexandre Leboeuf



VersionHistory

Date	User	From REV	To REV	Page Name	Note Number	Change Notes
27/04/2015	PFM		1.0			First Release
30/06/2015	PFM	1.0	1.1			Remove conditionning board, add ethernet and RS485 interface
13/07/2015	PFM	1.2	1.3			Add current output board, move transformer to mapping #2
20/07/2015	PFM	1.3	1.4			Add space for Woodward controller
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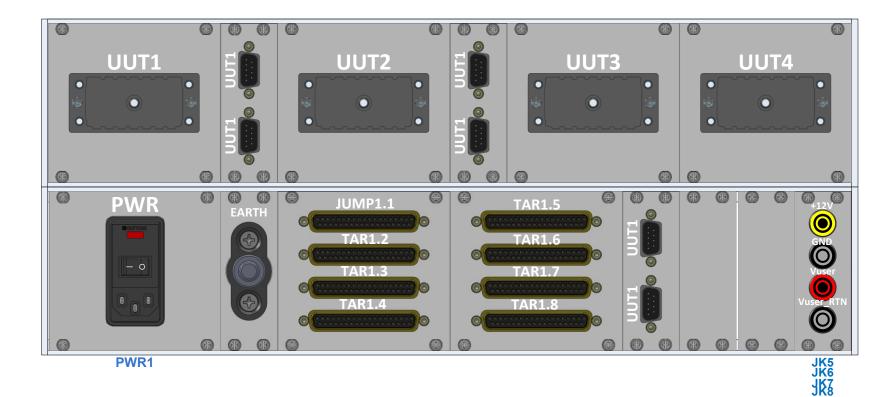
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	PF315-085			July 20, 2015
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	PASCAL FEITER-MURPHY		2 OF 8	
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Mapping Box, 4U P/N: 210-0021

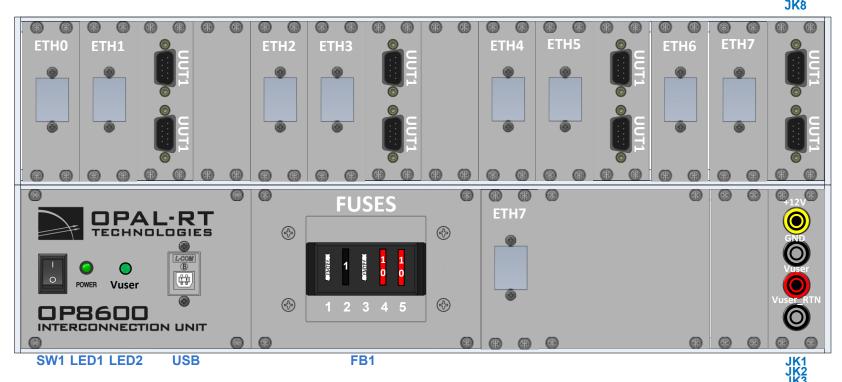
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OPAL-RT PROJECT # DESCRIPTION

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Alexandre Leboeuf

Blue Label are Drawing Reference

White Label are Drawing Reference & a Sticker must be put on plate

PF315-085_MAPPINGBOX#1_OVERVIEW_REV1.4.VSD

REV1.4



FRONT

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PS ☐ ☐ AC Line △ AC Neutral ☐ M AC Earth 4 GND (+12V PS3 PS2 PCB₁ 000000 PCB₂

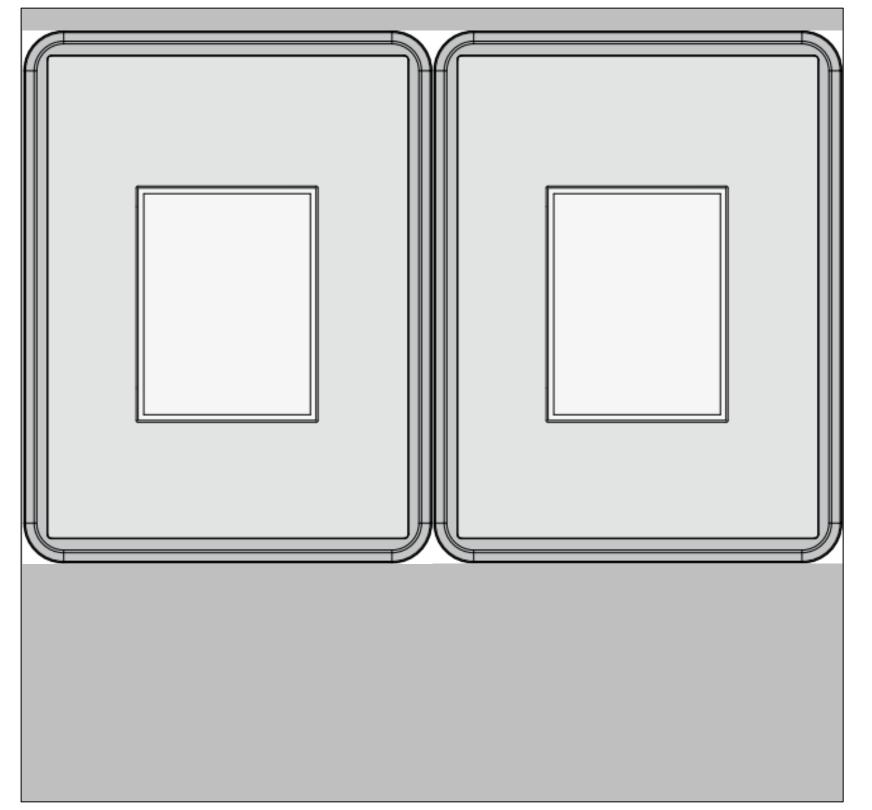
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REV1.4 OPAL-RT PROJECT # DESCRIPTION PF315-085 July 20, 2015 **Top Side Overview** DRAWN BY 4 OF 8 PASCAL FEITER-MURPHY FILENAME APPROVED BY PF315-085_MAPPINGBOX#1_OVERVIEW_REV1.4.VSD Alexandre Leboeuf

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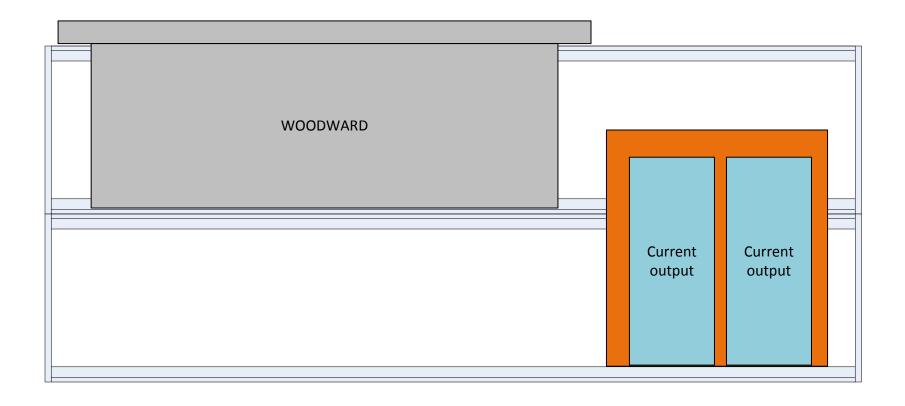


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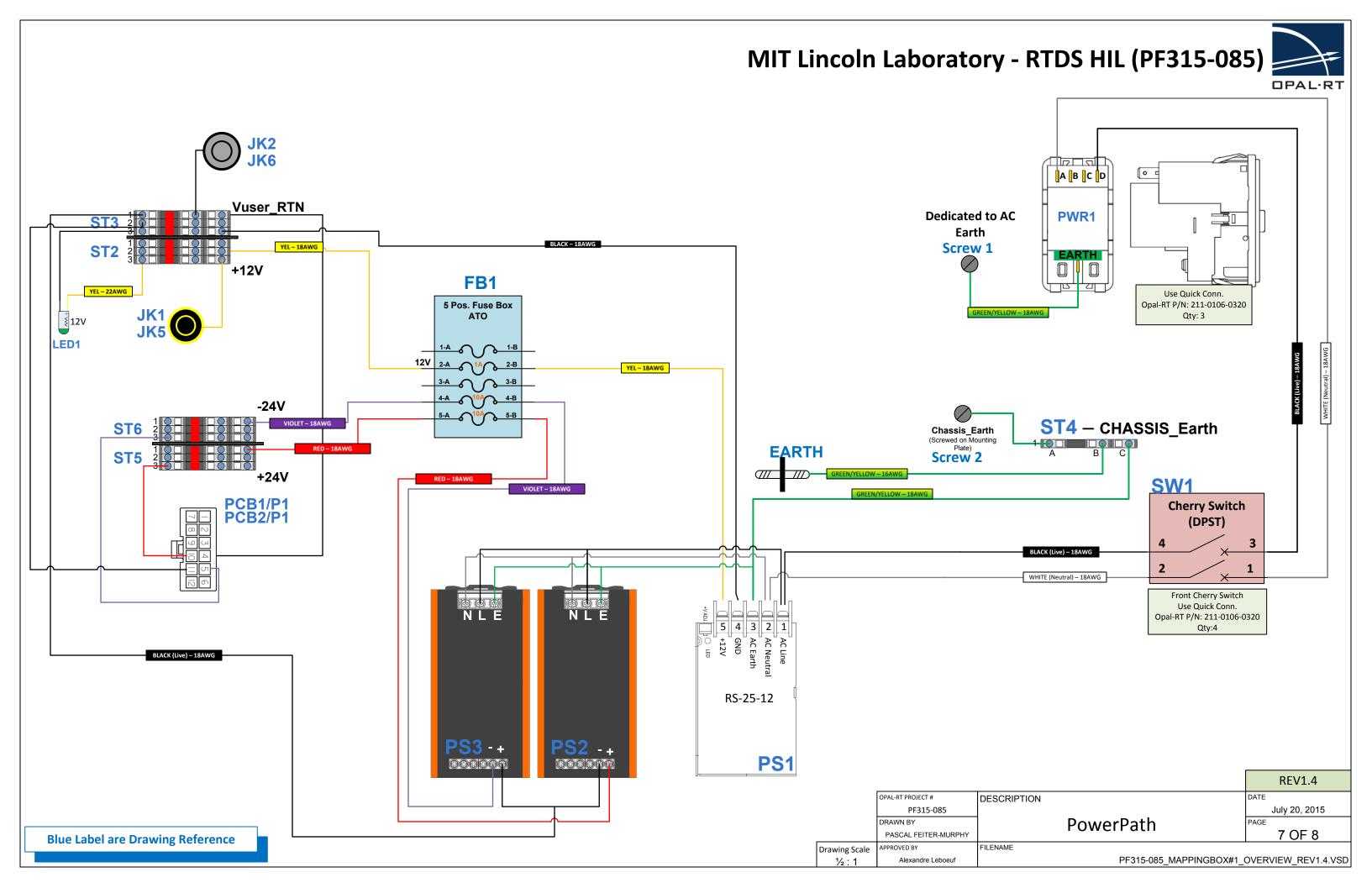
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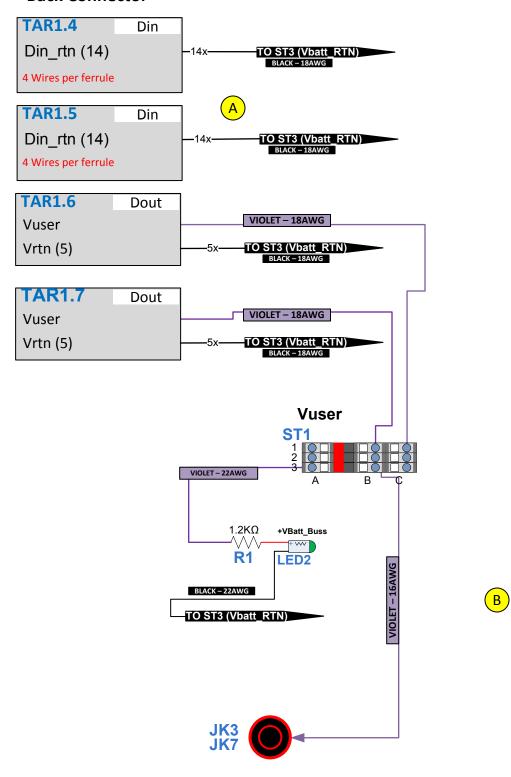
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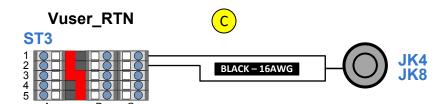


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Back Connector





				REV1.4
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Blue Label are Drawing Reference