

*Title*

# PF315-085

*Subtitle*

## MIT Lincoln Laboratory - RTDS HIL Hardware Overview



**OPAL-RT**

<i>Project</i>	PF315-085
<i>Date</i>	mai 19, 2015
<i>Revision</i>	<b>REV 1.2</b>
<i>Project Manager</i>	Alexandre Leboeuf

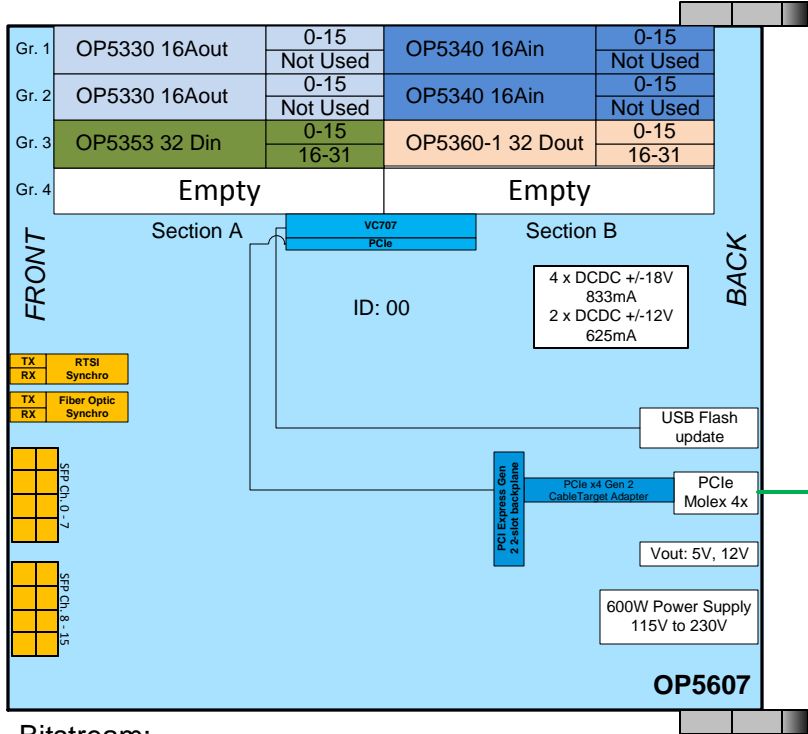


# VersionHistory

Date	User	From REV	To REV	Page Name	Note Number	Change Notes
2015-04-09	PFM		1,0	All		Initial release
2015-04-09	PL	1,0	1,1			Use recent template for OP5607, add optical fiber cable for PCIe comm
2015-05-19	PL	1,1	1,2			Change PCIe cards position

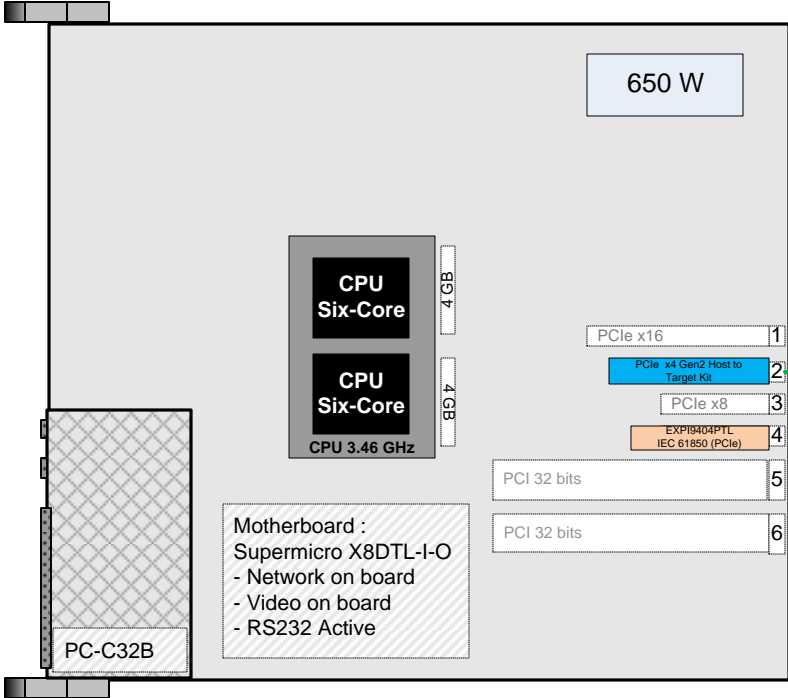


System #2 (PF315085S02)



Bitstream:

System #1 (PF315085S01)



PCIe x4 Fiber Optic  
213-0100-0111

Operating System	:	REDHAT
Chassis Type	:	Industrial-PC & OP5607 Chassis (Virtex 7)
Motherboard	:	Supermicro X8DTL-I
Processor	:	Intel Six-Core/Xeon 3.46GHz
Memory	:	4 Gb
OP5340-1 Ain	:	2x 16 ch
OP5330-3 Aout	:	2x 16 ch
OP5353-1 Din	:	1x 32 ch
OP5360-1 Dout	:	1x 32 ch
Bitstream : 32Ain/32Aout, 16Din/16Dout, 16 PWMIn, 16 PWMout		

REV 1.2	
OPAL-RT PROJECT # PF315-085	DESCRIPTION Configuration
DRAWN BY PASCAL FEITER-MURPHY	DATE mai 19, 2015
APPROVED BY Alexandre Leboeuf	PAGE 3 OF 3
FILENAME PF315-085_HARDWARE_OVERVIEW_REV1.2.VSD	