**Address:** SS Sreekara Appartments, Harika Pride **Email\_id:**  manojkumarmadiraju@gmail.com

E – 501, Swaroop Nagar, Road No: 10, **Mobile:** 9908806746

uppal , Hyderabad.

**Career Objective:**

To work in a professional environment where I can enhance my knowledge in the latest technologies which encourages me to succeed in my career professionally by utilizing my academic skills and also for the development of the organization.

**Academic Qualification:**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **S.no** | **Course** | **Institution** | **Board/University** | **Year of completion** | **Aggregate**  **(C.G.P.A)**  **Or(%)** |
| 1. | B.Tech  (Electronics and Communication Engineering) | Anurag Group of Institutions  (Formerly C.V.S.R college of Engineering) | Jawaharlal Nehru Technological University - Hyderabad | 2020 | 8.48 C.G.P.A |
| 2. | Intermediate(M.P.C) | Sri Chaitanya Junior College | Board of Intermediate Education - TS | 2016 | 81.7% |
| 3. | Secondary School | Vijayaratna Concept School. | Board of Secondary  Education-AP | 2014 | 9.7 C.G.P.A |

**Technical Skills:**

* Basics in C programming
* Verilog HDL(VLSI), Matlab
* Arduino(hardware)
* Microsoft Office (Word, Powerpoint)

**Projects Undertaken:**

* **Major Project :**

**Project Title:** Change Detection in Satellite images using CNN and PCA K means Clustering.

**Brief Description:** The project focus on the change detection of high resolution satellite images using  pre trained Convolutional Neural Networks (CNN) and this method can omit the limited performance of hand-crafted features and a change map was computed using pixel-wise Euclidean distance.

* **Mini Project :**

**Project Title:** Design of Current Mirror based Two-stage CMOS Cascode OP-AMP for High frequency applications.

**Brief Description:** The project mainly focuses on a High slew rate, Moderate gain,

Ultra wide band two stage CMOS cascode operational amplifier for

“Radio Frequency” application. Current mirror based cascoding technique is used and implemented on 180nm technology. This radio frequency integrated circuit (RFIC) is in high demand for VLSI.

* **Agama Tech Fest Project :**

**Project Title:** car parking management system

**Brief Description:** The main aim of the project is to provide a high-end car parking slot by making most of the working mechanism automatic with relative less budget for the convenience of the user in the smart city industrial parks by reducing the chaos in parking.

**Strengths:**

* Good team player
* Creative thinking
* Quick learner and can work under pressure

**Workshops attended:**

* Real-Time Application Using Matlab With raspberry PI
* VLSI Design (Maven technologies)

**Achievements:**

* Secured “2nd prize” in Project Presentation in “AAGAMA Tech Fest” conducted by Anurag Group of Institutions.

**Hobbies:**

* Writing stories
* Surfing articles related to Visual effects and exploring it.

**Personal Details:**

* Date of birth : 25th May 1998
* Father’s name : Madiraju Shyam Prasad
* Languages Known : English, Hindi, Telugu (Mother- tongue)

**Declaration:**

I hereby declare that all the above-mentioned particulars filled in by me are correct and true.

Place:

Date: Signature.