



\* Two MPU-60X0s can be connected to the same I2C bus
The LSB bit of the 7 bit address is determined by the logic level on pin ADO.
Default Address = 0x68 (pin ADO is logic low)
Alternative Address =0x69 (pin ADO is logic high)

\*\* Optional external reference clock input. Connected to GND by default.

Cut trace for external clock

\*\*\* Frame synchronization digital input. Connected to GND by default
Cut trace for external sync



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