



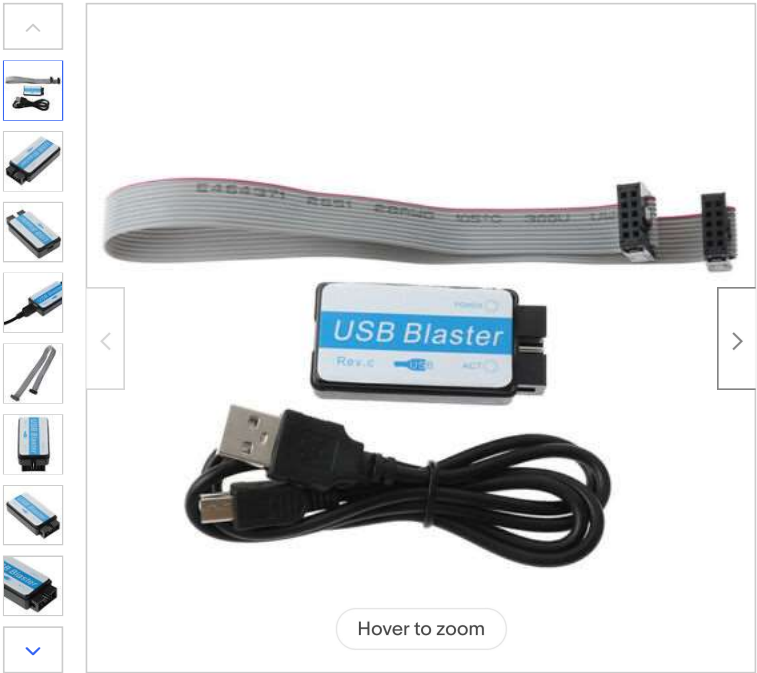
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Mini Usb Blaster Cable For ALTERA CPLD FPGA NIOS JTAG Altera Program.Z.YK

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Description&Preview

shipping&policy

Mini Usb Blaster Cable For ALTERA CPLD FPGA NIOS JTAG Altera Program.Z.YK

DESCRIPTION:

Description:

1. Support 1.5V,1.8V,2.5V,3.5V and 5.0V.
2. Support all ALTERA products : CPLD (MAX3000, MAX7000, MAX9000 and MAX II) ; FPGA (Stratix, StratixII, Cyclone, CycloneII, ACEX 1K, APEX20K, EPCS1, EPCS4, EPCS16)
--- Now we have tested chip using this tool is: Cyclone (EP1C3, EP1C6, EP1C12, EP1C20) ; Cyclone II (EP2C5, EP2C8, EP2C35) ; Stratix (EP1S10, EP1S10K(EPF10K10, EPF0K30) ; ACEX1K(EP1K30) ; MAX7000(MAX7128SLC84, MAX7128) ; AETC100(MAX3000, MAX3128) ; MAXII(MAXII240, MAXII240S4, EPCS16) ; EPC(EPC1, EPC4)
3. Support AS, PS and JTAG program (with Verify and BankCheck function) .
--- When we tested the chip above , we use the different program mode: JTAG (Cyclone, CycloneII, Stratix II, Flex10K, Acex1K, Max7000 and Max3000) (Stratix , Stratix GX)
4. Support embedded logic analyzer function of SignalTapII
5. Support NIOS II communication and debugger-- When you use it to debug your Black Gold , it will not Pop-up warning
6. Faster -- about 6 times than ByteblasterII
7. USB interface ! -- you don\'t need a PC with serial port now
8. 100% compatible with Official ALTERA USB Blaster

Package Included:

- 1 X USB Blaster
- 1 X USB cable
- 1 X 10pin JTAGE cable

Preview:



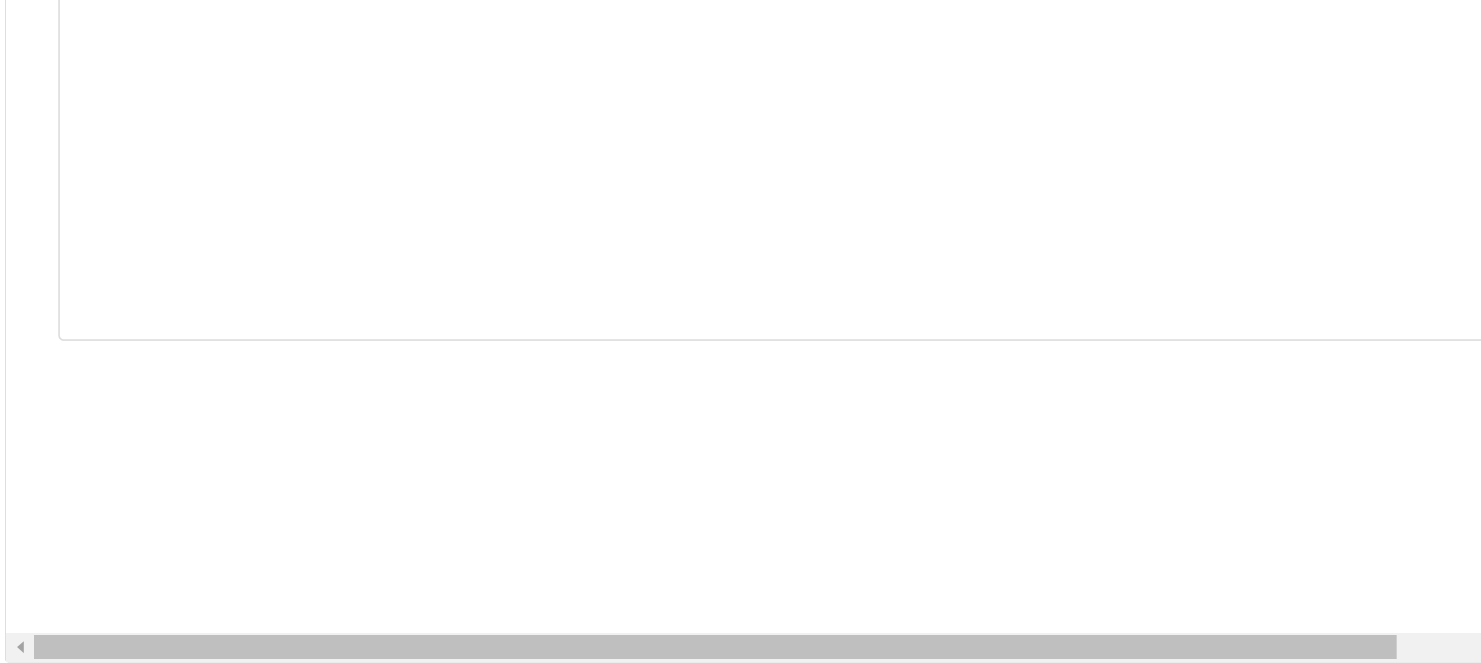












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