

ELC 2137 LaTeX Tutorial

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Summary

Students were required to insert and center a floating table with necessary numbers, manipulate a screenshot to fit properly, and list questions. By coding all of the following in Latex students are able to produce a professional report.

Q&A

1. MMARTIN1999
2. Itemize Environment/Command
3. $y(t) = 1/2e^2$
4. F5

Results

In this section, put your simulation waveforms, results tables, pictures of hardware, and any other required items.

Binary	Hex	Decimal
0000	0	0
0010	2	2
0100	4	4
1000	8	8
1010	A	10

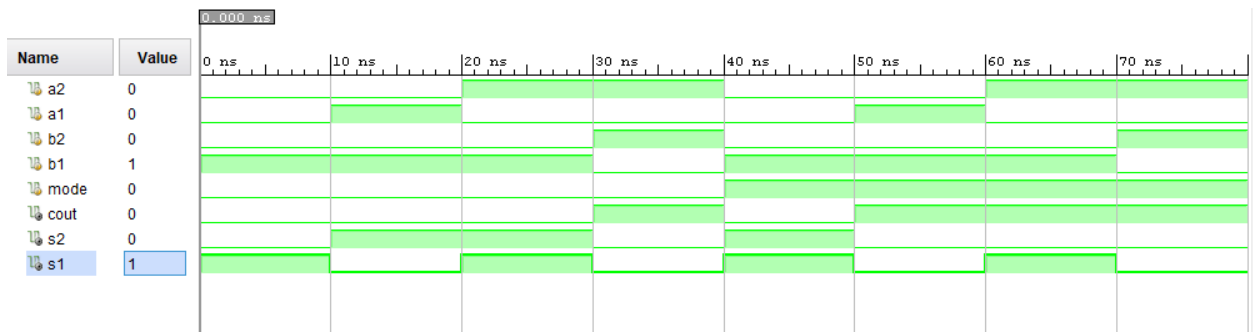


Figure 1.1: Table and simulation waveform to reproduce

Current repository

DL202010_MayaMartin

Current branch

master

Push origin

Last fetched 28 minutes ago

1 +

Changes

History

No branches to compare

Files Uploaded

MMARTIN1999 committed 2 minutes ago

Initial commit

MMARTIN1999 committed 2 hours ago

Files Uploaded

MMARTIN1999 committed 75eeff9 24 changed files

Hide Whitespace

Lab 1 Git La...\2137_Lab1_Git_Latex.pdf

Lab 1 Git Latex\2137_lab1_report_aux

Lab 1 Git Latex\2137_lab1_report_log

Lab 1 Git Latex\2137_lab1_report_out

Lab 1 Git Latex\2137_lab1_report_pdf

Lab 1 G...\2137_lab1_report_synctex.gz

Lab 1 Git Latex\2137_lab1_report_tex

Lab 1 Git L...\lab1_example_code (2).sv

Lab 1 Git Latex\lab1_example_code.sv

Lab 1...\lab1_example_screenshot.PNG

Support Docs\2137_lab1_report_aux

Support Docs\2137_lab1_report_log

Support Docs\2137_lab1_report_out

Support Docs\2137_lab1_report_pdf

Suppor...\2137_lab1_report_synctex.gz

Support Docs\2137_lab1_report_tex

Support D...\2137_report_template.tex

This binary file has changed.

Open file in external program.

Code

Listing 1: File-included Verilog code example

```

module example
  #(parameter BITS=4)
  (
    input [BITS-1:0] in0, in1,
    input sel,
    output [BITS-1:0] out
  );

  // Choose in1 or in0
  out = sel ? in1: in0;
endmodule

```