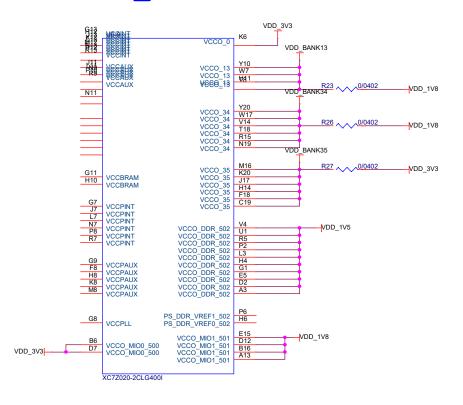




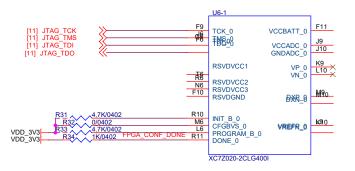
FPGA_POWER



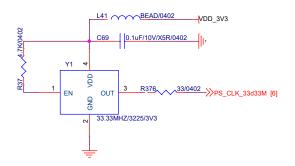
	www.arter	niskaria.i	r			
Title	SDR P201Pro-FPGA_I	POWER				
Size	Document Number AK201/203 Pro - A	rtemis Karia	SDR		1.0	Rev
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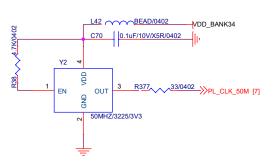
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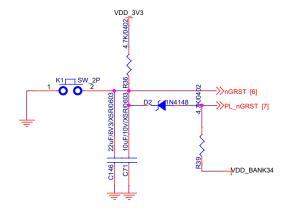


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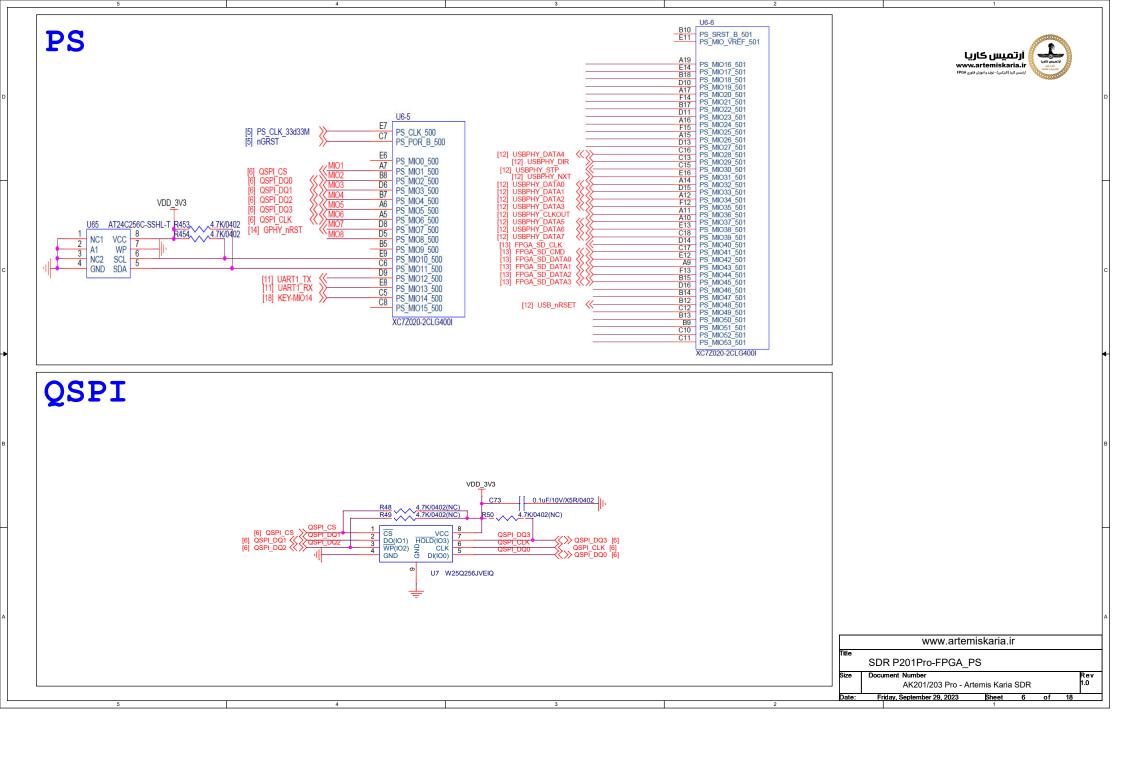




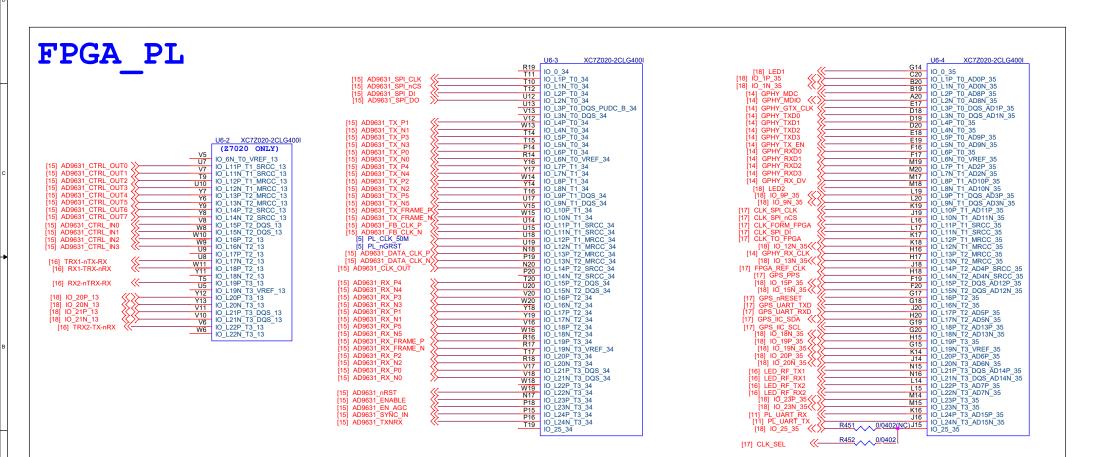
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Size	Document Number AK201/203 Pro - Artemis Karia SDR					Rev 1.0
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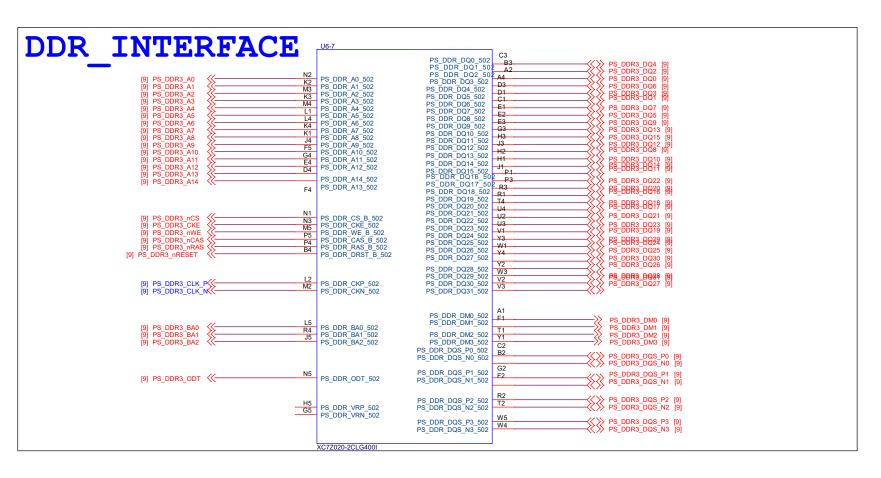




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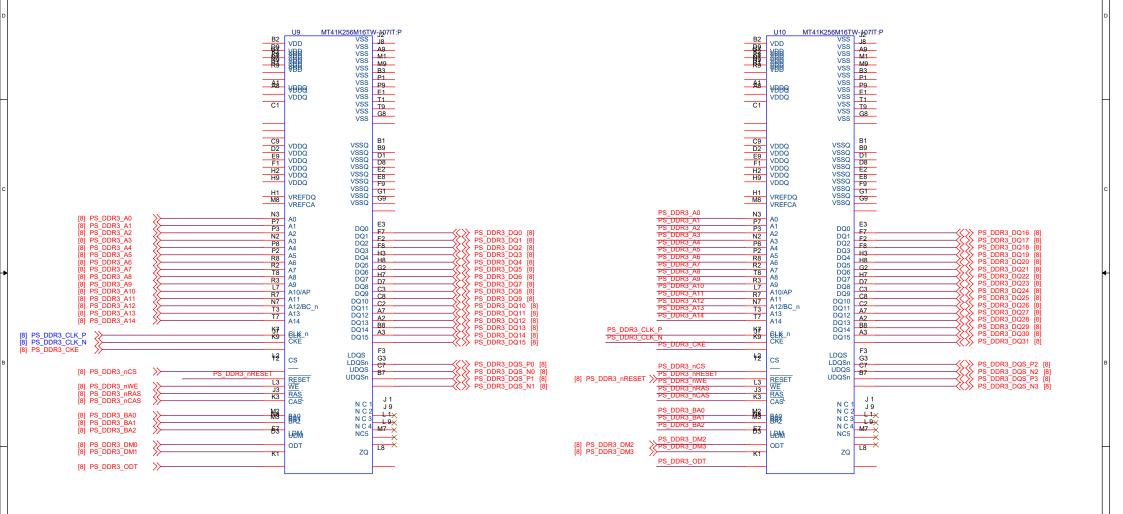




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Size	Document Number AK201/203 Pro - Ar	temis Karia	SDR			Rev 1.0
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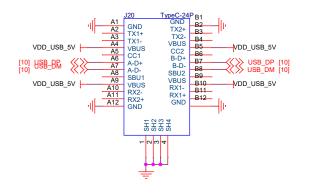
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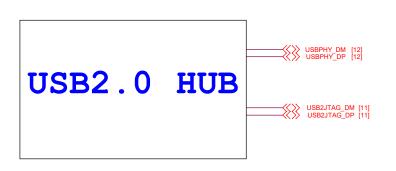




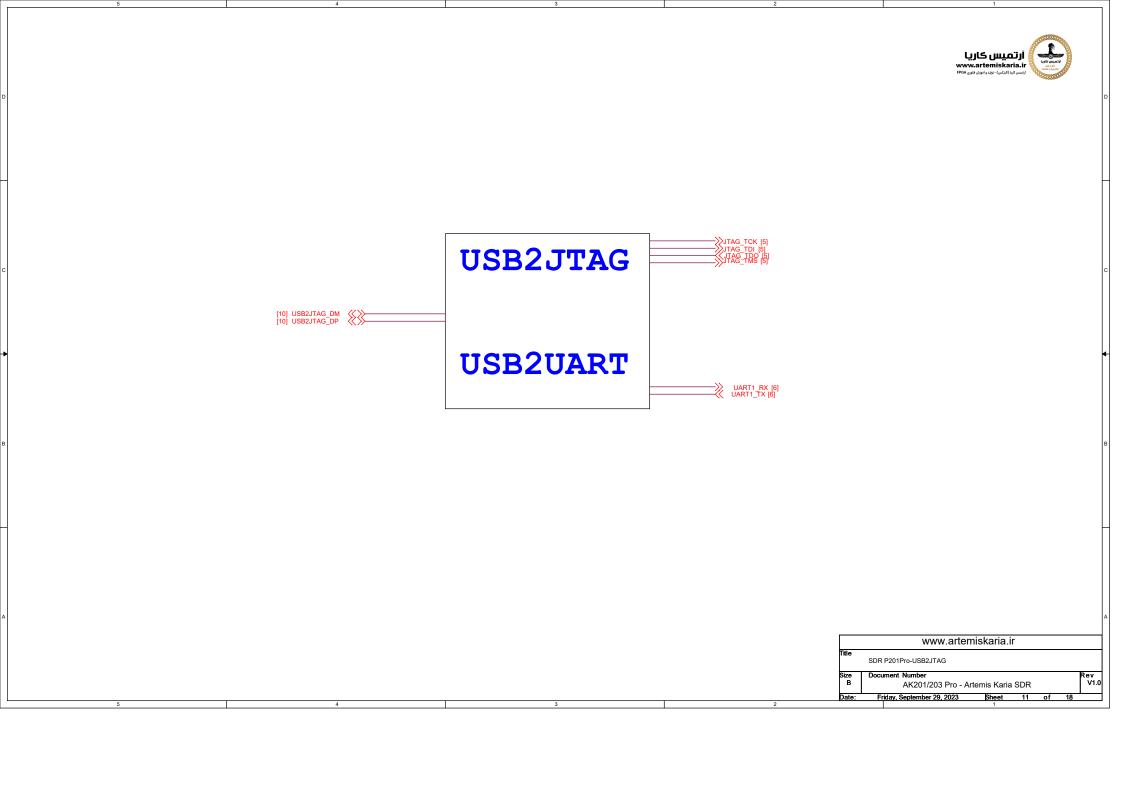
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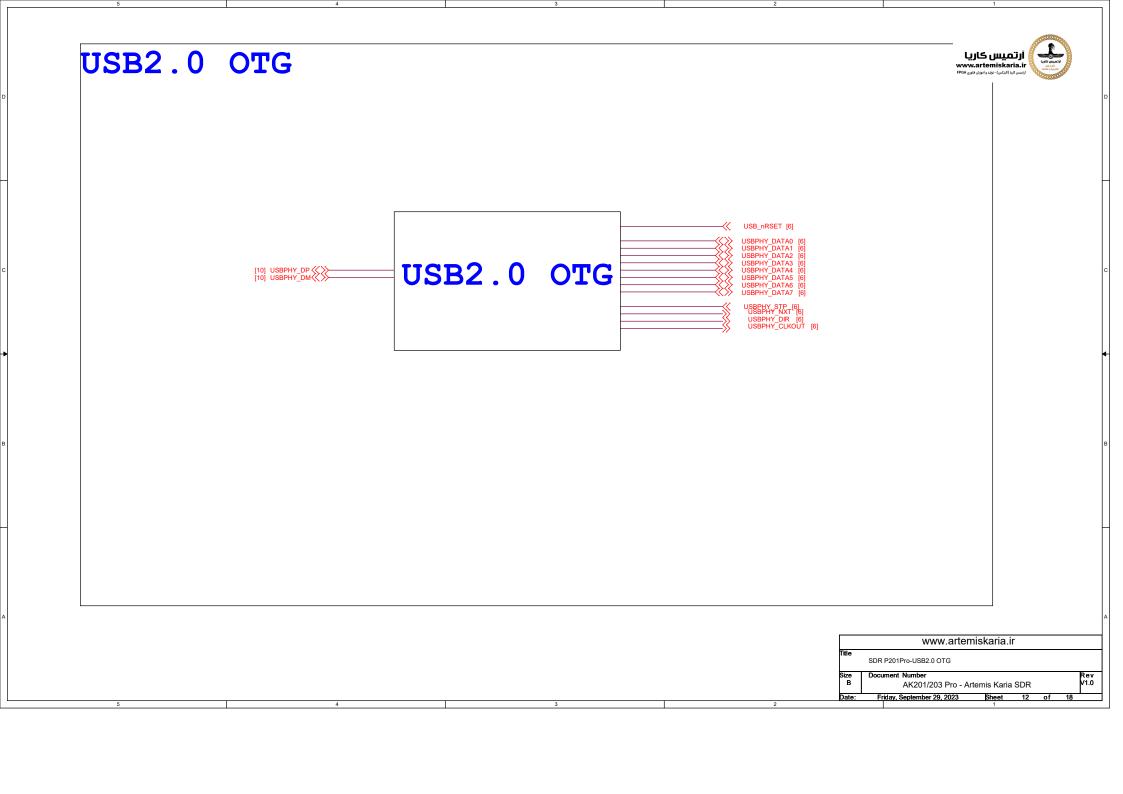




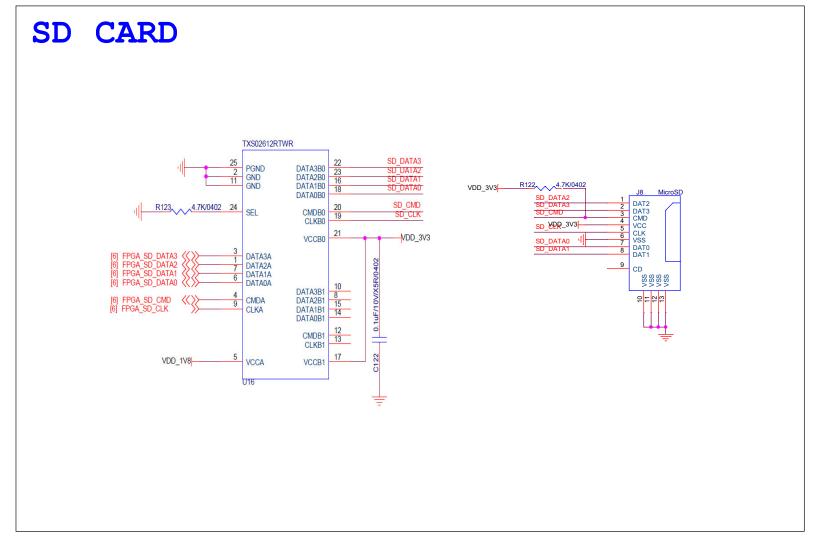


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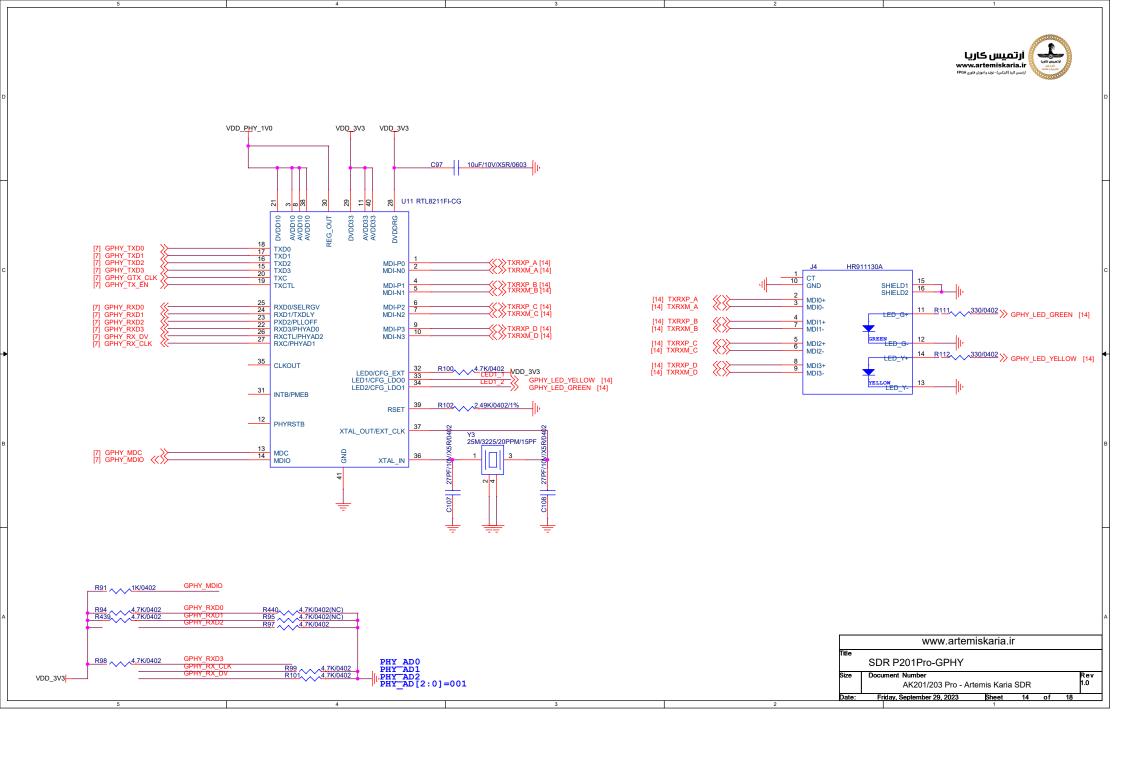








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Title	SDR P201Pro-SD CARD					
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أرتميس كاريا E B 55 2 2 2 ಬ B3 B10 F12 www.artemiskaria.ir 88 **₹** U13 AD9361BBCZ بس کاریا (الینکس) - تولید و آموزش فتاوری FPGA VDDA1P3_RX_TX 1P3_TX_LO_BUFFER E12 P0_D0/TX_D0_N4
E11 P0_D1/TX_D0_P&
D10 P0_D2/TX_D1_P&
E10 P0_D3/TX_D1_P&
P0_D4/TX_D2_N_I VDDA1P3_RX_VCO_LDO VDDA1P3_TX_SYNTH (7) AD9631_TX_N0 (7) AD9631_TX_P0 (7) AD9631_TX_N1 (7) AD9631_TX_N2 (7) AD9631_TX_N2 (7) AD9631_TX_P2 (7) AD9631_TX_P3 (7) AD9631_TX_P3 (7) AD9631_TX_N4 (7) AD9631_TX_N4 (7) AD9631_TX_N5 AD9631_TX1A_P [16] AD9631_TX1A_N [16] VDDA1P3_RX_SYN TX1A_N VDDA1P3_F TX1B_P M10 E10 PO_D3/TX_D1_P2 D9 PO_D4/TX_D2_N_0 E9 PO_D5/TX_D2_P2 D8 PO_D6/TX_D3_N D7 PO_D8/TX_D3_N D7 PO_D8/TX_D4_N PO_D1/TX_D5_N PO_D1/TX_D5_P AD9631_RX1A_P [16] AD9631_RX1A_N [16] M2 RX1A_N RX1B P J1 [7] AD9631_FB_CLK_P [7] AD9631_FB_CLK_N F10 G10 FB_CLK_P FB_CLK_N RX1C_P K1 RX1C_N L1 G9 H9 TX_FRAME_P TX_FRAME_N [7] AD9631_TX_FRAME_P [7] AD9631_TX_FRAME_N K11 P1_D0/RX_D0_N TX2A_P AD9631_TX2A_P [16] AD9631_TX2A_N [16] [7] AD9631_RX_P0 [7] AD9631_RX_N1 K10 P1_D1/RX_D0_P J11 P1_D2/RX_D1_N TX2A_N A10 A9 J11 P1 D2/RX D1 N K9 P1 D3/RX D1 P J10 P1 D4/RX D2 P K8 P1 D5/RX D2 P J9 P1 D6/RX D3 N K7 P1 D7/RX D3 P TY2R P AD9631 RX P1 AD9631_RX_N2 AD9631_RX_N2 AD9631_RX_P2 TX2B_N AD9631_RX_N3 AD9631_RX_P3 RX2A F [7] AD9631_RX_P3 [7] AD9631_RX_N4 [7] AD9631_RX_P4 [7] AD9631_RX_N5 [7] AD9631_RX_P5 A1 AD9631_RX2A_N [16] RX2B_P F1 G11 DATA_CLK_P DATA_CLK_N C1 D1 [7] AD9631_DATA_CLK_P [7] AD9631_DATA_CLK_N RX2C P RX2C_N G8 RX_FRAME_P RX_FRAME_N [7] AD9631_RX_FRAME_P [7] AD9631_RX_FRAME_N TX_MON1 A5 [7] AD9631_CLK_OUT CLK_OUT [7] AD9631_SPI_CLK[7] AD9631_SPI_nCS[7] AD9631_SPI_DI J5 K6 J4 L6 SPI_ENB SPI_DI SPI_DO B3 [7] AD9631_SPI_DO AUXDAC1 C3 [7] AD9631_nRST RESETR ALIXDAC2 AUXADC L5 [7] AD9631_ENABLE >> ENABLE TEST/ENABLE [7] AD9631_EN_AGC >>-EN AGC RX_VCO_LDO_OUT G2
VDDA1P1_RX_VCO G3 [7] AD9631_SYNC_IN >>-SYNC_IN [7] AD9631_TXNRX >> **TXNRX** TX_EXT_LO_IN A12 RBIAS RX_EXT_LO_IN G1 AD9631_CTRL_OUT0 AD9631_CTRL_OUT1 AD9631_CTRL_OUT2 AD9631_CTRL_OUT3 CTRL_OUT0 CTRL_OUT1 CTRL_OUT2 CTRL_OUT3 AD9631 CTRL OUT3 AD9631 CTRL OUT4 AD9631 CTRL OUT5 AD9631 CTRL OUT6 AD9631 CTRL OUT7 F6 CTRL_OUT3 F5 CTRL_OUT5
CTRL_OUT6
CTRL_OUT7 A3 NC1 C5 C6 D6 CTRL_IN1 CTRL_IN2 CTRL_IN3 NC2 M3 [7] AD9631_CTRL_IN0 [7] AD9631_CTRL_IN1 [7] AD9631_CTRL_IN2 [7] AD9631_CTRL_IN3 B7 B6 B5 GPO_1 GPO_2 GPO_2 GPO_3 M11 XTALP M12 [17] AD9631_REF_CLK >> XTALN D12 F9 F11 G12 H7 K12 www.artemiskaria.ir SDR P201Pro-AD9361 Document Number Rev V1.0 AK201/203 Pro - Artemis Karia SDR

