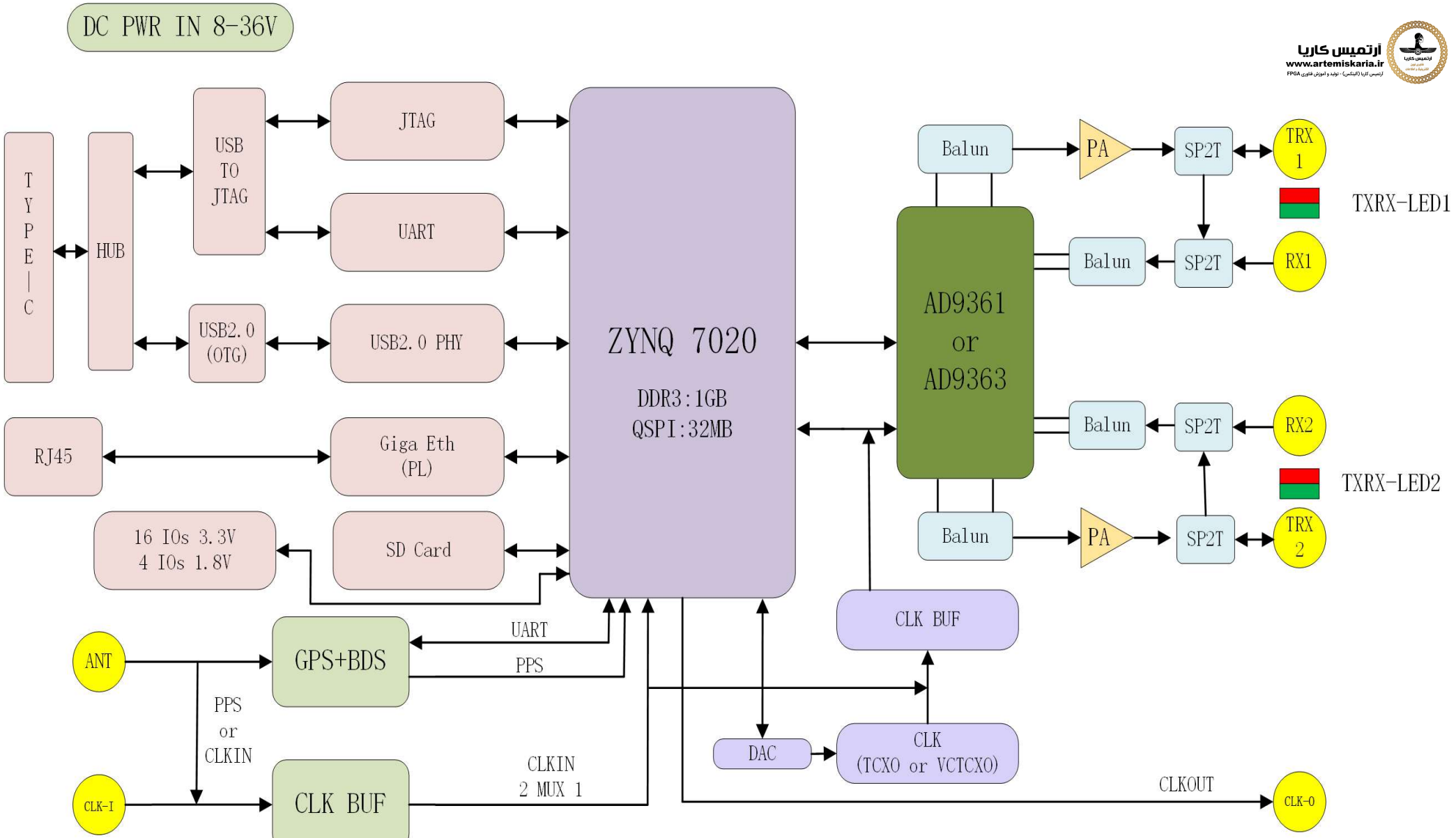

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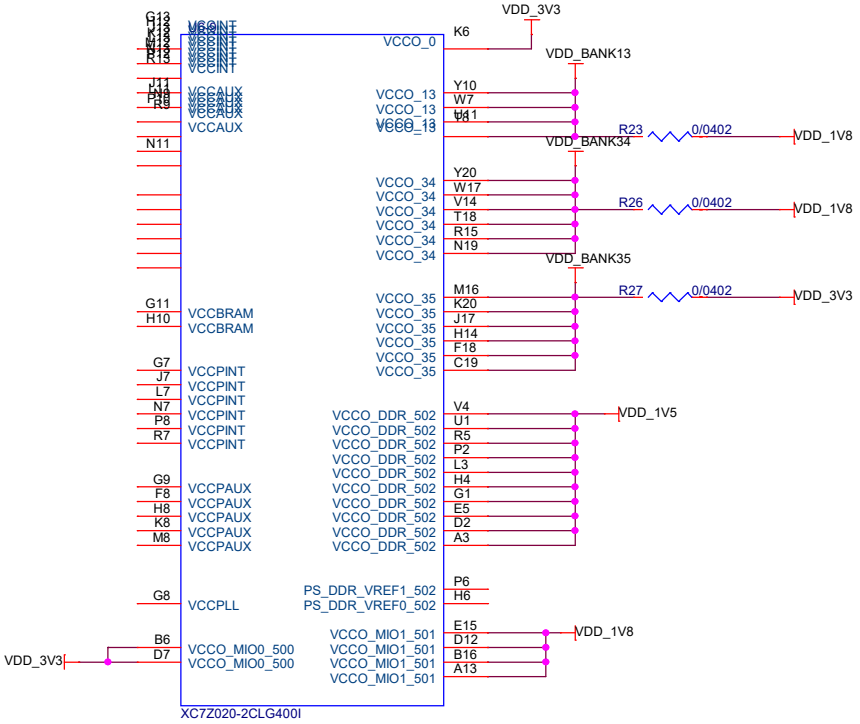
Title	SDR P201Pro-BLOCK	
Size	Document Number: AK201/203 Pro - Artemis Karia SDR	Rev 1.0
Date: Friday, September 29, 2023	Sheet 1 of 18	

FPGA_POWER



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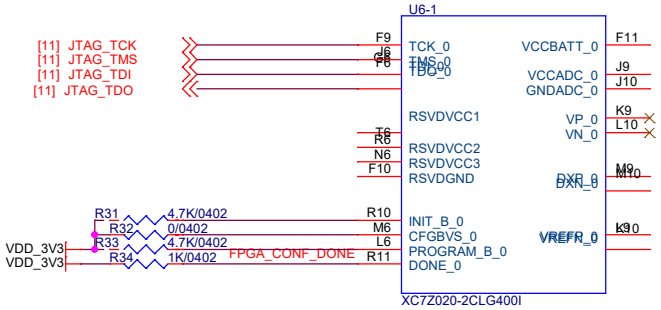
Title: SDR P201Pro-FPGA_POWER		
Size	Document Number: AK201/203 Pro - Artemis Karia SDR	Rev: 1.0
Date: Friday, September 29, 2023	Sheet 4 of 18	

ZYNQ_CONFIG



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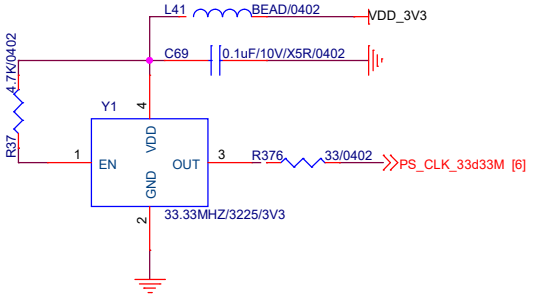

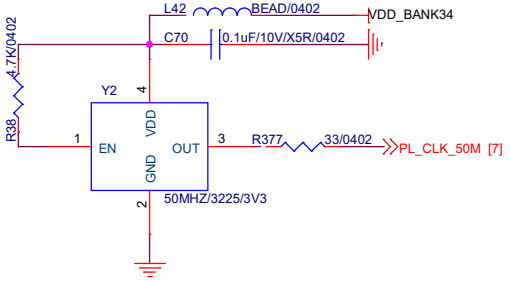

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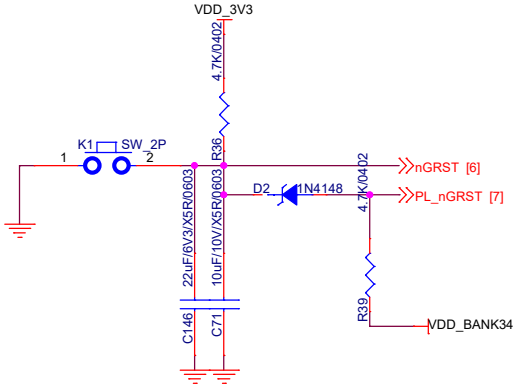

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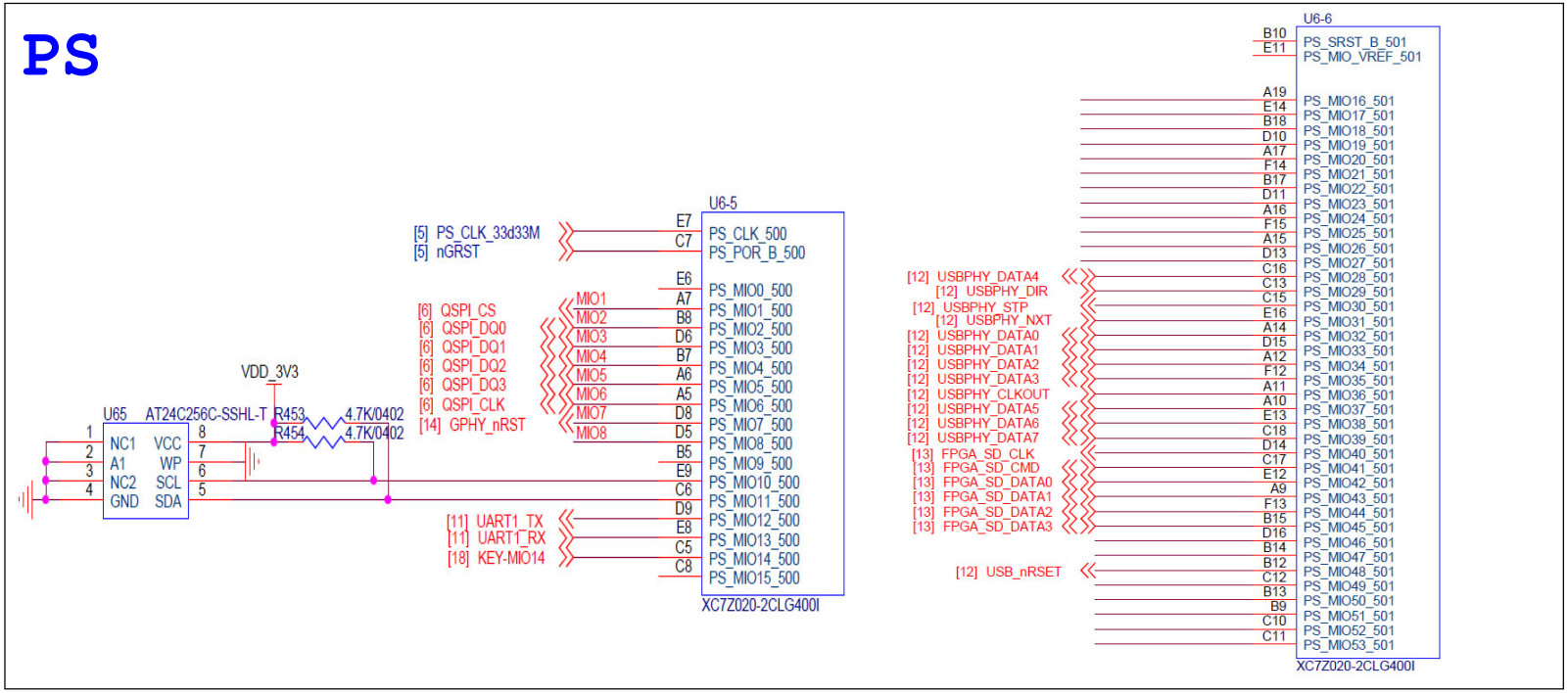
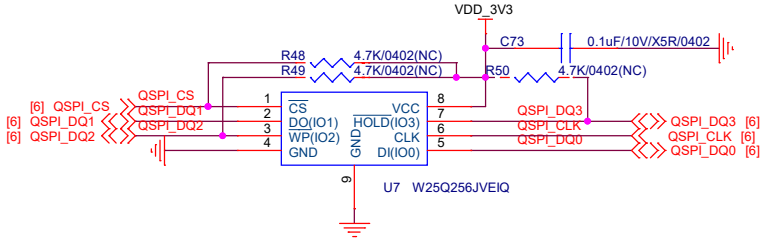
Title: SDR P201Pro-FPGA_CONFIG		
Size	Document Number: AK201/203 Pro - Artemis Karia SDR	Rev: 1.0
Date: Friday, September 29, 2023	Sheet 5 of 18	

PS



QSPI



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Title	SDR P201Pro-FPGA_PS	
Size	Document Number: AK201/203 Pro - Artemis Karia SDR	Rev 1.0
Date: Friday, September 29, 2023	Sheet 6 of 18	

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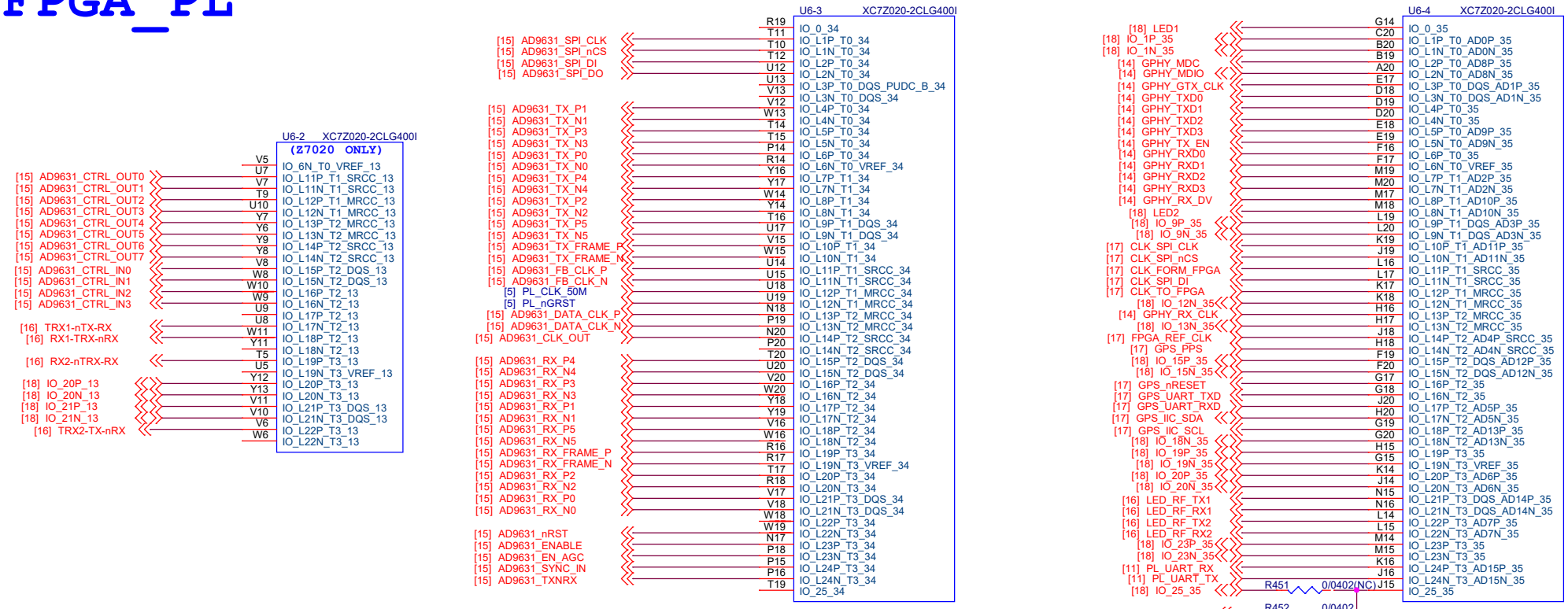
FPGA_PL



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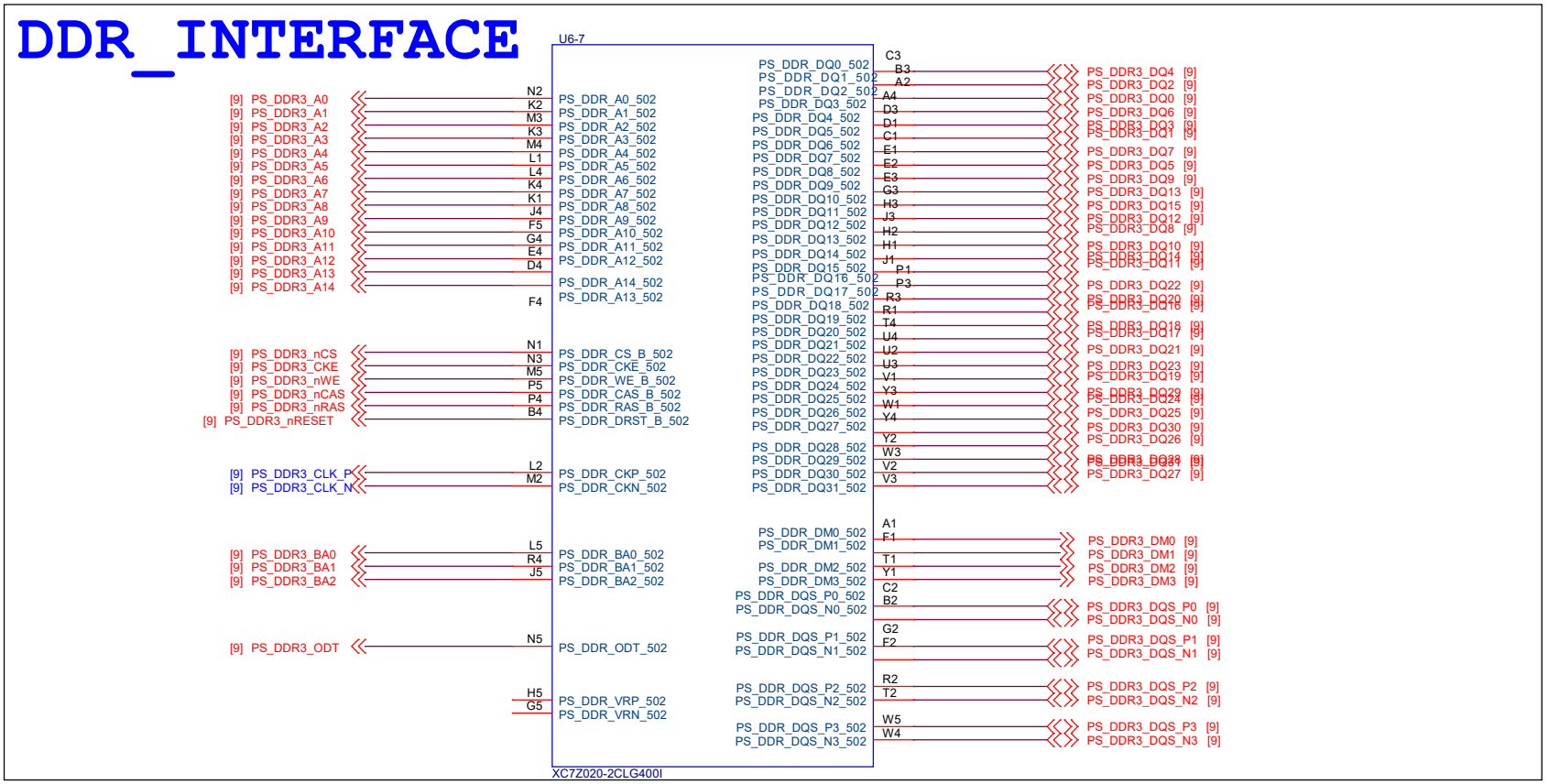
Title: SDR P201Pro-FPGA_PL		
Size	Document Number: AK201/203 Pro - Artemis Karia SDR	Rev: 1.0
Date: Friday, September 29, 2023	Sheet 7 of 18	

آرتمیس کاریا
www.artemiskaria.ir
آرتمیس کاریا (البتکس) - تولید و آموزش فناوری FPGA

DDR_INTERFACE



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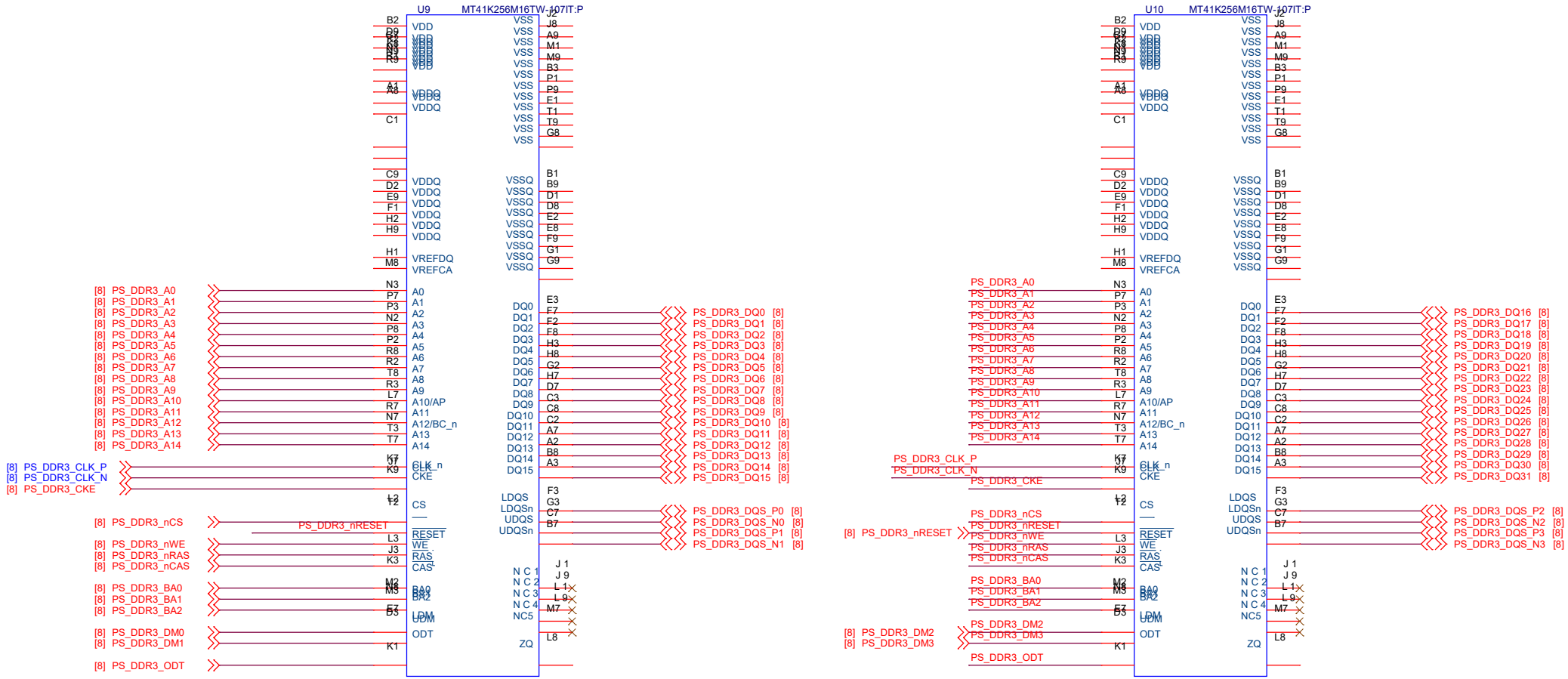
Title: SDR P201Pro-DDR INTERFACE		
Size	Document Number: AK201/203 Pro - Artemis Karia SDR	Rev: 1.0
Date: Friday, September 29, 2023	Sheet 8 of 18	

PS_DDR3



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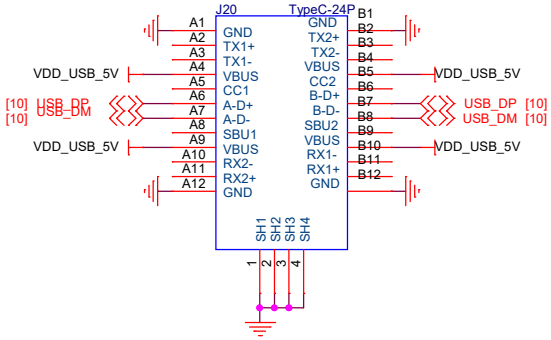
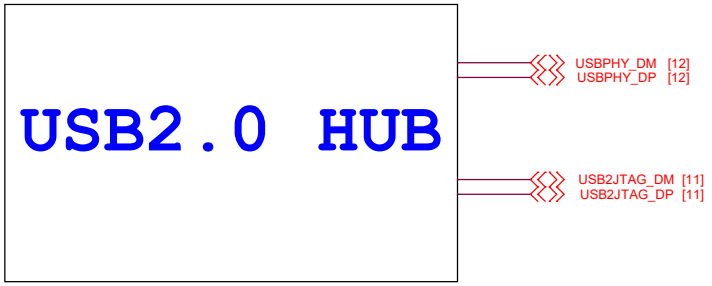
Title	SDR P201Pro-DDR	
Size	Document Number: AK201/203 Pro - Artemis Karia SDR	Rev 1.0
Date: Friday, September 29, 2023	Sheet 9 of 18	





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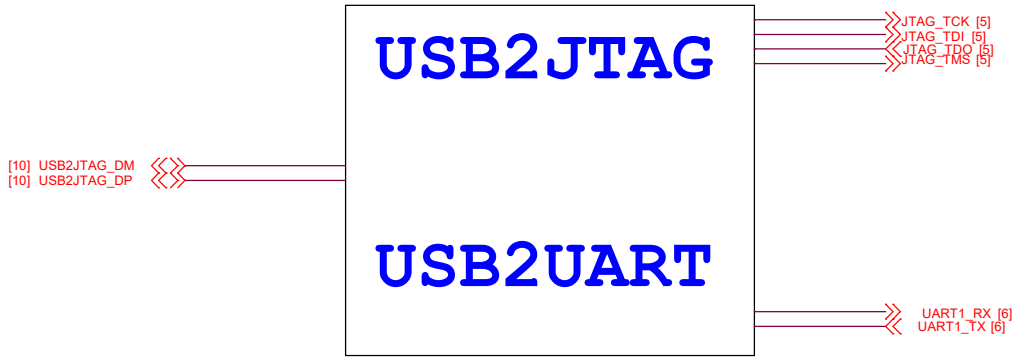
Title	SDR P201Pro-USB HUB	
Size B	Document Number AK201/203 Pro - Artemis Karia SDR	Rev V1.0
Date: Friday, September 29, 2023	Sheet 10 of 18	



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Title: SDR P201Pro-USB2JTAG		
Size: B	Document Number: AK201/203 Pro - Artemis Karia SDR	Rev: V1.0
Date: Friday, September 29, 2023	Sheet 11 of 18	

USB2.0 OTG

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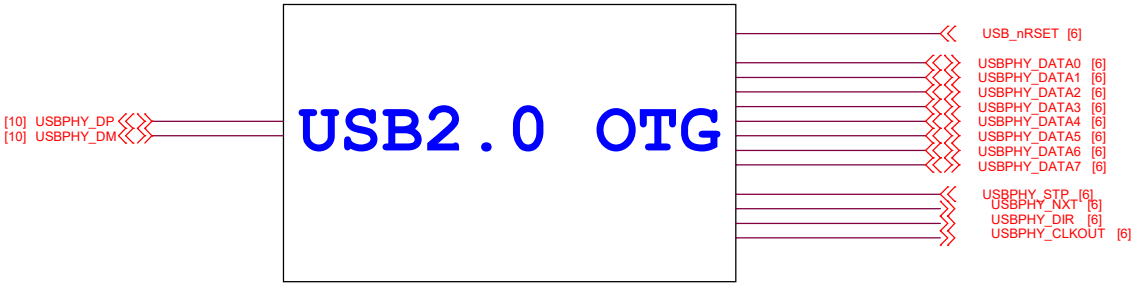

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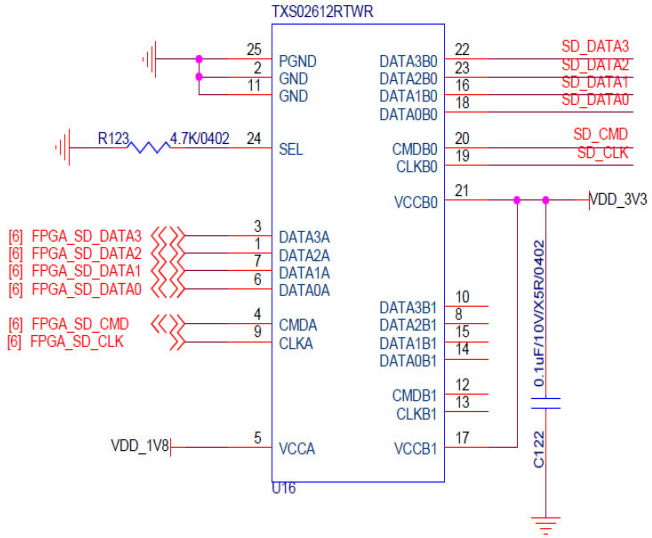
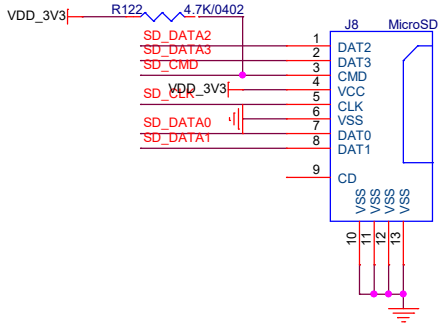
Title: SDR P201Pro-USB2.0 OTG		
Size: B	Document Number: AK201/203 Pro - Artemis Karia SDR	Rev: V1.0
Date: Friday, September 29, 2023	Sheet 12 of 18	

SD CARD





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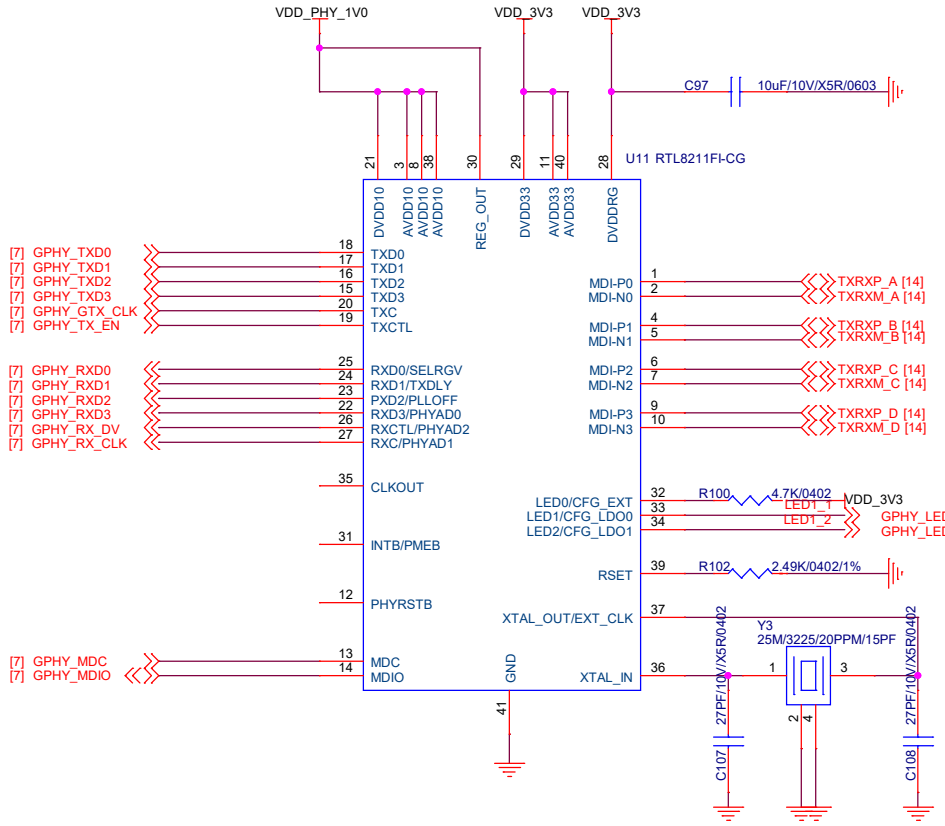
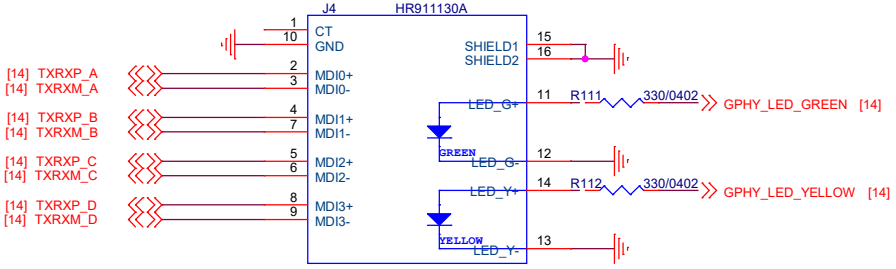
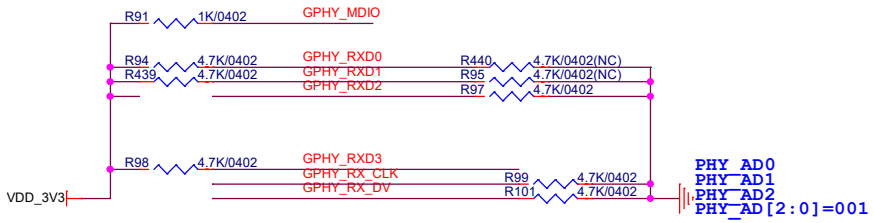
Title: SDR P201Pro-SD CARD			
Size: B	Document Number: AK201/203 Pro - Artemis Karia SDR		Rev: V1.0
Date: Friday, September 29, 2023	Sheet 13 of 18		







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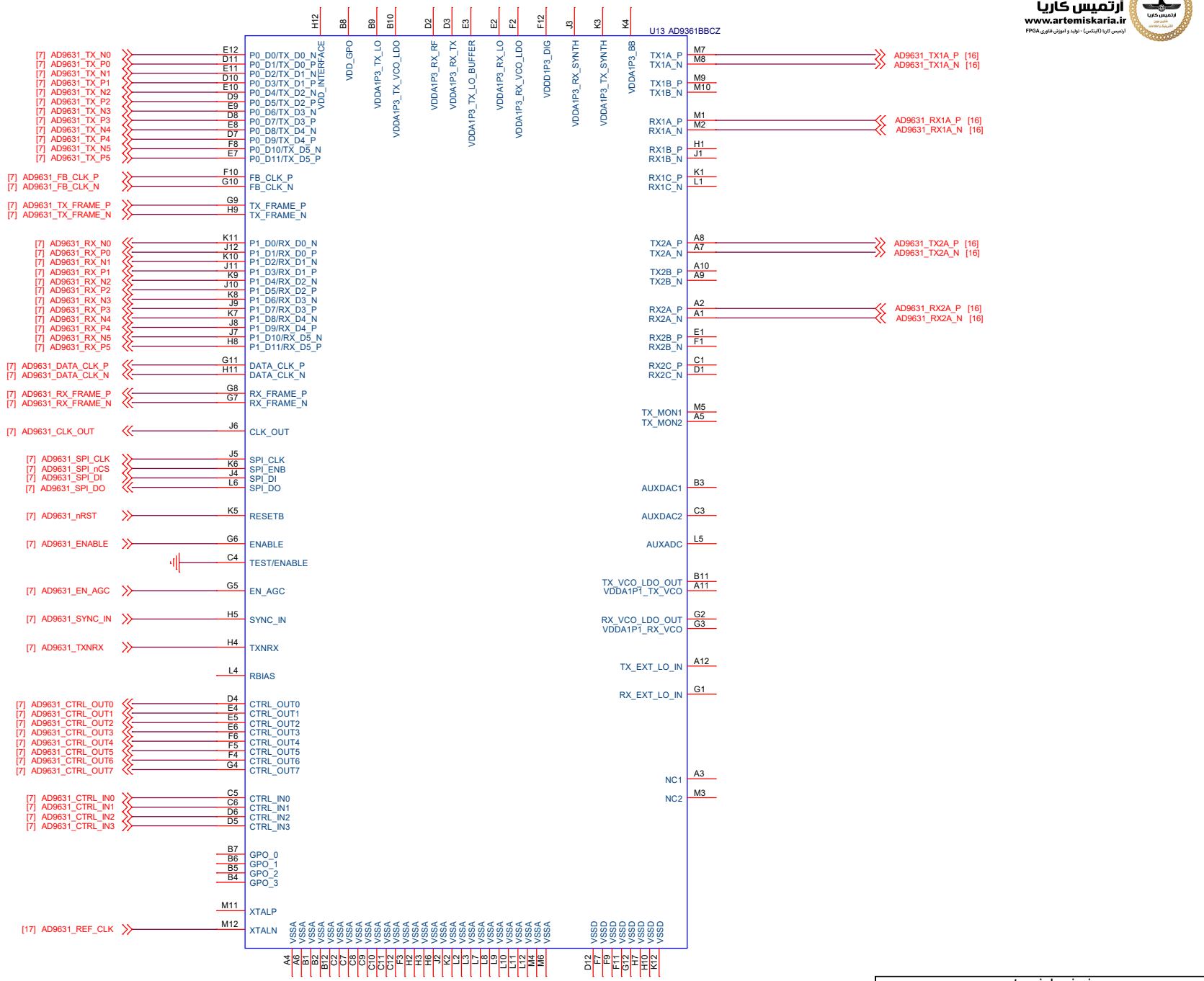
Title	SDR P201Pro-GPHY	
Size	Document Number: AK201/203 Pro - Artemis Karia SDR	Rev 1.0
Date: Friday, September 29, 2023	Sheet 14 of 18	



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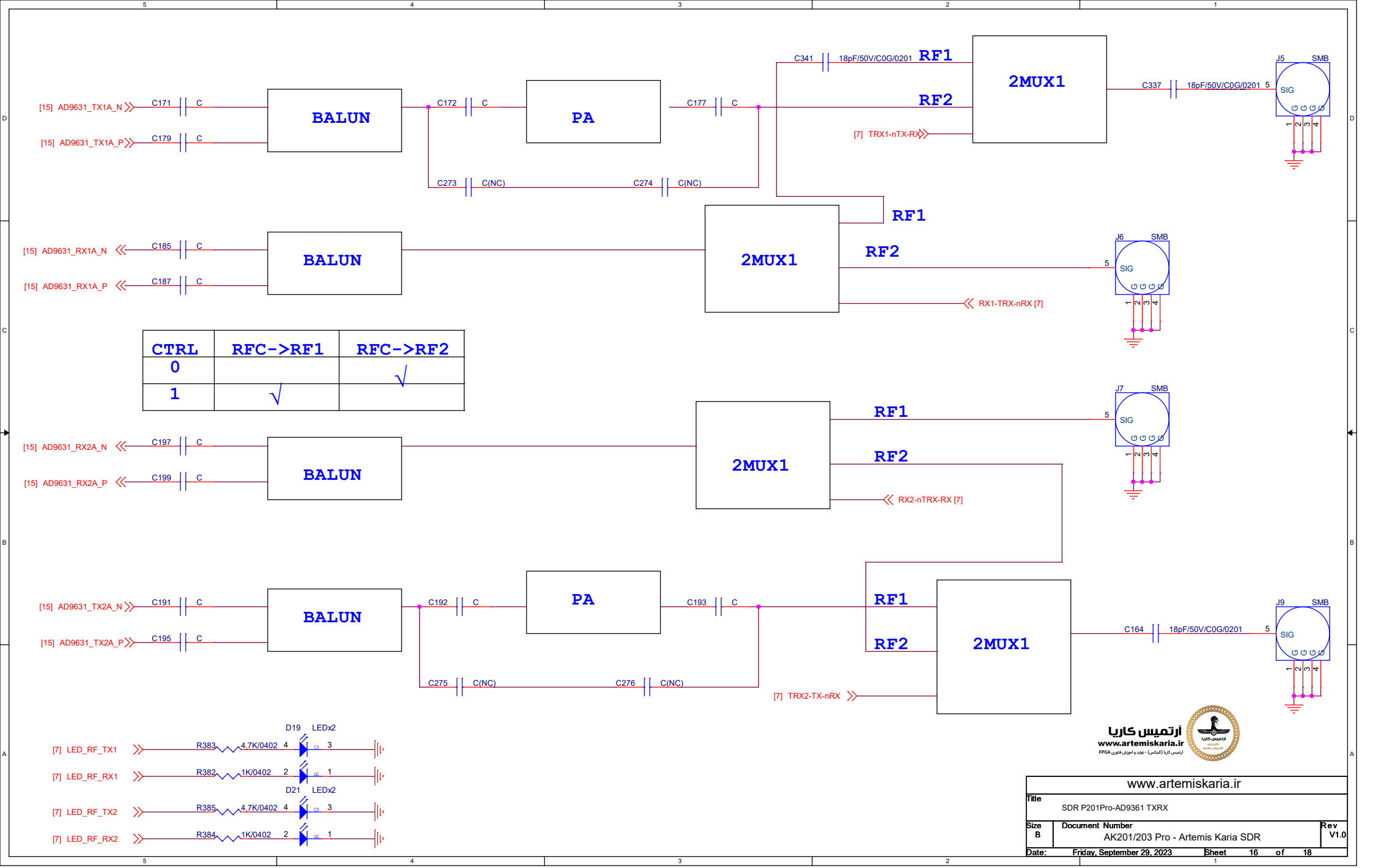
Title: SDR P201Pro-AD9361		
Size: Custom	Document Number: AK201/203 Pro - Artemis Karia SDR	Rev: V1.0
Date: Friday, September 29, 2023	Sheet 15 of 18	


[15] AD9631_TX1A_N
[15] AD9631_TX1A_P
C171 C
C179 C
BALUN
C172 C
C273 C(NC)
PA
C177 C
C274 C(NC)
C341 18pF/50V/C0G/0201
RF1
RF2
[7] TRX1-nTX-RX
2MUX1
C337 18pF/50V/C0G/0201
J5 SMB
SIG
[15] AD9631_RX1A_N
[15] AD9631_RX1A_P
C185 C
C187 C
BALUN
2MUX1
RF1
RF2
RX1-TRX-nRX [7]
J6 SMB
SIG
CTRL | RFC->RF1 | RFC->RF2
0 | | √
1 | √ |
[15] AD9631_RX2A_N
[15] AD9631_RX2A_P
C197 C
C199 C
BALUN
2MUX1
RF1
RF2
RX2-nTRX-RX [7]
J7 SMB
SIG
[15] AD9631_TX2A_N
[15] AD9631_TX2A_P
C191 C
C195 C
BALUN
C192 C
C275 C(NC)
PA
C193 C
C276 C(NC)
RF1
RF2
[7] TRX2-TX-nRX
2MUX1
C164 18pF/50V/C0G/0201
J9 SMB
SIG
[7] LED_RF_TX1
[7] LED_RF_RX1
[7] LED_RF_TX2
[7] LED_RF_RX2
R383 4.7K/0402
R382 1K/0402
R385 4.7K/0402
R384 1K/0402
D19 LEDx2
D21 LEDx2
ارتمیس کاریا
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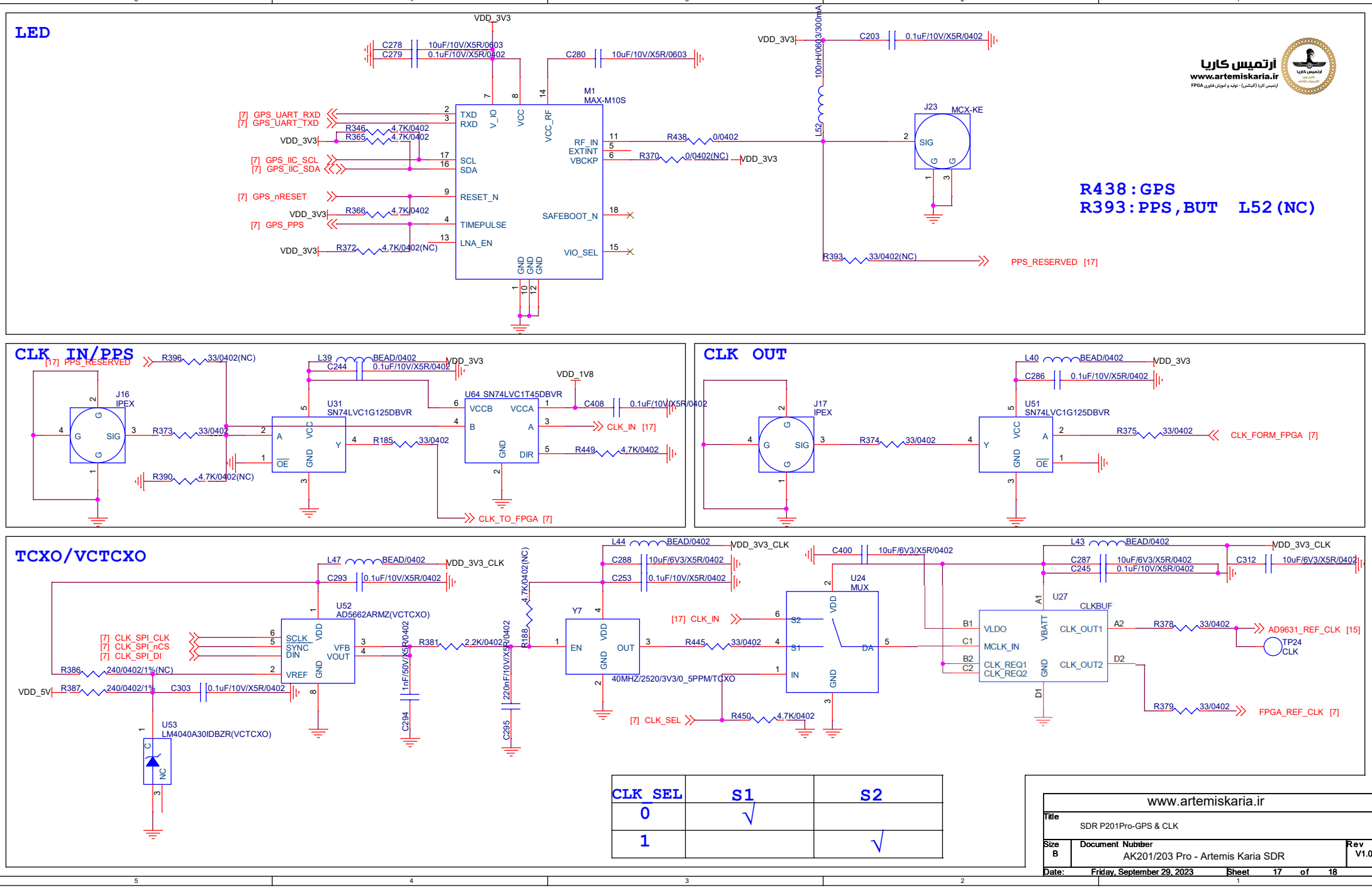
Title
SDR P201Pro-AD9361 TXRX
Size B
Document Number
AK201/203 Pro - Artemis Karia SDR
Rev V1.0
Date: Friday, September 29, 2023
Sheet 16 of 18



CLK_SEL	S1	S2
0	√	
1		√

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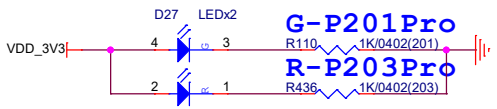
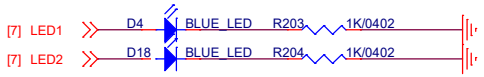
Title	SDR P201Pro-GPS & CLK	
Size B	Document Number AK201/203 Pro - Artemis Karia SDR	Rev V1.0
Date: Friday, September 29, 2023	Sheet 17 of 18	

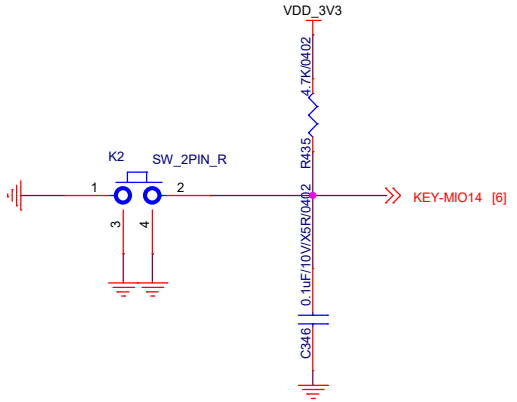
PWR LED



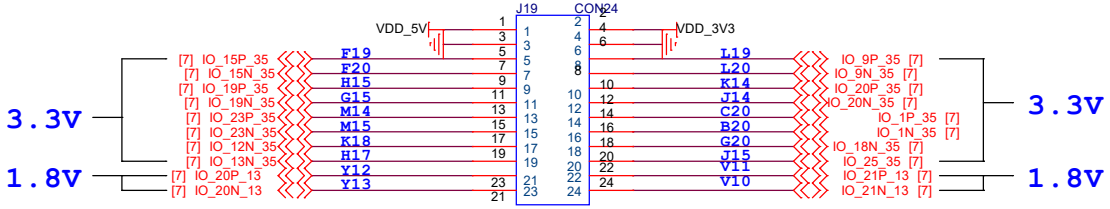
User LED



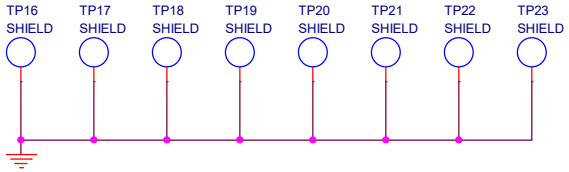
KEY



EXT CON



RF SHIELD



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Title	SDR P201Pro-LED & EXT CON	
Size	Document Number: AK201/203 Pro - Artemis Karia SDR	Rev 1.0
Date: Friday, September 29, 2023	Sheet 18 of 18	