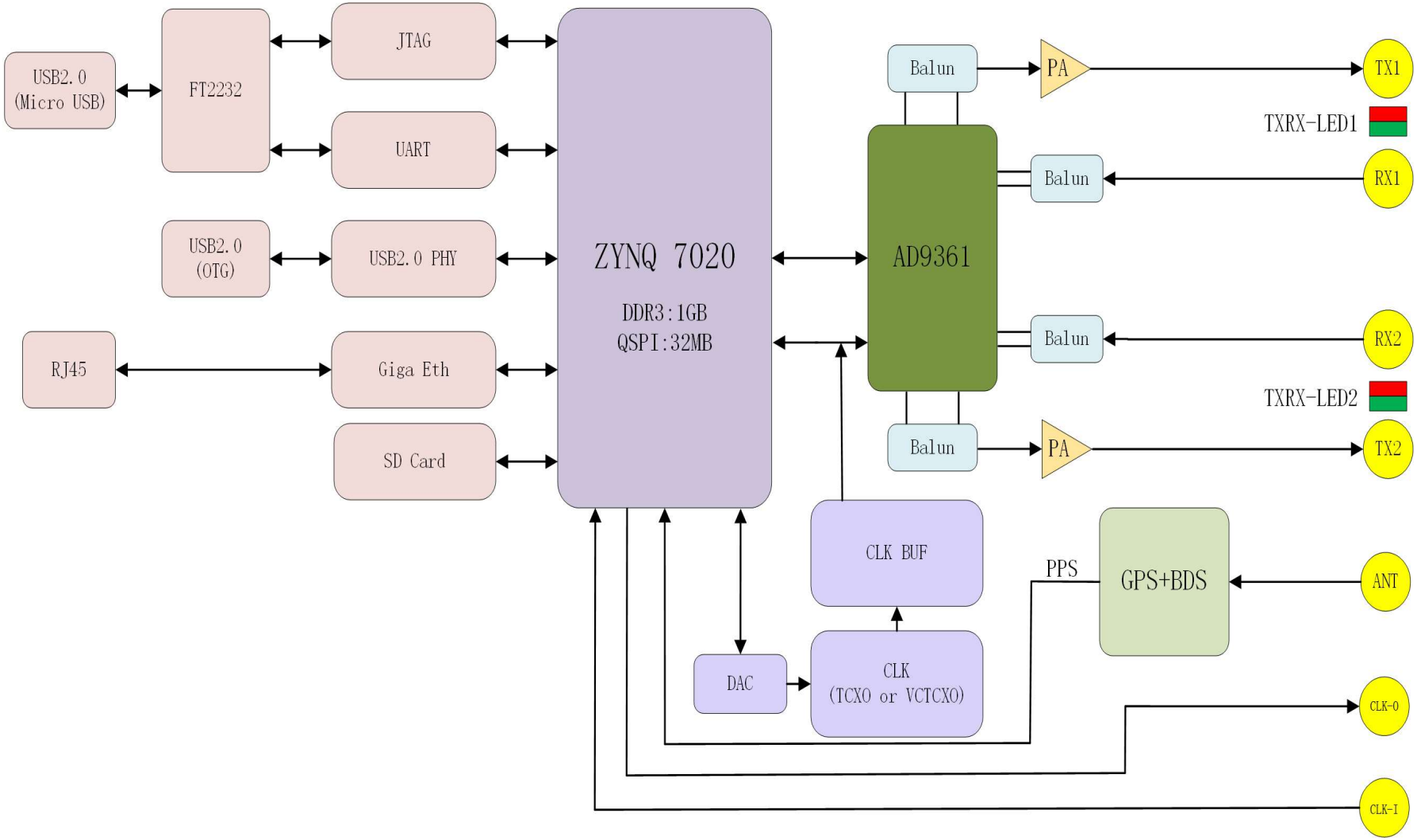

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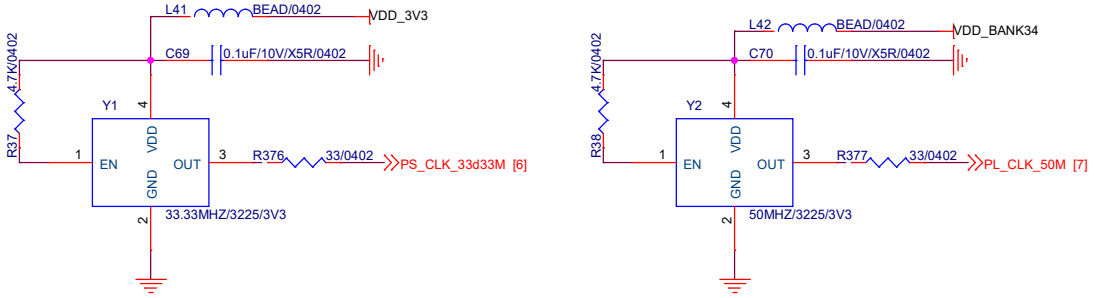
ZYNQ_CLK



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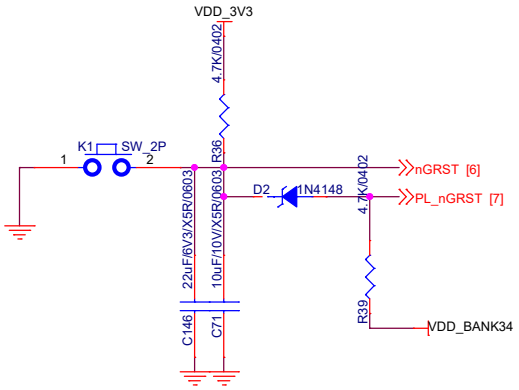

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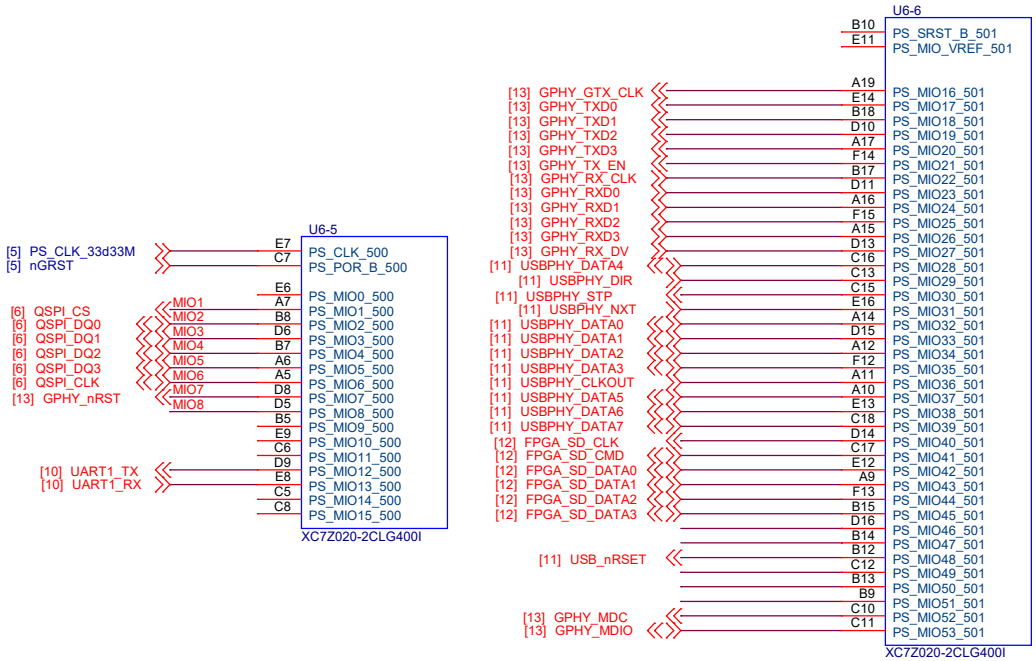
PS



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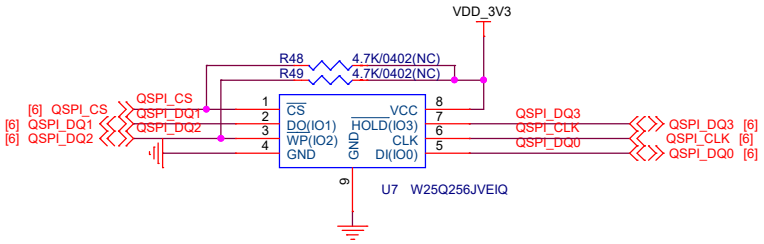

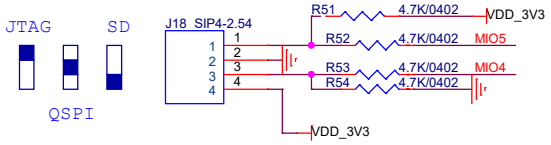

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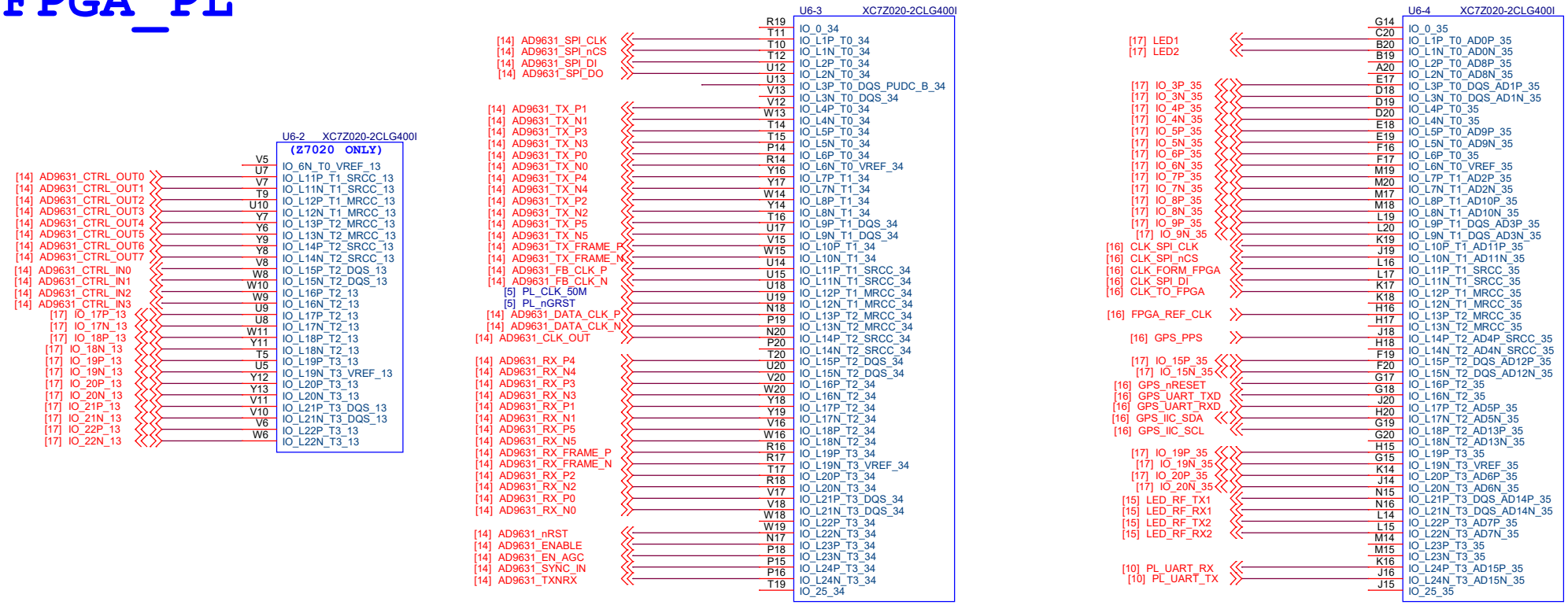
FPGA_PL



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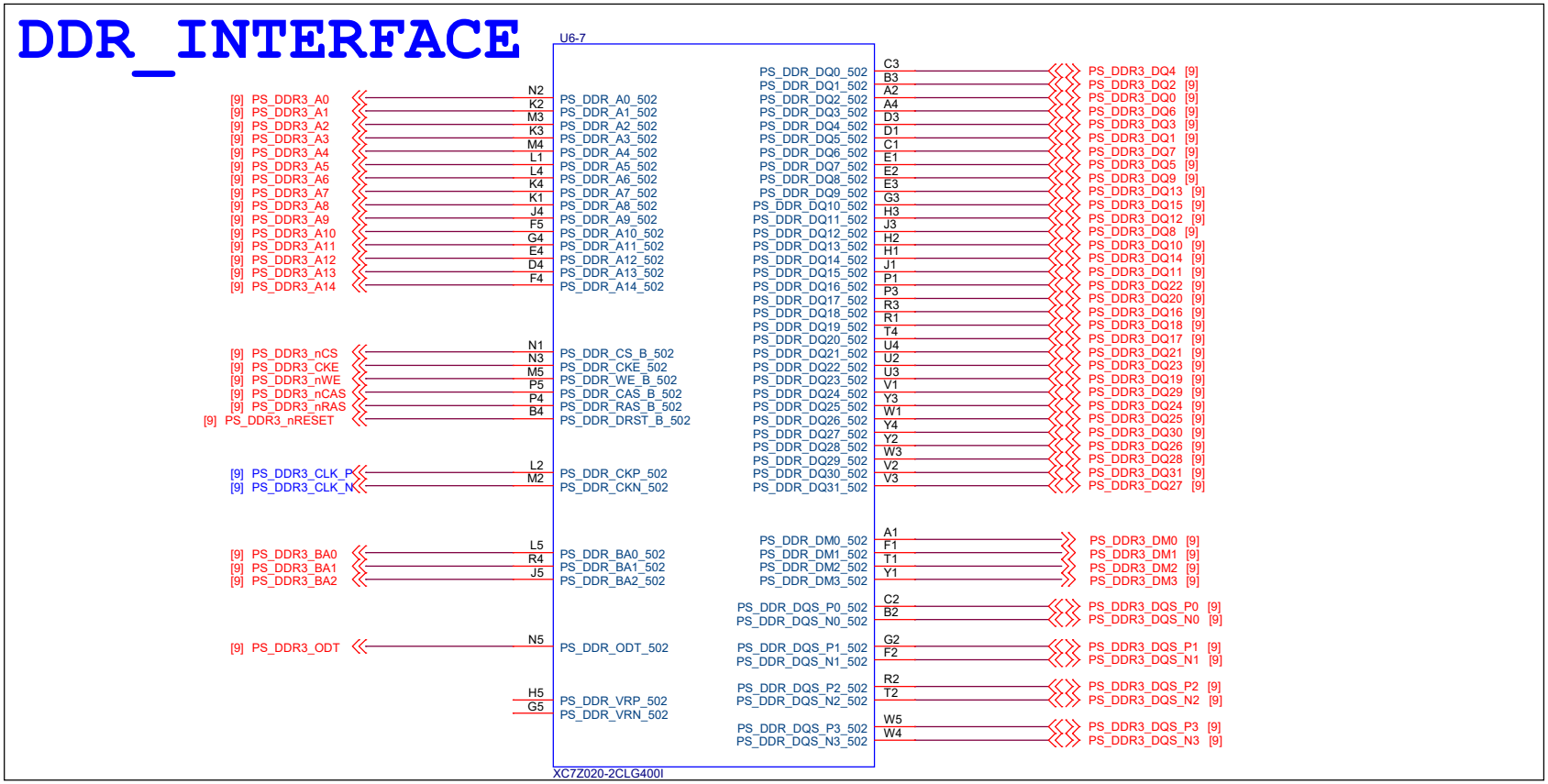
DDR_INTERFACE



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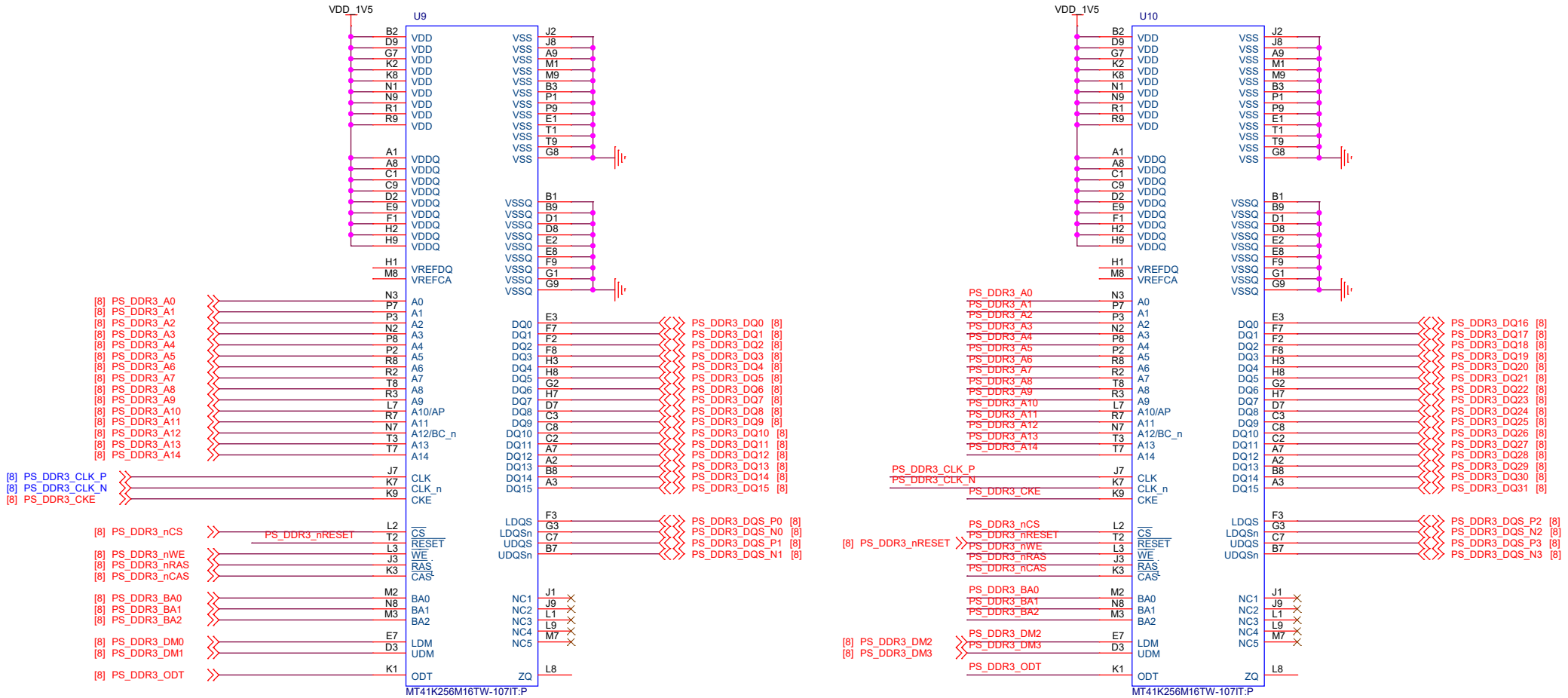
PS_DDR3



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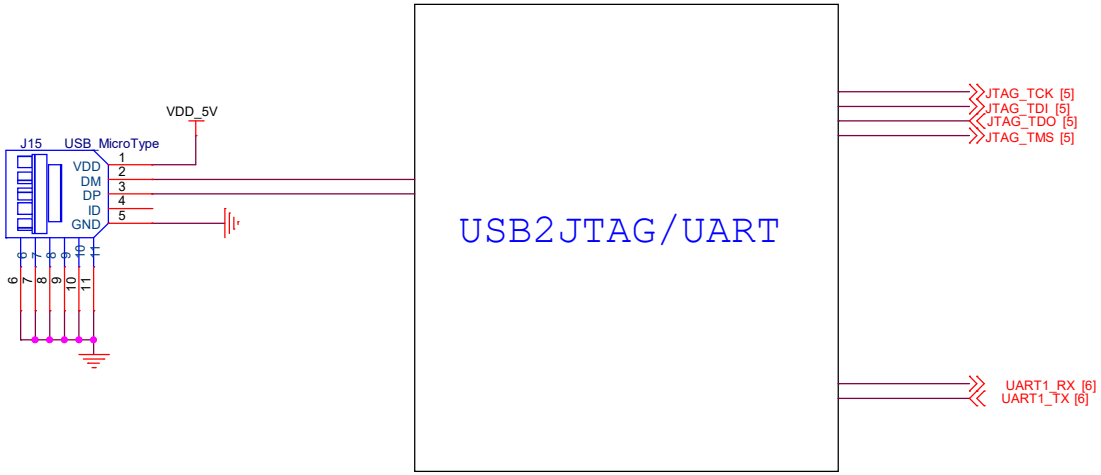

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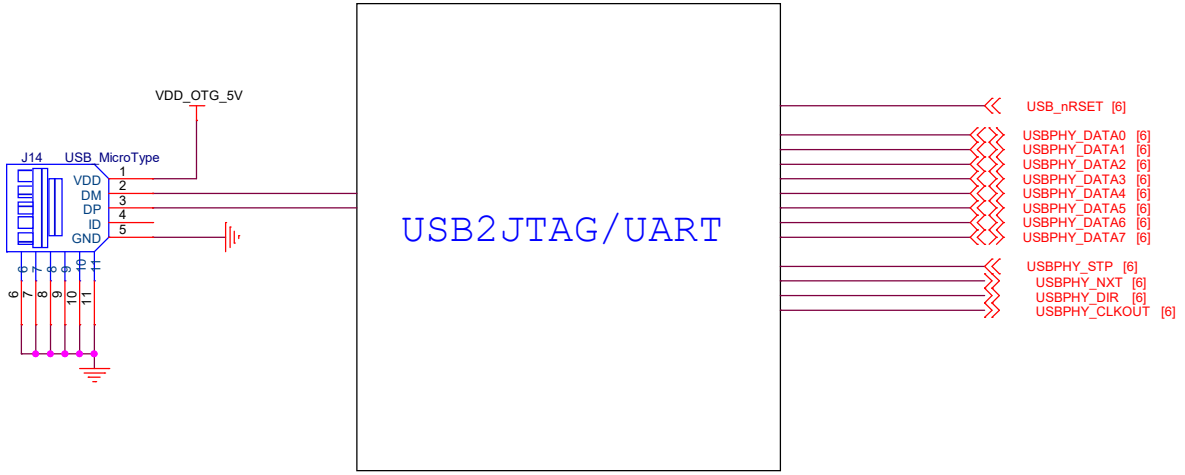
USB2.0 Host/Slave



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Title: SDR P20X-USB2.0 OTG		
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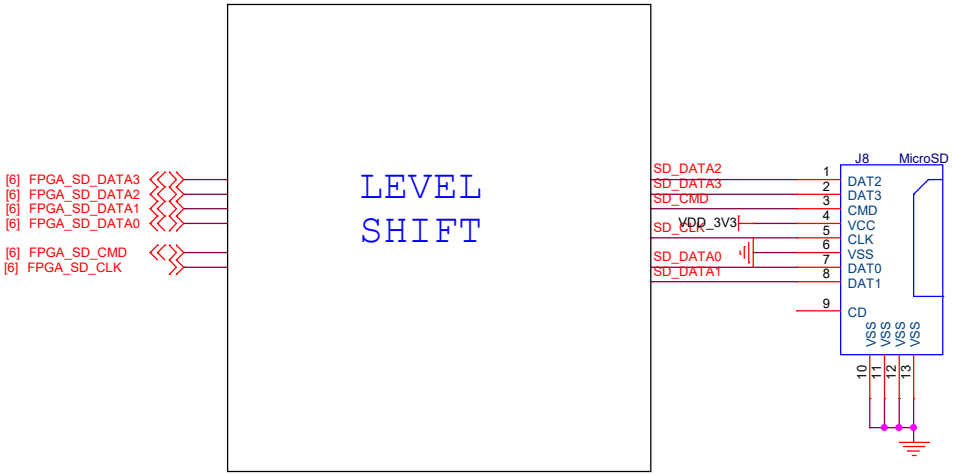
SD CARD



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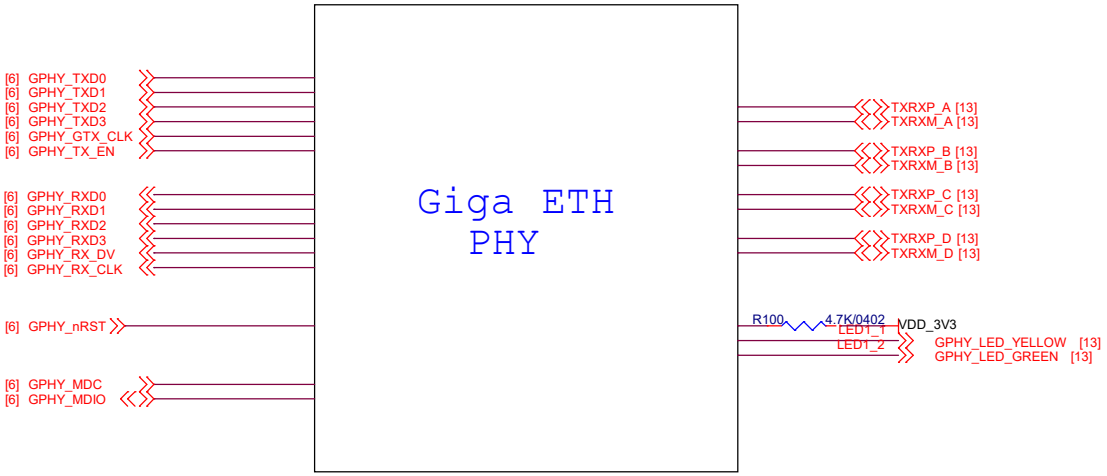

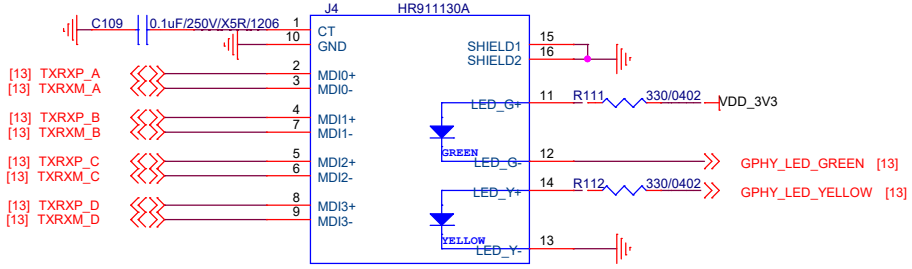

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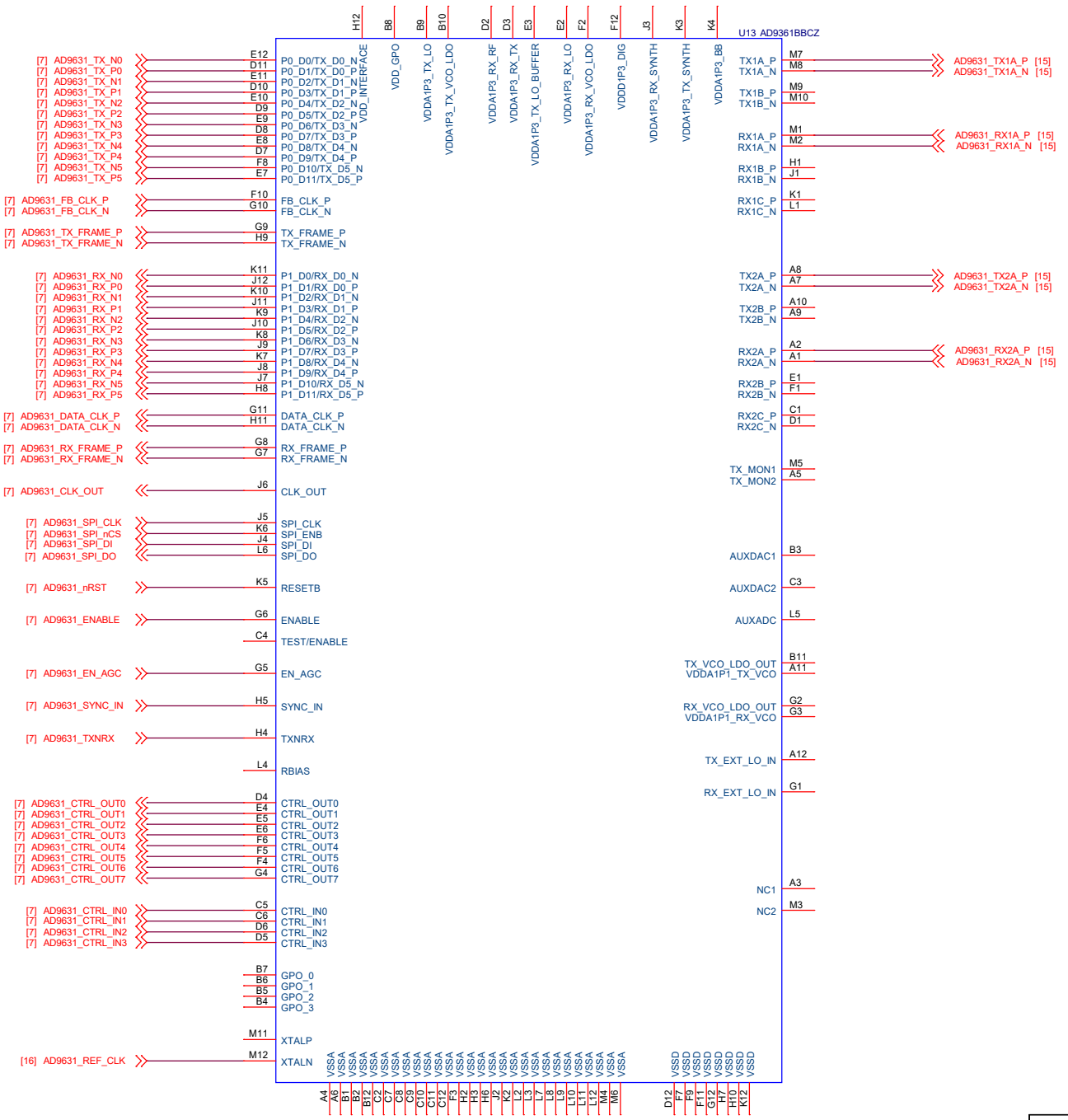

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Title	SDR P20X-AD9361	
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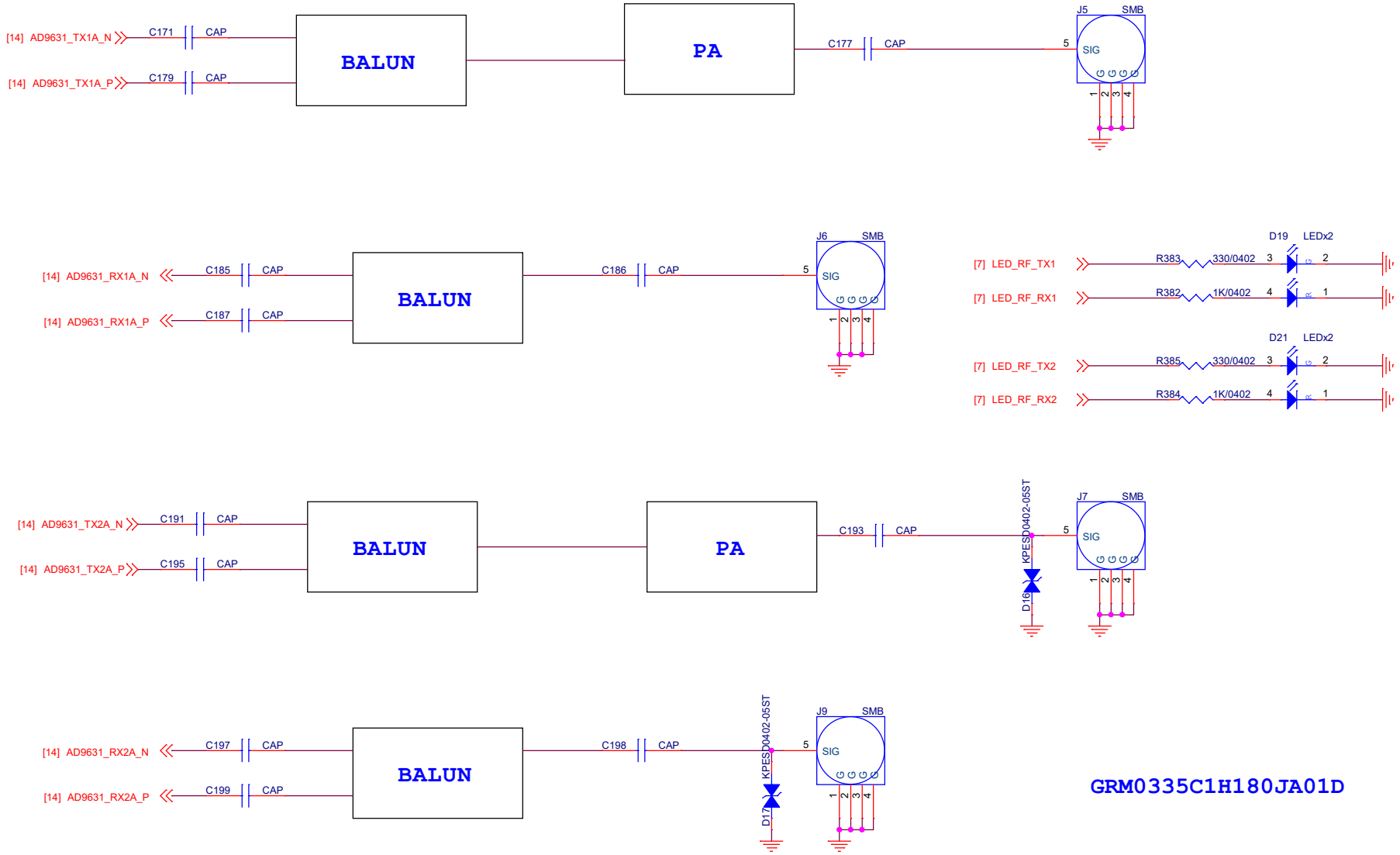

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Title: SDR P20X-AD9361 TXRX		
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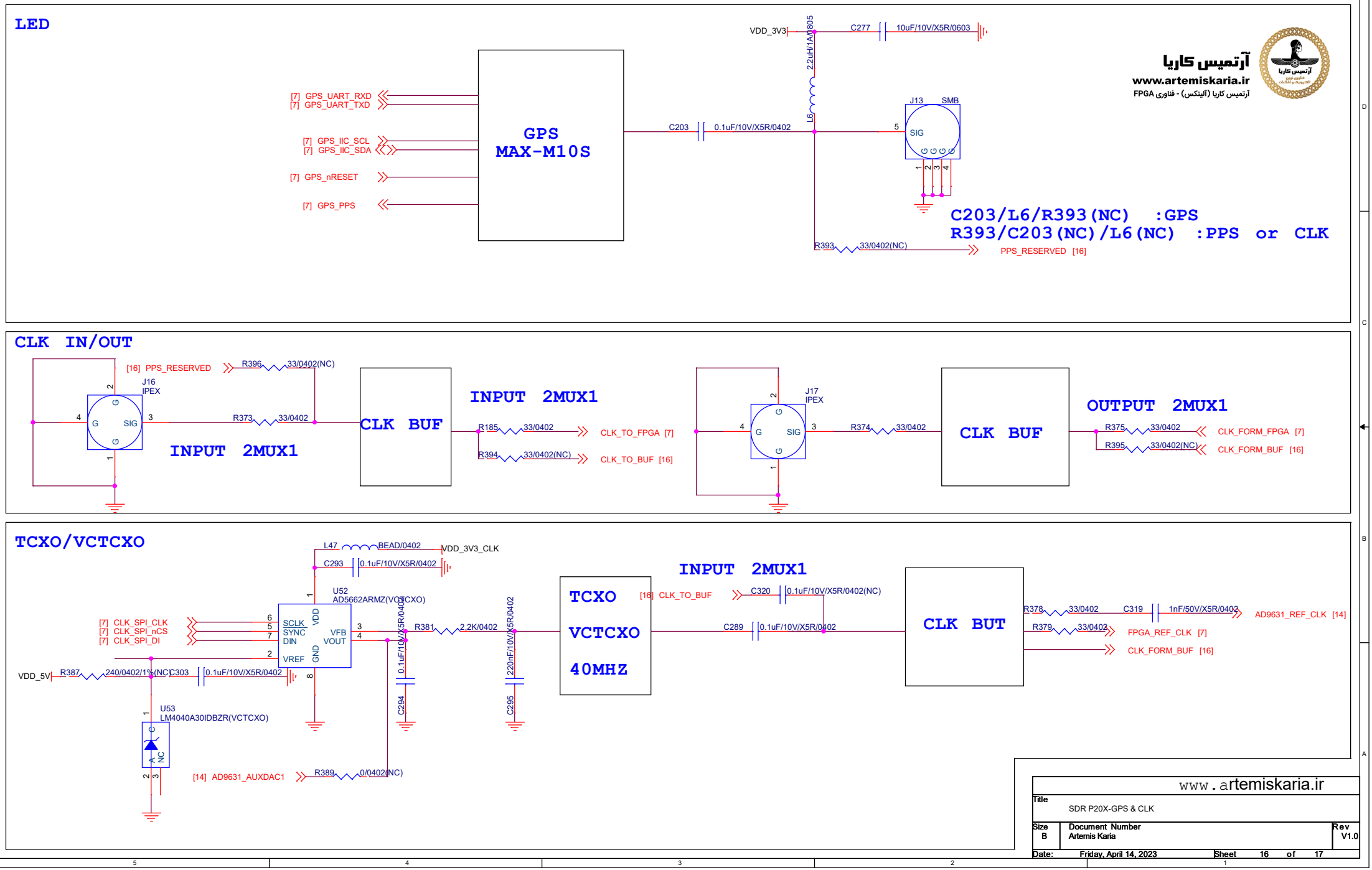
LED
GPS
MAX-M10S
[7] GPS_UART_RXD
[7] GPS_UART_TXD
[7] GPS_IIC_SCL
[7] GPS_IIC_SDA
[7] GPS_nRESET
[7] GPS_PPS
VDD_3V3
C277
10uF/10V/X5R/0603
2.2uH/1A/0805
L6
C203
0.1uF/10V/X5R/0402
J13
SMB
SIG
R393
33/0402(NC)
PPS_RESERVED [16]
C203/L6/R393(NC) :GPS
R393/C203(NC)/L6(NC) :PPS or CLK
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CLK IN/OUT
[16] PPS_RESERVED
R396
33/0402(NC)
J16
IPEX
R373
33/0402
INPUT 2MUX1
CLK BUF
R185
33/0402
CLK_TO_FPGA [7]
R394
33/0402(NC)
CLK_TO_BUF [16]
INPUT 2MUX1
J17
IPEX
R374
33/0402
CLK BUF
OUTPUT 2MUX1
R375
33/0402
CLK_FORM_FPGA [7]
R395
33/0402(NC)
CLK_FORM_BUF [16]
TCXO/VCTCXO
L47
BEAD/0402
VDD_3V3_CLK
C293
0.1uF/10V/X5R/0402
U52
AD5662ARMZ(VCTCXO)
[7] CLK_SPI_CLK
[7] CLK_SPI_nCS
[7] CLK_SPI_DI
SCLK
SYNC
DIN
VDD
VFB
VOUT
VREF
GND
R381
2.2K/0402
0.1uF/10V/X5R/0402
220nF/10V/X5R/0402
C294
C295
VDD_5V
R387
240/0402/1%(NC)
C303
0.1uF/10V/X5R/0402
U53
LM4040A30IDBZR(VCTCXO)
[14] AD9631_AUXDAC1
R389
0/0402(NC)
TCXO
VCTCXO
40MHZ
INPUT 2MUX1
[16] CLK_TO_BUF
C320
0.1uF/10V/X5R/0402(NC)
C289
0.1uF/10V/X5R/0402
CLK BUT
R378
33/0402
C319
1nF/50V/X5R/0402
AD9631_REF_CLK [14]
R379
33/0402
FPGA_REF_CLK [7]
CLK_FORM_BUF [16]
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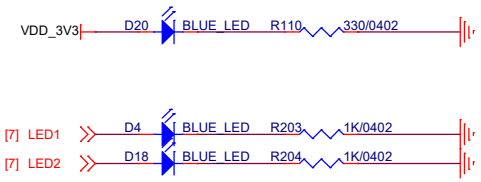
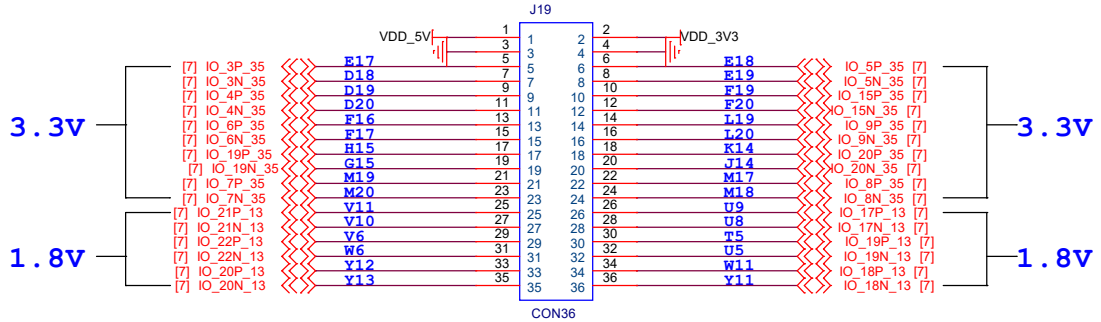
LED



EXT CON



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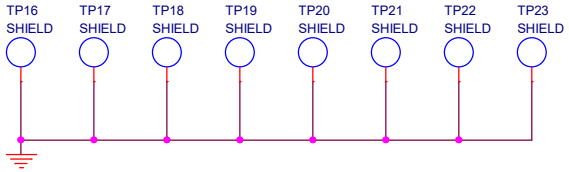

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