



Setting Up MCP2515 using FPGA

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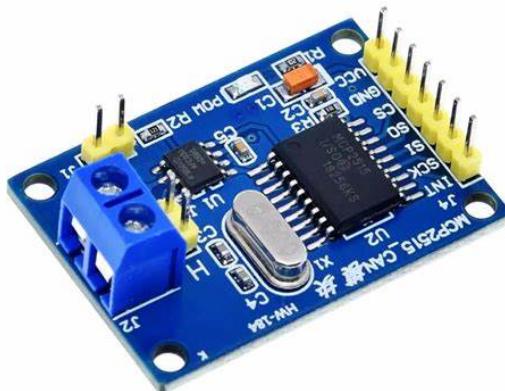
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1 Introduction to MCP2515 module

MCP2515 is a CAN controller. This controller is a simple module that supports CAN protocol version 2.0B which can transfer data with rate of 1Mbps.

In order to communicate using a CAN Bus you need two of MCP2515 modules. One for the Transmitter and one for the Receiver.



This module includes a MCP2515 CAN Controller IC and also a TJA1050 CAN Transceiver IC. The MCP2515 IC is a CAN Controller itself and has pin required for SPI communication which you can use to communicate with local PCs like FPGA.



The MCP2515 is the main controller and it has 3 sub-modules including CAN Block, SPI Module and Control Logic Unit. CAN Block transmit and receive messages through the CAN Bus. Control Logic Unit is responsible for setting up the module and communication between different parts of module. The SPI Module handles communication using SPI protocol with local PCs.

TJA1050 IC connects the MCP2515 IC and the CAN Bus.

2 MCP2515 modes of operation

MCP2515 IC has 5 modes of operation :

1. Configuration Mode
2. Normal Mode
3. Sleep Mode
4. Listen-Only Mode
5. Loop-back Mode

2.1 Configuration Mode

When you power on the MCP2515, it will automatically goes to Configuration mode. You can also change mode to Configuration mode by sending a SPI instruction and set the value of CANCTRL[7:5] to 100. In order to initialize this IC you have to set some internal registers.

1. First we set Configuration registers including CNF1, CNF2, CNF3. CNF1 is used to control baud rate prescaler. CNF2 deals with propagation segment.
2. Next step is to set TXRTCTRL register. By setting TXRTCTRL[2:0] to 1, TXnRTS pins will be used to request message transmission of TXBn buffers.
3. The CANINTE register contains the individual interrupt enable bits for each interrupt source. When an interrupt occurs, the INT pin is driven low by the MCP2515 and will remain low until the interrupt is cleared by the MCU. We should activate this feature by setting CANINTE register value.
4. Each MCP2515 module must have and ID. When a message is received by the module, it will compare ID bits of the message. If the message ID fits the module ID, it means that the message is related to this node and it will stored in receive buffers. We have to set this ID for the module by changing appropriate filter register. It is also needed to set the Mask register that defines which ID bits are used for comparison.

Note : these registers are only modifiable in configuration mode.

2.2 Loop-back Mode

Loop-back mode will allow internal transmission of messages from the transmit buffers to the receive buffers without actually transmitting messages on the CAN bus. This mode can be used in system development and testing. In order to change operating mode to Loop-back mode you have to send a SPI byte write instruction and set the value of CANCTRL[7:5] to 010.

2.3 Normal Mode

Normal mode is the standard operating mode of the MCP2515. In this mode, MCP2515 will transmit messages over the CAN bus.

Other modes of operation are not mentioned in this article.

3 How MCP2515 works

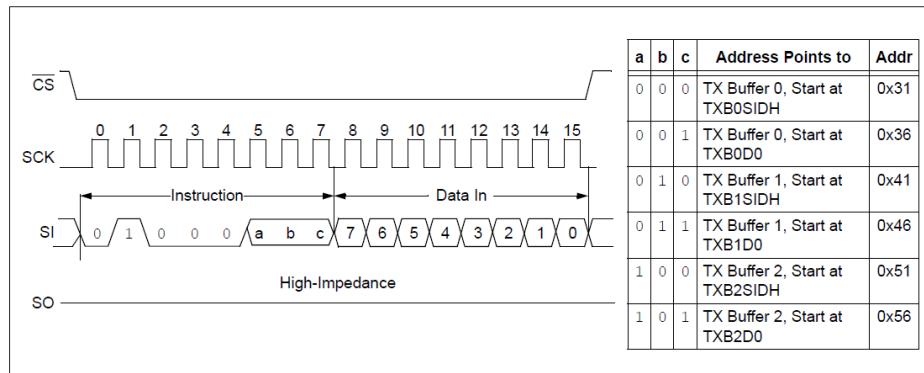
3.1 Transmit Data

In order to send and transmit data through the CAN bus you have to do four steps bellow :

1. Load transfer buffers

First step for transmitting message is to lead the desired transfer buffer. In order to do so, you have to send Load TX Buffer SPI Instruction. You can see this instruction's format bellow :

FIGURE 12-5: LOAD TX BUFFER INSTRUCTION



2. The next step is to Set the message ID. By doing this you can set who is this message for. Use byte write SPI instruction and set appropriate address and value.
3. In this step you have to send Ready To Send (RTS) SPI instruction. The message will not be sent unless you send this SPI instruction.
4. The last step is to be sure that the message has been sent successfully. We check the value of TXERR bit of TXBnCTRL register to see whether there is an error in transmission. If an error occurs we try to send message again for particular times for example 10 times. If transmission still failed we abort this message.

3.2 Receive Data

Message Reception includes these steps :

1. Whenever a message pass the acceptance filters and be stored in receive buffers and interrupt will be occurs and value of INT pin will be set to '0'. Under this condition we will send Read RX Buffer SPI Instruction and will receive the data through SPI module.
2. after receiving a message INT pin will be '0' and won't be reset to '1' so in order to be able to receive more new messages you should related bit if CANINTF register by sending Byte modify SPI Instruction.

Note : Click [here](#) to Download MCP2515 data sheet

4 FPGA Code

4.1 Overview

In this section we will define a mini project.

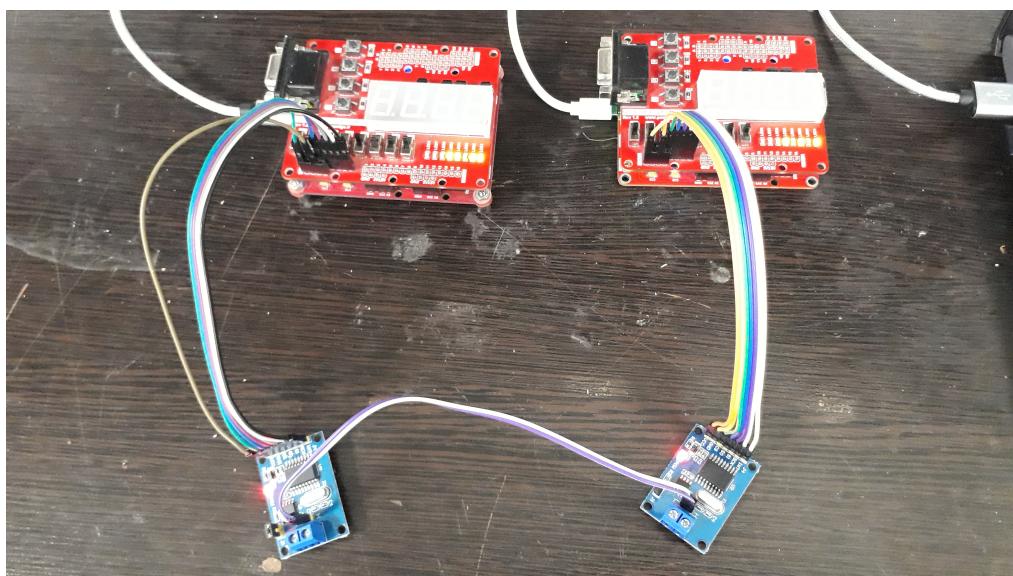
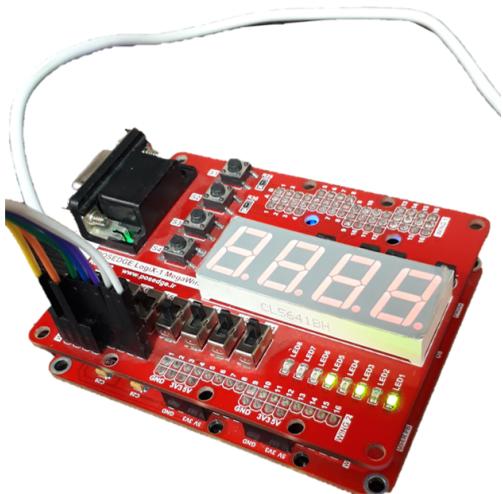
The subjective is to send a byte from one FPGA-1 to another one.

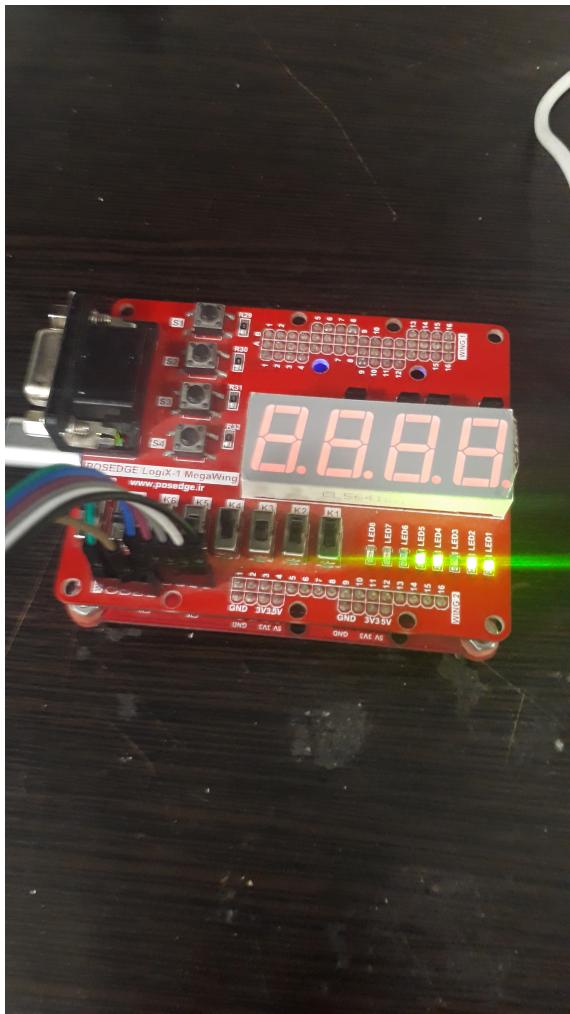
Show the received data by LEDs on the FPGA-2.

Defining a byte by deep switches on the FPGA-2.

Add this value to the received data and send the result to the FPGA-1.

There is a button for reset(Btn1) and also a button for sending message(Btn4). In this project we have used two posedge boards including Xilinx spartan-6 FPGA and two MCP2515 modules.





4.2 Code

As mentioned before, MCP2515 communicates through SPI protocol so we need to define a VHDL module for this. According to SPI instruction list, instructions include 8, 16, 24, 32 bits to transfer via SPI pins so our VHDL module has a parameter N that defines number of bits to transfer and then in the main code we instantiate 4 of these module. You can use codes for SPI controller available on the Internet.

After that we create another VHDL module named "MCP_driver". In this module we control the can IC for example, we configure the IC and then do whatever needed to send and receive data through the CAN bus.

Here we create a SPI component from the code we wrote before :

```

1 component spi_master
2 generic(
3   data_length : integer := 8
4 );
5 port(
6   clk      : in      std_logic;           —system clock
7   reset_n : in      std_logic;           —asynchronous active low reset
8   enable   : in      std_logic;           —initiate communication
9   miso     : in      std_logic;           —master in slave out
10  sclk    : out     std_logic;            —spi clock
11  ss_n    : out     std_logic;            —slave select
12  mosi    : out     std_logic;            —master out slave in
13  busy    : out     std_logic;            —master busy signal
14  tx      : IN      std_logic_vector(data_length-1 downto 0); —data to transmit
15  rx      : out     std_logic_vector(data_length-1 downto 0); —data received
16  tx_end  : out     std_logic;            —data received
17 );
18
19 end component;
20

```

In this module we create a FSM to handle configuration, transmission, reception and ... of MCP2515 IC.

```

1 type FSM_STATE is (RESET, GO_CONFIG, SET_CNF1, SET_CNF2, SET_CNF3, CHECK_TX, READ_REG,
2   SET_MASK_H, SET_MASK_L,
3   SET_TXRTSCTRL, SET_CANINTE, GO_NORMAL, LOAD_TXB, CENTER, SET_ID_H, READ_ID, SET_DLC,
4   SET_RTS, READ_RXB, READ_STATUS, RX_STATUS, SET_CANINTF, SET_FILTER);

```

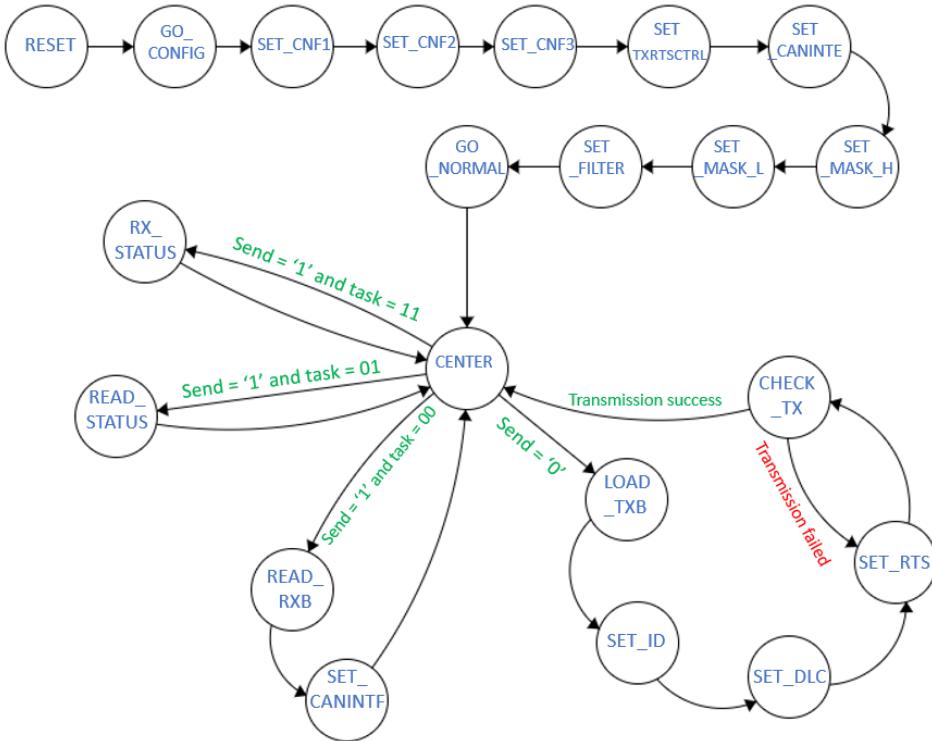
For example the code for going to configuration mode is shown bellow :

```

1 ━━━━━━━━ Go to Config mode of Operation ━━━━━━
2 when GO_CONFIG =>
3   if tx_end_24 = '1' then
4     c_state <= SET_CNF1;
5   else
6     c_state <= GO_CONFIG;
7   end if;
8
9   data_in_24 <= "00000010" & "00001111" & "10000000";
10  spi_sel <= 3;
11
12  if busy_24 = '0' then
13    busy_counter <= busy_counter + 1;
14    if busy_counter = 3 then
15      enable_24 <= '1';
16      busy_counter <= 1;
17    end if;
18    else
19      enable_24 <= '0';
20    end if;
21

```

FSM block diagram :



After these steps we create a VHDL file as our top module and instantiate a MCP_Driver component.

```

1  receiver_module : MCP_driver
2  port map(
3    clk => clk ,
4    rst => rst ,
5    SO => SO,
6    SI => SI ,
7    SCLK => SCLK,
8    SS => CS ,
9    INT => INT ,
10   data_in => data_in ,
11   data_out => data_out ,
12   task => task ,
13   send => send
14 );
15
16  process (clk)
17  begin
18    if rising_edge(clk) then
19      if INT = '0' then
20        LED_reg <= data_out;
21        end if;
22      end if;
23    end process;
24    data_in <= LED_reg + ("00" & num);
25    oLED <= LED_reg;
26
  
```

It is also possible to communicate between a FPGA board like spartan-6 and an Arduino Uno board using CAN protocol.

Here we assume the transmitter is the Arduino Uno and the receiver is our spartan-6 FPGA.

We send a byte using Arduino serial interface and also we receive new data from FPGA using the send button and show the received data using serial.

Here we just use CAN and SPI libraries available in the Internet.

```
1 #include <SPI.h>
2 #include <mcp2515.h>
3
4 struct can_frame canMsg1;
5 struct can_frame canMsg;
6
7 MCP2515 mcp2515(10);
8
9 int temp;
10
11 void setup() {
12     canMsg1.can_id = 0x88;
13     canMsg1.can_dlc = 1;
14     canMsg1.data[0] = 0x00;
15
16     while (!Serial);
17     Serial.begin(115200);
18
19     mcp2515.reset();
20     mcp2515.setBitrate(CAN_125KBPS, MCP_8MHZ);
21     mcp2515.setNormalMode();
22     mcp2515.sendMessage(&canMsg1);
23     Serial.println("Example: Write to CAN");
24 }
25
26 void loop() {
27     if (mcp2515.readMessage(&canMsg) == MCP2515::ERROR.OK) {
28         Serial.println("-----");
29         Serial.print(canMsg.can_id, BIN); // print ID
30         Serial.print(" ");
31         Serial.print(canMsg.can_dlc, HEX); // print DLC
32         Serial.print(" ");
33
34         for (int i = 0; i<canMsg.can_dlc; i++) { // print the data
35             Serial.print(canMsg.data[i], BIN);
36             Serial.print(" ");
37         }
38
39         Serial.println();
40     }
41
42     if (Serial.available() > 0){
43         temp = Serial.read();
44         if (temp != 10){
45             canMsg1.data[0] = temp;
46             // Serial.println(temp);
47             mcp2515.sendMessage(&canMsg1);
48         }
49     }
50
51 }
52 }
```

```
1000101000 8 1110001 0 0 0 0 0 0 0  
-----  
1000101000 8 1110001 0 0 0 0 0 0 0  
-----  
1000101000 8 1110001 0 0 0 0 0 0 0  
-----  
1000101000 8 1110001 0 0 0 0 0 0 0  
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1000101000 8 1110001 0 0 0 0 0 0 0  
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1000101000 8 1110001 0 0 0 0 0 0 0  
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1000101000 8 1110001 0 0 0 0 0 0 0  
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1000101000 8 1110001 0 0 0 0 0 0 0
```

Autoscroll Show timestamp Newline Clear output

