

Plaformio

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1. In the circuit shown below, P and Q are the inputs. The logical function realized by the circuit shown below

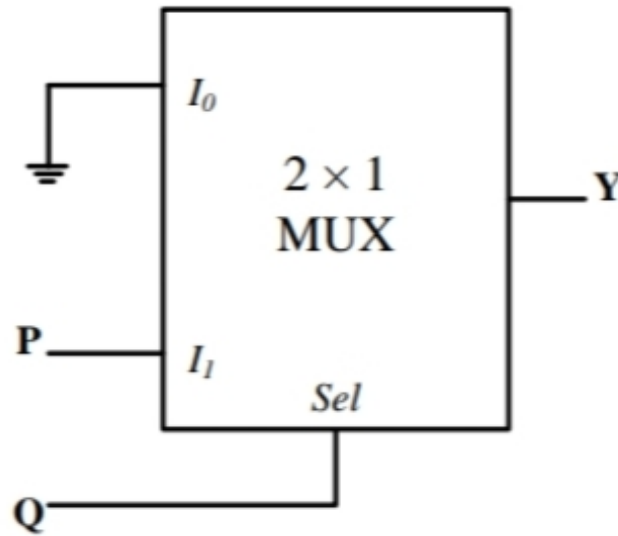


Figure 1: Neglecting the delays

(GATE EC 2023)

- (a)  $Y=PQ$
- (b)  $Y=P+Q$
- (c)  $Y= \overline{PQ}$
- (d)  $Y= \overline{P+Q}$