· UNIT-亚米

Number systems: It represents the magnitude (pr) quantity of a number.

Decimal number System contains 10 unique Symboly 0, 1, 2, 3, 4, 5, 6, 7, 8, 9. As counting in decimal involves 10 symboly, its bage on radix is 10.

— In this system, any number can be represented by the use of these 10 symbols only. Each Symbol is called a digit.

The left most ligit in any number which has greatest Positional weight out of all ligits present is called MSB (Most Significant Bit) and the right most ligit with least positional weight is called LSB (Least Significant Bit).

- The first digit to the left of the Lecimal point has a weight of wait x or 10°, second digit has a weight of 10' or 10 so on.

- The first digit to the right of decimal point has a weight of 10-1, second has a weight of 10-2 and soon

- It is a positional number system (positional weighted &. - thousand thank ten units.

- In this system any numbers of any magnitude any be represented by the use of these 10 symbols.

- Group 4 bits is called as Nibble.

- Group of 8 bits is called as Bytes.

- Group of 16 bits is called as word

- Group of 32 bits is called as Double word.

- In general the value of any decimal number system can be represented as dudn-1 dn-2-d2d, to

- Representation: dux10"+ dn-1×10"++ --- d,x10"+ dox10".

- Representation: dux10"+ dn-1×10"++ dox10".

Example: i) Represent 4367 number in decin (4367) = 4x13+3x102+6x10+7x100 = 4000 + 300 + 60+7 = (4367)10 ii) Represent (6379.581) number in decimal: (6379.581) = 6x103+3x102+7x101+9x10°.5x10-1+8x10-2 = (6379.581) IN Binary Number system: The base or radix of this system is 2. The symbols used are and A binary digit is colled a bit. A binary number consist of sequence of bits, each of which either our. - Decimal equivalent of dn dn-1 dn-2 dido dad-2 da 15 dnx2"+dn-12"+---dox20+d-12"+---d-nx2" - This can be used in digital computers because switching circuits used in the computer uses two state Levices such as (on & off) Liodes, transistars etc. Example: Decimal equivalent of (10101)2 is 1x2+0x2+1x2+0x2+1x20 = 16+0+4+0+1=21 · (10101) 2 = (21) 10 -> convert (101-100)2 into Lecimal 1×22+ 0×21+ 1×20. 1×2-1+0×2-2+0×2-3 4+0+1.1/2+0+0 (101·100)2 - (5.5)10 2 10-1 > convert (H3)10 to binary-2/2-1 ·· (43)10 = (101011)2

1. CONVERT (13-52)10 into PINARX 2 to to ax 2/13 0.52 × 2 = 1.04 - 1 Fractional 058 2 3-0 0-0H×J= 0.08-0 Park 0.08×5 = 0.10 - 0 0.16×2 = 0.32 - 0 $0.32 \times 2 = 0.64 - 0$ Integer Part 0.64x2 = 1.28 - 1V ·· (13.52)10 = (1101.100001)2 Octol Number System: - Base or radix -> 8=8 - It is also a positional weighted number 14sten - Symboly - 0,1,2,3,4,5,6,7 - Representation - (number) 8 Example: convert (43)10 into octal 8/43 (43)10= (53)q convert (13.52)10 into octal Fractional Part Integer Part 0.52 × 8 = 4.16 - 4 8/1-5/ 0.16 ×8 = 1.28 - 1 0.58 x8 = 5.24 - 5 0.24 x8 = 1.92 - 1 0,92×8 = 7.36 - 7. ·. (13.52) = (15.41217) 8

```
> convert (356)8 into Decimal
          3x 82+5x8'+6x80
          = 3x64+5x8+6x1
           = 192+40+6
            238
         (356)8 = (238)10
 -> convert (2 41.12) 8 into Decimal
     2x82+ 4x81 + 1 x80 . 1x8-1+2x8-2
        2×64+4×8+1×1.1/8+2/64
          128+32+1.0.125+0.03125
                 161.15625
             · (241.12) = (161.15625)10
Hera Decimal Number System.
- It is also a Positional weighted number system
- Base or radin is 16.
- Symbols → 0,1,2,3,4,5,6,7,8,9, ,A,B,C,D,E,
- Representation - (Number) 16
- If radix is 5 the symbols are - 0,1,2,34.
-> Convert (26.52) to into Hexa Decimal : (26.52)
                  0.52×16 = 8.32-8 = (IA.851E)
   16/26
    16 [1 - A
                  0.32 × 16 = 5.12 - 5
                    0.12×16=1.92-1
                    0.92×16=14.72-E
```

? Convert (AF3) 16 into Decimal = 10×162+15×161 +3×160 = 10×256+15×16+3×1 = 2560 + 240 +3 = (2803)10 > Internal conversions - Convert (10110)2 into octal -> we know that 8= 23 - convert binary number in terms of groups of the power (overthere 3) - Always add zero to the left side so that there is no increase in weight. 010/110 · (10110) 2 = (26) 8 -> (110 1110) 2 to ()8 00 110 1110 : (1101110)2 = (156)8 convert (101101.11011) Into octal 101/101.110/110 5 5 . 6 6 : (101101-11011)2 = (55.66)8

-> convert (356)8 into bhasx 3 5 6 - Convert each Light

1 1 to binary only upp 011 101 110 3-61+3. : (356)8 = (011101110)2 -> convert (101101.11011)2 to Here decimal 0010/11011101/1000 2 D · D 8 : (101101.11011)2 = (2D.D8)H -> convert (A F 3)16 to binary A F 3 (AF3)16 = (101011110011) -> convert (AF3)16 into octal - First convert Hera decimel number into binary then convert binary into octal. 10 10 11 11 0011 5 3 6 3 : (AF3)16 = (5363)8 -> convert (356.12)8 into Here decimal - First convert octal number into binary then convert that binary number into heredecimal. 3 5 6 . 1 2 0 E E . 2 . 8 : (356-12)8 = (EE. 28)6

Find the valin for the given number:

(35)6 = (43)_x

- Multiply number with base

3 5 = 4 3

61 6°
$$x$$
1 x 9

3x6+5x1 = 4x4+3x19

18+5 = 4x43

23 = 4x43

20 = 4x

 $x = 20/4 = 5$

-> Convert (101101)₂ into ()₅

- Fixst convext binary to decimal then convert it like base 5:

(101101)₂ = 1x3 +0x3 +1x3 +1x2 +0x2 +1x2 = 1x3 +0x2 +1x3 = 1x3 +0x2 +1x1 = (45)₁₀

5 45

5 9 - 0

5 1 - 7

Convert (101101)₂ = (140)₅

Binarsy Arithmentic

A B Sum carry

A B Diffe Barrow

A

32 X 10

di

4

-> Add 13, 15 in binary 010 -> subtract 11,7 13-> 1101 in binary 15 -> (+1) 1 1 28 -> 11100 11 -> 6011 7-> 0111 13+15= (11100)2 4-> 0100 Signed and Unsigned Numbers in Binary - Unsigned binard numbers represent only magnitude. - Signed binary numbers represent both magnitude and sign (direction). - In signed number, the Most significant Bit (MSB) represent the sign of the number. If MSB = 0 -> Positive Number If MSB=1 -> Negative Number - In general an n-bit signed magnitude integer lies within the range of $-(2n^{-1}-1)$ through $(2^{n-1}-1)$ Number indications in signed & unsigned: Number Signed unsigned -0 -> 1 000 0000 0000 0000 +0 -> 00000000 0000 0000 -3 -> 10000011 0000 0011 +3 -> 0000 0011 1100 0000 - In case of unsigned 8-bit binary number the decimal range is 0 to 255. - In case of Signed number the largest magnitude is reduced from 255 to 127. So the range is from - 127 to 0 to +127. Complements: One's complement: The number that results by changing all ones to zeroes and all Zeroes to ones is called one's (1's) complement.

calculate the 1's complement of (101101)2 Given Number -> 101101 1's complement - 010010 > Two's complement: (2's) 2's complement is a binary number that results when we add one to 1's complement. 2's complement = 1's complement + 1 Ex: Given number -> 101101 1's complement > 010010 21's Complement > 0 1 0 0 1 1 -> Nine's complement: subtracting each & every digit from nine gives a's complement. Ex: Colculate the 9's complement of 543 999 (-) 543 - Given number 456 - 9's complement -> Ten's complement: Adding I to the nine's comple ment will give los complement. 10's complement = 9's complement +1 En: Calculate the 10's complement of (624)10 (-1624 - Given number 375 - 9's complement 376 - 10's complement - In general we have 7's (radin's complement) comple ment of any given numbers the formula used is 7m-N r - radin of given number where

M- number of digits in number N- the diven number (in any number -> (8-1)'s-(Diminished radia) complement: To find the diminished radia complement of any given number system we use the formule -> (87-1)-N Ex: Find the ofs & (8-1)'s complement for the given number (345) 8's complement = xn-N 8=6, n=3, N=6345 : 8's complement = 6-345 = -129 (8-1)'s complement = (8n-1) - N = (63-1)-345 =(216-1)-345 ~ 215-345 = - 130 -> subtract 1011011 from 1100101 10000101 - 101 (-)1011011 - 91-> Subtract (101) = (91)10 - In this case we have to subtract there number from parget number: Procedure: 1) Determine the 1's complement of the smollest number 2) Add I's complement of the smaller number to the larger number

3) If carry is present to the to
in LSB Position. This carry is called as end-
Land Could 12 courses or suff.
namber
91 -> 1011011
1's complement = 0 1 001 00
Larger Number -> 1100101
1's complement -> +10100100
M0001001
+1
0001010 = 10
=> Subtraction of larger number from smaller number
Procedure: 1) Determine 1's complement of larger
number
2) Add 1's complement of the larger number to
Smaller number
3) To get the answer in true form, take 1's
complement of the result and assign negative sign
to the result.
Ed: Subtract (101)10 from (91)10
91-101 = -10
1's complement of 101 -> 1100101
0011010
Add 1's complement to (91%
91-> 1011011
1's complement of 101 -> (+)00 11 0 10.
-> we don't have carrox, so use step 3.
xegult = 1110101 -> 1's complement = 0001010

Find result = 1010 A22 sign (-ve) = -1010 = -10 2's complement Subtraction. - Subtraction of Smaller number from larger number Steps: 1) Determine 2's complement of smaller number 2) All the 2's complement of smaller to larger 3) In this case, discared the carry. Ex: 76 - 42 = 34 Sol: Find 2's complement of 42 Given number -> 42=0101010 1's complement of 42 = 1010101 2's complement -> 1010110 Add a's complement to larger number 21s complement of 42 = 1010110 Griven larger number 76=1001100 CO0100010 DISCAND CARRY Final result = (100010)2 = (34)101 => Subtraction of larger number from smoller: Steps: 1) Determine 2's complement of larger number 2) Add 2's complement of larger to smallers 3) To get answer in true form, take 2's complement of result and assign negative sign. Ex: Subtract 42-76= -34 Given larger number -> 76 = 1001100 1's complement -> = 0110011 2's complement -> 0110100

(1) Given smaller number 42 = 0101010 21s complement of 76 =(+) 0110100 No carry - 1011110 Result -> 1011110 1's complement - 01 00001 0100010 Find result = (100010) == (34)0 Add -ve sign = - (100010) = - 34 Codes: we are having different types of codes F 8421, 2421, 5211,5421 - BCD : - Excess-3 2) Non-weighted - 5-bit BCD - 2421 3) Reflective .-- Excess-3 - Groay -8421 4) Sequential -- Excess-3 - ASCII 5) Alphanumeric -6) Error Letecting & correcting -1 -> In this gray code is used in k-map simplify cations. The advantage of the gray code over the Straight binary number sequence is that only one bit in the code group changes in Joing from one number to the next. For example, in going from T to 8, the gray coke changes from 0100 to 1100. only the first bit changes, from a to 1, the other

3 bits remain the same. By contrast, with binary numbers the change from 7 to 8 will from 0111 to 1000, which causes all four bits to change values. - It is a special case of unit distance. - Bit Patterns for 2 consecutive numbers lifter in only 1-bit position. Ex convert the number (10101101) to Groay Given Binary: POTOTIOT Gray code : 11111011 -> Convert 11111011 number from Gray to binary Given Gray code: 111110 Binary: 1/1/1/1 Boolean Algebra: - Boulean Algebra, like any other deductive methe metical system, may be defined with a set of elements, a set of operators, and a no-of unproved axioms or postulates. A set of elements is any collection of objects, usually having a common property. Properties and theorems Boolean Algebra 1) x+0 = xx 2) x. 1= x 3) xfx = 1 H) N. x = 0 5) nttn=n 6) x.x=x 7) 1+1=1 8) 4.0=0 9) In volution theorem: (x') = x

Commutative law x+y=x+n x+y=x+n(11) Associative law: x+(x+z)=(x+y)+zx+(x+z)=(x+y)+z

12) Distributive law: n(x+z) = nx + nzn+xz = (n+x)(n+z)

13) Demorgan's law: (n+x) = n'.x1
(xx) = n'+x1

It) Absorption 'law: n+xx = n n(x+x) = x

Principle of Duality: In boolean algebra,

If one expression can be obtained from the

other in each pair by replacing every o with 1,

every I with 0, every (+) with (i), and every (·)

with (+), then the Pair of expression satisfying

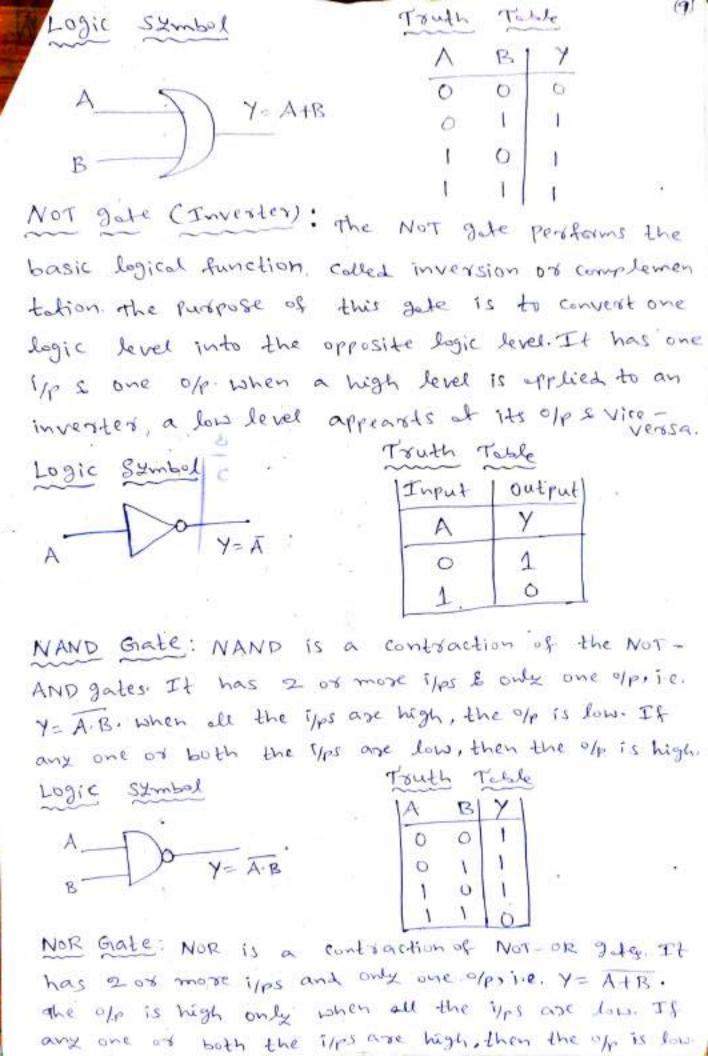
this property is called dual empression of the principle

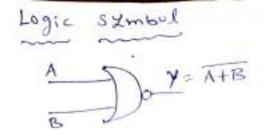
of duality.

Logic Gates .

-Boolean eyebra is used in describing a simply in my dogic circuits. Simplification of boolean logic empressions is very important because it reduces the hardware required to design a specific system. The boolean empression corresponding to a given that metwork can be derived by systematically progressing from the 1/p to the 0/p of Jotes, the Joling or logic metwork can be formed by interconnecting the or AND and Not Jotes.

- A logic gate is an electronic ckt which make logical decisions. To arrive of these decisions, the most common logic gates used are OR, AND, NOT. NAND & NOR gales. Among these, AND, OR, NOT got are alled as basic garty. The NAND & NOR gate are called as universal gets because all the gates can be realized by using only these states. The other two gates are Ex-OR & Ex-NOR. AND Gate: The AND. Jake Performs Logical multi Plication, commonly known as AND function. The AND gate has two or more . Ups & a single output the o/p of an AND gate is high only when all the ips are high. Even if any one of inputs is low the op will be low. If A & B are the ip variable of an AND gate & y is its o/p, then Y = A.B Logic - symbol Truth Table Inputs output Y = AB A B 0 0 0 0 0 OR Grate: lothe OR gate 0 1 Performs dosical addition, Commonly known as OR function. The OR gate has two or more you & only one o/p. The operation of or gate is such that a high on the olp is produced when any of the ilps is high. The olp is low only when all the 1/ps are low. If A & B are the 1/p variables of an organic y is its ofp, then yo A+B.





rut	h	Table
A	B	Y
D	0	1
0	1	D
1	0	0
1	1	0_

Exclusive - OR (Ex-OR) Gate: An Ex-OR gate is a gate with 2 or more 1/ps & one o/p, the o/p of a 2 1/P Ex-or gate assumes a high state if one and only one 1/p assumes a high state. This is equivalent to saying that the o/p is high if either 1/p A or 1/p B is high exclusively, and low when both are 1000 Truth Toble Simultaneous Ly.

Logic	Symbol
Α	Y=ADB.
B	

ADB = AB+ AB

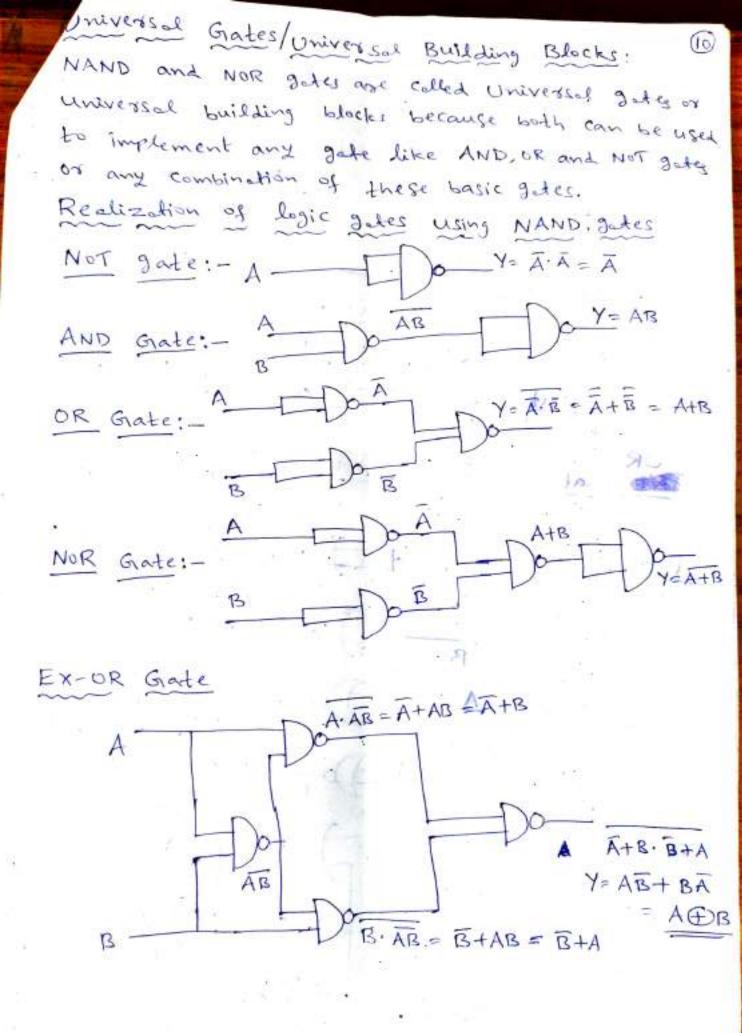
Enclusive -NOR (EX-NOR) gate: It has 'two or more ilps and one op. The op of a two lip Ex-NOR gote assumes a high state if both 1/Ps assume the Same logic state or have an even no of Ls, and its ofp is dow when the 1/ps assume different logic

Logic Symbol

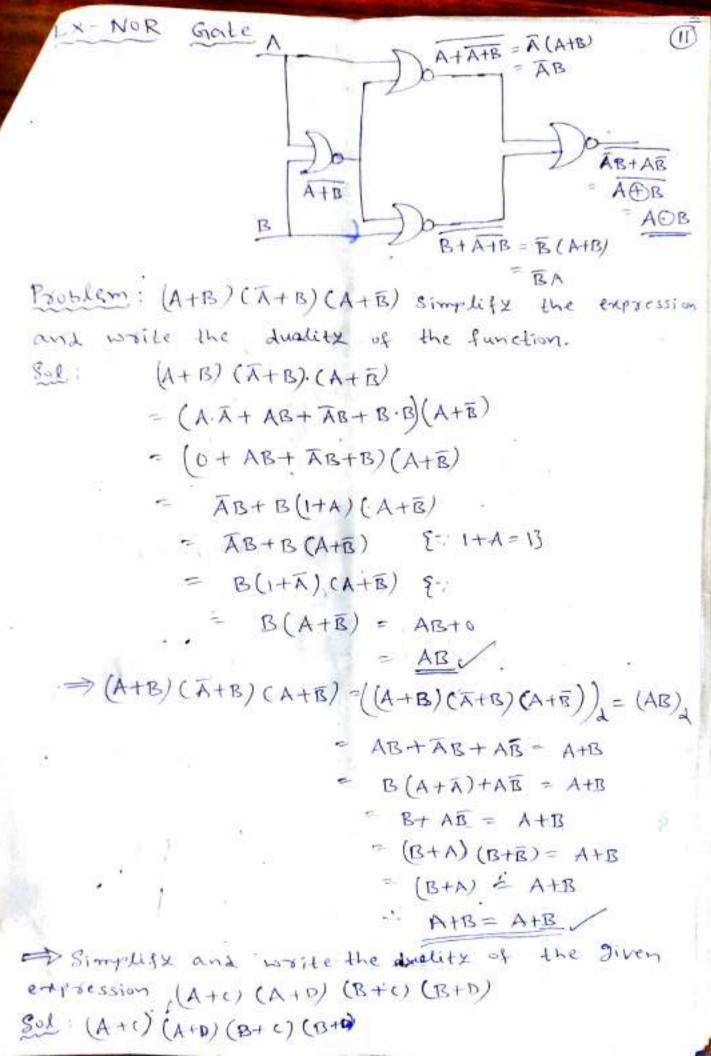
States or have an odd no-of 1's

AOB = ABB = AB - AB

A	B	·y
D.	. 0	1
0	1	0
1	0	0
1	4	1



EX-NOR Gate: A- AB = A+B A AB AEB ADB = AOB B. B. AB = B+A of logic gates using NOR Grates Realization - Y = A+A = A > NOT Gate: OR $\mathcal{B} + A = \frac{\mathcal{B} + \overline{A}}{1 + A}$ Gate AND Gate ! A+B = AB B NAND Gate: Y= AB B A+ A+B = A (A+B) EX-OR Gate: = AB A . AOB AB+ AB A+B - AOB =AEB B -B+A+B=B(A+B) AB



$$= (A \cdot A + AD + Ac + CD) (B \cdot B + BD + Bc + CD)$$

$$= (A + AD + Ac + CD) (B + BD + Bc + CD)$$

$$= (A(1+D) + Ac + CD) (B(1+D) + Bc + CD)$$

$$= (A + Ac + CD) (B + Bc + CD)$$

$$= (A + CD) (B + CD)$$

$$= (A + CD) (B + CD)$$

$$= (A + CD) (B + CD)$$

$$= (AB + CD)$$

$$= (AB$$

Implement Y = AB + A+ (B+c) using NAND 12 gates only. Sol -AB Y = AB. A. (B+ () = AB+A+ (B+0) Reclize Y= (A+C)(A+D)(A+B+Z) using NOR goles. Sal (A+B+c) (A+c) Y=(A+C)(A+D)+ (A+D) (A+B+c) => Simplify the given logic circuit below. A+E·BD = A+E+BD (A+E) A+Z·BD = AC+BD BD Z= (A+2+BB) (AC+BB) = (A+E+B+D) (AC+B+D) = (AC) (BD) + (A+E) (BD) = B (A C + (A + E))

{- A+(B+0) = (A+B)(A+) - AC+ (A+E)= 1 Simplify the given logic circuit below: Y= A (BC+D) - A+BCD : Y= A+BCD Canonical and Standard Forms: - To implement a logic functions in less no. of gates, we have to minimize literals or varia bles and the number of terms. - Visually, literals and terms are arranged in one of the two standard forms: i) sum of Products (SOP) ii) Product of sum terms (POS) SOP: It is a group of product terms OR togetter Pos: It is a grown of sum terms AND together. AB+BC - SOP (A+B) (B+c) - Pos => In the truth task 18 Y= (01101011) then represent sop & Pos forms. Y=01101011 :: SOP= 5m(1,2,4,6,7) iks > 01234567 POS= TM (0,3,5)

Table, A B C SOP (Min terms Pos (Man. terms) mo= ABE 0 0 0 Mo = A+B+C mi= ABC 0 0 M, = A+B+2 m2= ABE 0 0 M2 = A+B+C m3= ABC M3 = A+B+E 1 0 mH= ABE M4 = A+B+C 0 M5 = A+B+C m5= ABC 1 0 M6 = A+B+C m6 = ABE 0 M7 = A+B+C my = ABC 1 Problem: Simplify the expression: AB+ABC+AB+AB Sol. AB+ABC+A(B+AR) = A(B+BC)+A(B+A) = A(B+c) + AB+A·A = AB+AC +AB+A = AB+AC+A(B+1) = AB+AC+A = (A+B)(A+E)+A = (A+BE) + A & (A+B)(A+C) = A+ = A+BE+A = 1+Bc 2: 1+ x=1] = 1 = 0 Problem: Simplify the empression: Y= ABE+ABE+ABE + ABE Sol: Y = ABC + ABC + ABC + ABC Ac (B+B) + AE (B+B)

= AE+AE

of every product term involves every variable in complement or uncomplement form.

Standard Pos: Product of Sum expression is referred to as a canonical or standard Pos expression if every sum term involves every variable in complement or uncomplement form.

Prob: Convert the given expression into std sop.

AB+BC

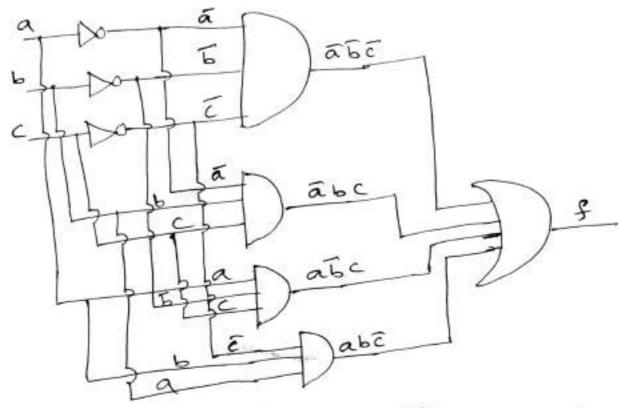
Sol: - It consists of 3 variables A, B, C. In first

term C is missing sinsecond term A is missing.

referred to as canonical or Standard sop expression

- We know that A+A=1 & C+E=1, hence we (4) multiply them to second and first term respec tively. Given Egn- AB+BC = AB (C+E) + BC(A+A) = ABC+ ABC + ABC + ABC - ABC + ABC + ABC = 111 110 011 7 6 3 .. f= Em (3,6,7) Prob: convert given expression, into standard Pos (A+B) (A+C) (B+E) Sol1 It consists of 3 variables A,B,C. We know that A. A=0. Find out the missing terms in first c, in second B, in third A. Hence add CT, BB, AA respectively in 1, 2nd and 3th terms Given ean - (A+B) (A+c) (B+C) = (A+B+cE)(A+BB+c)+(AA+B+E) = (A+B+C)(A+B+C)(A+B+C)(A+B+C)(A+B+C) (A+B+E) = (A+B+() (A+B+E)(A+B+C) (A+B+E) = (A+B+c)(A+B+c)(A+B+c)(A+B+c) = 0000001010101 .. F = TM (0,1,2,5) Prob: Represent the given function in terms of expression. Simplify it and draw the logic ckt diy dam. f= 2m (0,3,5,6)

Sol: f(a,b,c) = 2m(0,3,5,6) $f = mot m_3 + m_5 + m_6$ f = 000 | 011 | 101 | 110f = abc + abc + abc



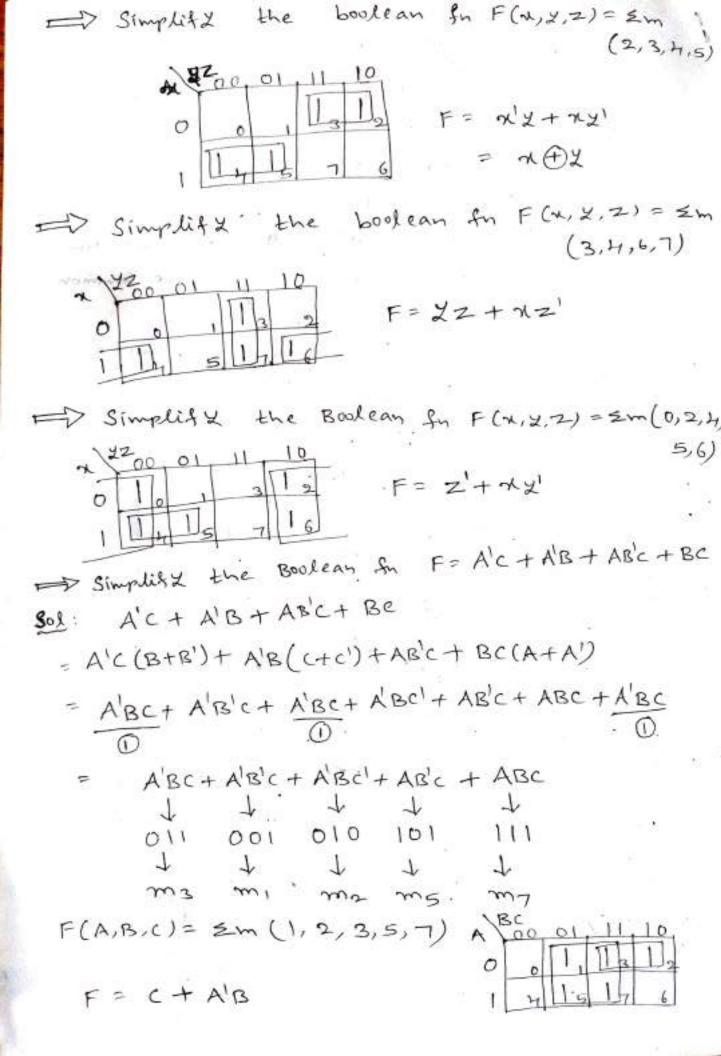
-If we minimize the empression, $f = \overline{abc} + \overline{abc} + \overline{abc} + \overline{abc}$, we will get $= \overline{a(bc+bc)} + \overline{a(bc+bc)}$ $= \overline{a(bbc)} + \overline{a(bbc)}$ $= \overline{a(bbc)} + \overline{a(bbc)}$ $= \overline{a(bbc)} + \overline{a(bbc)}$ $= \overline{a(bbc)} + \overline{a(bbc)}$

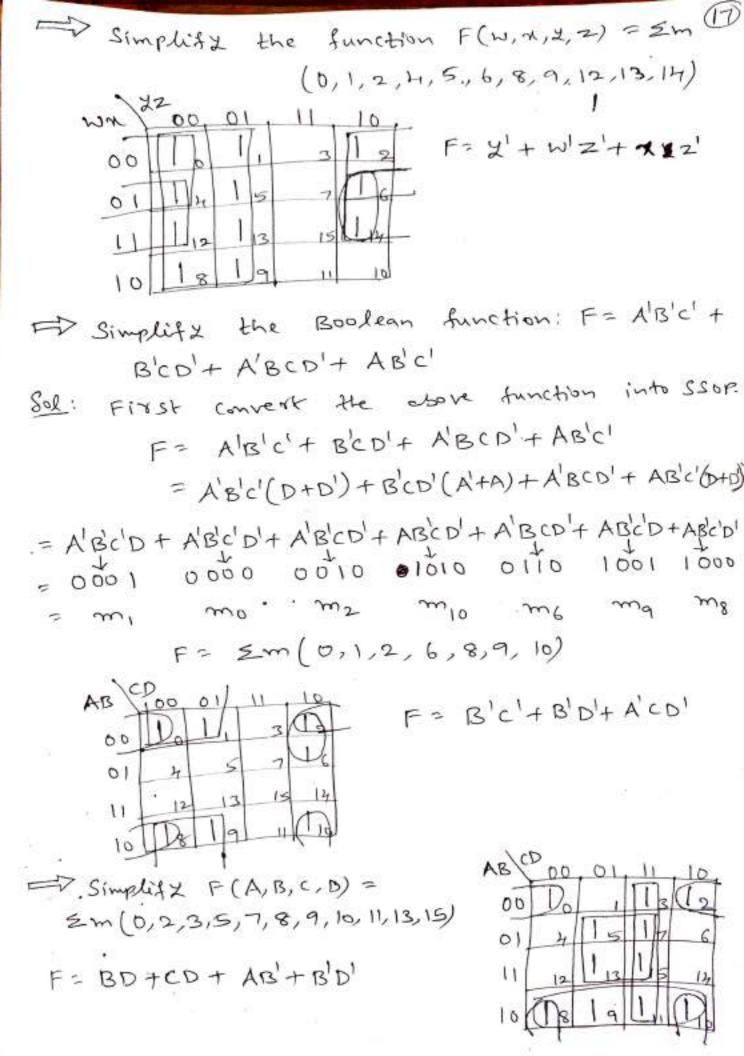
The karsnaugh (k-mer) Mar Method: (15) - The simplification of the switching functions Using Boolean laws & theorems becomes complex with the increase in the mo-of variables and terms. The K-map technique provides a system atic method for simplifying and manipulating Switching expressions. In this technique, the infor mation contained in a truth table or available in the pos or sop forms is represented on the K-map. The k-map is actually a modified form of a truth-toble. In an n-variable t-map, there are 2" cells. Each cell corresponds to one combination of on variables. Therefore, for each now of the touth table, i.e. for each min term and for each maxtery there is one specific cell in the k-map. The K-maps for 2,3 and 4 variable are shown in fig. below. The decimal codes corresponding to the combination of variable are given inside the cells. the variables have been marked as A,B,CID, and the binary numbers formed by them are taken as AB, ABC and ABCD for 2, 3 and 4 variously respectively. AB (CD00, 01, 11, 10 ABC 00 01 11 10 0 1 A 80 1 4 5 7 6 01 14576 11 12 13 15 14 3- Variable 2 - Varsishle 4-variable Fig. Karknaugh Maps

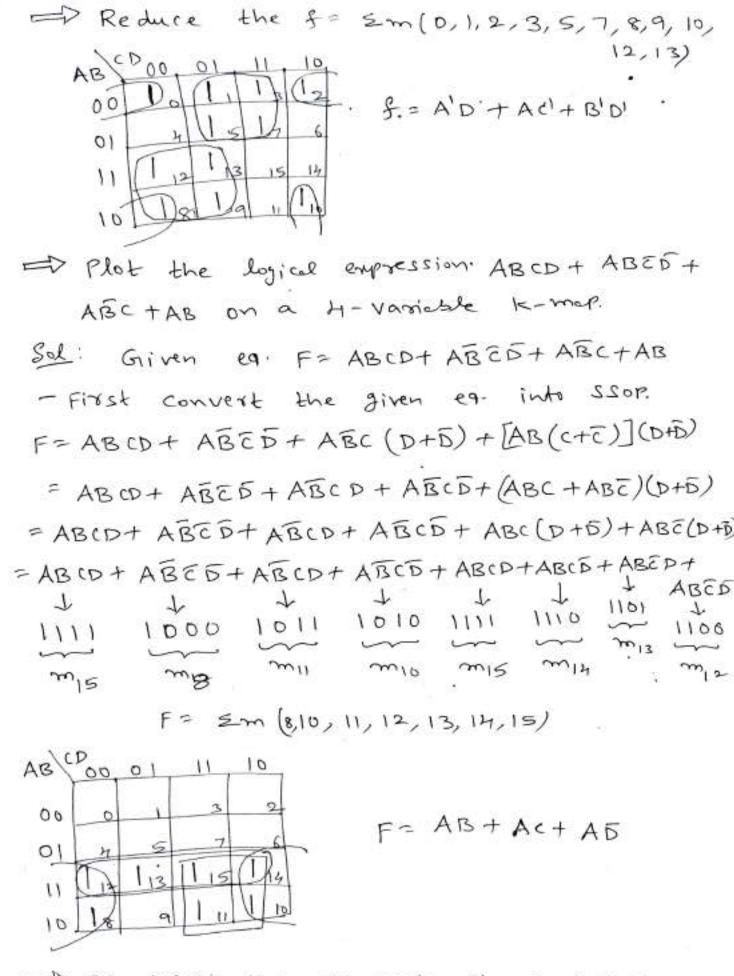
- The 3 and It variable K-maps show that the column and sow headings, used in representing the cells, are cyclic or unit distance code which result in adjacent cells, differing in just one Varsichle. This helps the grouping of the adjacent cells and in their simplification. The left and right most cells of the 3-variable k-map are adjacent. For example, the cells 0 and 2 are abjacent, and the cells 426 are adjacent. This is because each pair differs in just a single variable. In the H- variable K-map, the cells to the entreme left and right as well as those, at the top and bottom must Positions are adjacent. - Simplification is based on the principle of combini my the terms Present in adjacent cells. The 1s in the adjacent cells can be grouped by trawing a loop around those cell following the given rules: 1) construct the K-map and enter the 1s in those celly corresponding to the combinations for which function value is 1, then enter the os in the Other cells. 2) Enamine the map for Is that cannot be combined

- 2) Enamine the map for Is that cannot be combined with such single 1.
- 3) Next, look for those Is which are adjacent to only one other I and form groups containing only 2 cells & which are not part of any group of 4 or 8 cells. A group of 2 cells is called a pair.
- 4) Group of 15 which results in group of 4 cells

but are not part of an 8-cells group. A group 6 of 4 cells is collect a quad. 5) Group the 1s which results in groups of 8 cells. A group of 8 cells is called an octet. 6) Form more pairs, quads and octets to include these is that have not get been grouped, and use only a minimum no-of groups. These can be overlapping of groups if they include common 1s. 7) Omit any redundant group. 8) Form the logical sum of all the terms generated by each group. - When one or more than one variable appear in both complemented & uncomplemented form within a group, then that variable is eliminated from the term corresponding to that group. - Variables that are the same for all the cells of the group must eppear in the term corresponding to that group. - A larger group of 15 eliminate more variably. TO be precise, a group of 2 eliminates one variable a group of 4 climinates two variables, similarly a group of 8 eliminates throne variables. Brob: Simplify Y= Em(2,3) => Simplify Y= AB+AB by using k-Mup by using K- Map 00 Y= AB+ AB Y = A

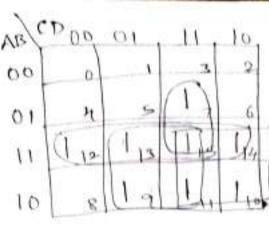






=> Simplify the empression Y = 2m (7,9,10,11, 12,13,14,15) using the k-mar method





Y= AB+ AD+ A(+ BCD = A(B+(+D)+BCD

Simplify the expression Y= 2m(3,4,5,7,9,

AB(CDOO 01 11 10

AB CD 00 01 11 10 00 0 1 13 2 01 14 D5 17 6 11 12 P3 (15 D14 10 8 D9 11 10

Y= ABE+ ABCD+ AED+ ABC

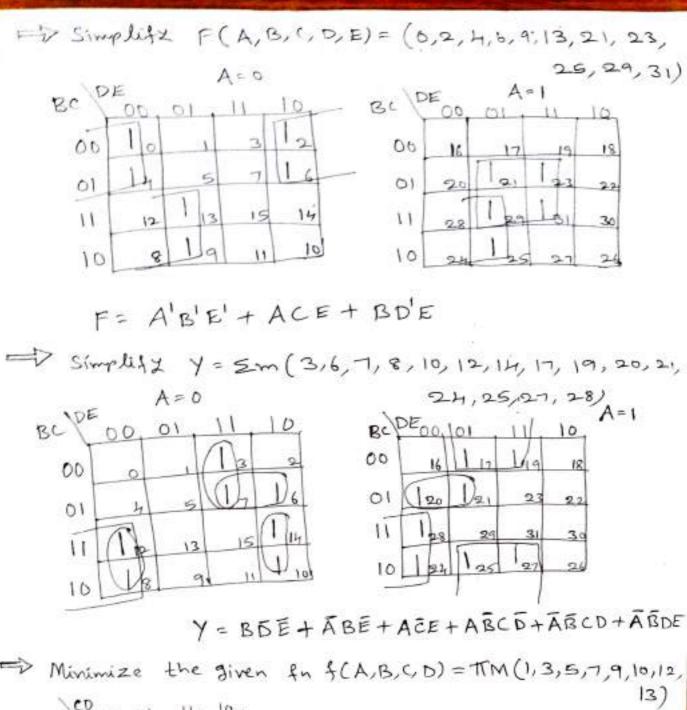
Five- variable k-Map

- Maps for more than four variables are not as simple to use as maps for four or fewer variables. A five - variable map needly 32 square, when the norof variables becomes large, the norof squares becomes excessive and the geometry for combining adjacent squares becomes more involved.

The consists of 2 four-varieties mers with varieties $A,B,C,D,E\cdot A$ listinguishes by the two maps, as indicated at the top of the distinguishes A=0

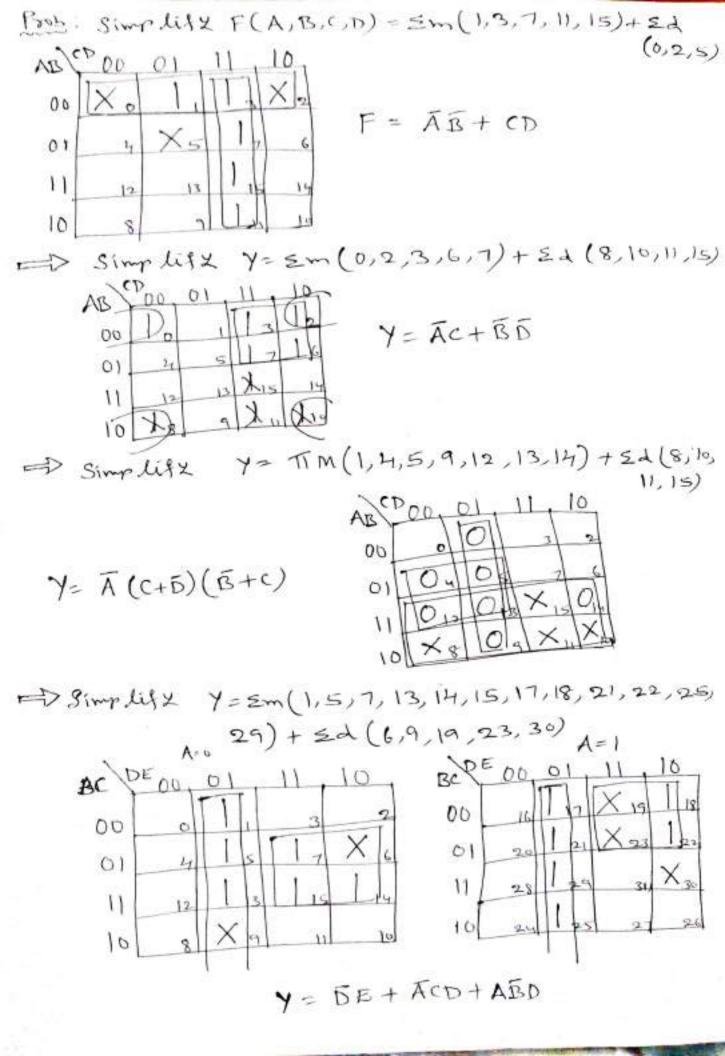
001 "	00	wi	mz	mo
	4/	ms	my	w(
11/20	112	213	mis	2017

BC P	E	01	A = 1	10
00	m16	السر	mid	m18
01	mag	m21	maz	meg
11	m28	m29	m31	m30
10	mak	m25	men	m26



Don't care Combinations: In certain digital systems, some i/p combinations mover occurs during the process of a mound operation because those i/p conditions are guara intera mover to occur. Such i/p combinations are don't care combinations. we don't care what the In o/p is for such combinations. We don't care what the In o/p is

on a map to provide further simplification of the for - The function considered so fard in the various examples, for simplification using the k-map method, are completely specified, ie. it assumes the volue I for some Up combinations & the value ofor others. Also, there are functions which assume the voluce I for some combinations, the value o for some other and either o or I for the remaining combinations Such functions are colled incompletely specified functions, and the combinations for which the value of the function is not specified are called don't care combinations. The don't care combinations are represented by a or x. - when an incompletely specified for ise a to with don't case combinations, is simplified to obtain minimal sop expression, the value 1 can be assigned to select don't case combinations. This is done in order to increase the no-of 1s in the selected groups, wherever further simplification is possible. Also, a don't case combination need not be used in grouping if it does not cover a large no-of 15. In each case, the choice depends only on the simplification that has to be achieved. Similarly when a function is simplified to obtain a mining pos expression, the value o can be assigned to selected don't case combinations in order to increase the number of os in the selected groups which results in further simplification.



NAND OF NOR gates wather than with AND and or determine components a are the basic dates used in electronic components a are the basic dates used in ell Ic digital logic families. Because of the prominence of NAND and NOR dates in the design of digital circuits, rules and procedures have been developed for the conversion from hoolean functions diven in terms of AND, OR and NOT into eqt NAND and NOR logic diagrams.

NAND circuits AND Inventer a Do-no (n'x') = n+x NAND Gate: three - input Implement F= AB+CD (b)

to

implement F = AB+CD

