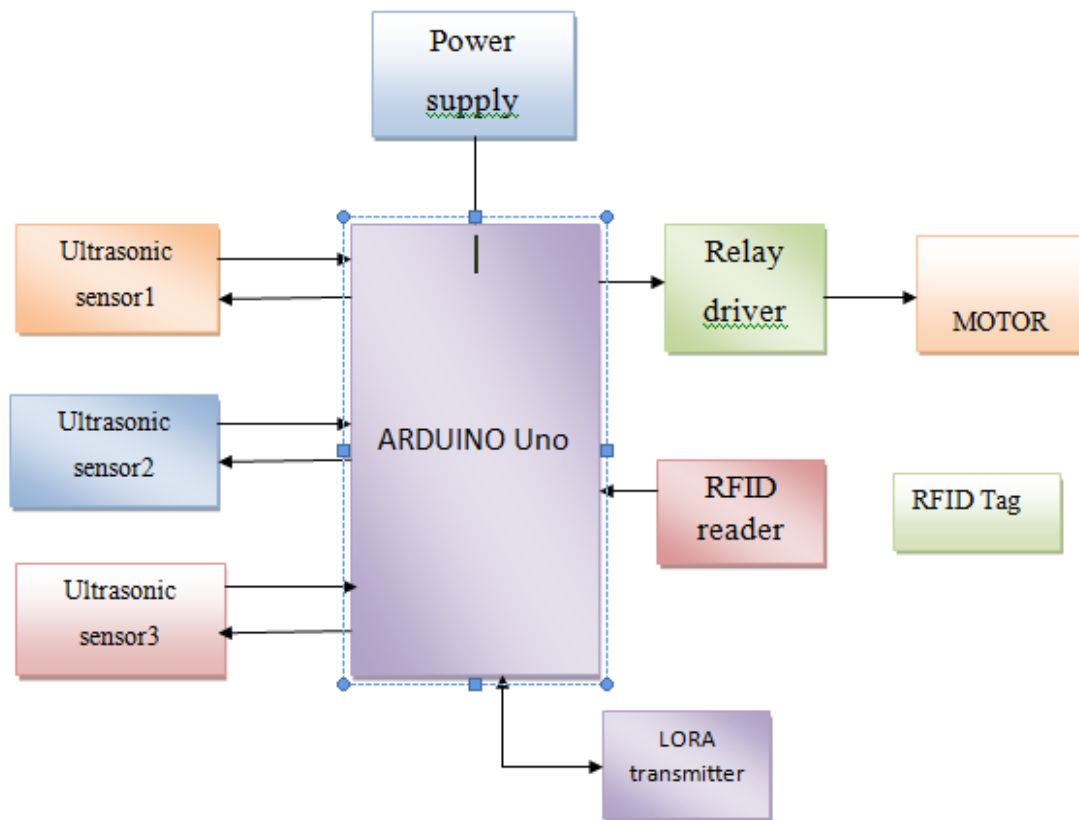


SYSTEM ARCHITECTURE

1. Transmitter part



2. Receiver part

