

CSU22022 Computer Architecture I

Prof. Michael Manzke

Full Processor Project 3rd Instalment

Checklist

11th November 2024

Version 1.0

All files must be submitted to Blackboard as individual files, no zip-files. **With one exception: the “Processor_XXXXXXXXX.srcs” must be a zip-file.** Submit only entities that are working and are fully tested. You must follow the entity order on the checklist. You cannot skip entities.

The entities must be programmed and tested in the following order. Please confirm by ticking all the boxes, next to the items, that all required files are available on Blackboard and that the entity is fully working. You must print this checklist, tick the boxes, sign it, and submit it with your 3rd Instalment to Blackboard.

All files must be submitted in the checklist order:

DP_Datapath_XXXXXXXX.vhd
DP_Datapath_XXXXXXXX_TB.vhd
DP_Datapath_XXXXXXXX_SchematicXX.pdf
DP_Datapath_XXXXXXXX_TDXX.png
DP_Datapath_XXXXXXXX_Doc.txt

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00	My submission complies with the “Instructions for the Blackboard Submission of the Assignment” (CSU22022 Full Processor Project 3rd Instalment 2024-2025 Instruction V1.0.pdf)	
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01	DP_Datapath_XXXXXXX.vhd	
	DP_Datapath_XXXXXXX_TB.vhd	
	DP_Datapath_XXXXXXX_SchematicXX.pdf	
	DP_Datapath_XXXXXXX_TDXX.png	
	DP_Datapath_XXXXXXX_Doc.pdf (can be any file format)	
	Is working	

02	Processor_XXXXXXX.srscs (including all subdirectories and files)	
	Processor_XXXXXXX.xpr	

03	CSU22022 Full Processor Project 1st Instalment 2024-2025 Checklist V2.0	
	CSU22022 Full Processor Project 2nd Instalment 2024-2025 Checklist V2.0	

Student Name: _____

Student ID: _____

Date

Signature