

CSU22022 Computer Architecture I

Prof. Michael Manzke

Assignment: Milestone Function Unit

Checklist

14th October 2024

Version 1.0

All files must be submitted to Blackboard as individual files, no zip-files. With one exception: the “Processor_XXXXXXX.srscs” must be a zip-file. Submit only entities that are working and are fully tested. You must follow the entity order on the checklist. You cannot skip entities.

The entities must be programmed and tested in the following order. Please confirm by ticking all the boxes, next to the items, that all required files are available on Blackboard and that the entity is fully working. You must print this checklist, tick the boxes, sign it, and submit it with your milestone assignment to Blackboard.

All files must be submitted in the checklist order:

DP_FullAdder_XXXXXXX.vhd
DP_FullAdder_XXXXXXX_TB.vhd
DP_FullAdder_XXXXXXX_SchematicXX.pdf
DP_FullAdder_XXXXXXX_TDXX.png
DP_FullAdder_XXXXXXX_Doc. txt (can be any file format)
DP_RippleCarryAdder32Bit_XXXXXXX.vhd
DP_RippleCarryAdder32Bit_XXXXXXX_TB.vhd
DP_RippleCarryAdder32Bit_XXXXXXX_SchematicXX.pdf
DP_RippleCarryAdder32Bit_XXXXXXX_TDXX.png
DP_RippleCarryAdder32Bit_XXXXXXX_Doc.txt (can be any file format)

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00	My submission complies with the “Instructions for the Blackboard Submission of the Assignment” (CSU22022 Milestone Function Unit 2024-2025 Instruction V1.0.pdf)	
01	DP_FullAdder_XXXXXXXX.vhd	✓
	DP_FullAdder_XXXXXXXX_TB.vhd	✓
	DP_FullAdder_XXXXXXXX_SchematicXX.pdf	✓
	DP_FullAdder_XXXXXXXX_TDXX.png	✓
	DP_FullAdder_XXXXXXXX_Doc.txt (can be any file format)	✓
	Is working	✓
02	DP_RippleCarryAdder32Bit_XXXXXXXX.vhd	✓
	DP_RippleCarryAdder32Bit_XXXXXXXX_TB.vhd	✓
	DP_RippleCarryAdder32Bit_XXXXXXXX_SchematicXX.pdf	✓
	DP_RippleCarryAdder32Bit_XXXXXXXX_TDXX.png	✓
	DP_RippleCarryAdder32Bit_XXXXXXXX_Doc.txt (can be any file format)	✓
	Is working	✓
03	DP_SingleBit_B_Logic_XXXXXXXX.vhd	✓
	DP_SingleBit_B_Logic_XXXXXXXX_TB.vhd	✓
	DP_SingleBit_B_Logic_XXXXXXXX_SchematicXX.pdf	✓
	DP_SingleBit_B_Logic_XXXXXXXX_TDXX.png	✓
	DP_SingleBit_B_Logic_XXXXXXXX_Doc.txt (can be any file format)	✓
	Is working	✓
04	DP_32Bit_B_Logic_XXXXXXXX.vhd	✓
	DP_32Bit_B_Logic_XXXXXXXX_TB.vhd	✓
	DP_32Bit_B_Logic_XXXXXXXX_SchematicXX.pdf	✓
	DP_32Bit_B_Logic_XXXXXXXX_TDXX.png	✓
	DP_32Bit_B_Logic_XXXXXXXX_Doc.txt (can be any file format)	✓
	Is working	✓

05	DP_SingleBit_LogicCircuit_XXXXXXX.vhd	✓
	DP_SingleBit_LogicCircuit_XXXXXXX_TB.vhd	✓
	DP_SingleBit_LogicCircuit_XXXXXXX_SchematicXX.pdf	✓
	DP_SingleBit_LogicCircuit_XXXXXXX_TDXX.png	✓
	DP_SingleBit_LogicCircuit_XXXXXXX_Doc.txt (can be any file format)	✓
	Is working	✓

06	DP_32Bit_LogicCircuit_XXXXXXX.vhd	✓
	DP_32Bit_LogicCircuit_XXXXXXX_TB.vhd	✓
	DP_32Bit_LogicCircuit_XXXXXXX_SchematicXX.pdf	✓
	DP_32Bit_LogicCircuit_XXXXXXX_TDXX.png	✓
	DP_32Bit_LogicCircuit_XXXXXXX_Doc.txt (can be any file format)	✓
	Is working	✓

07	CPU_Mux2_32Bit_XXXXXXX.vhd	✓
	CPU_Mux2_32Bit_XXXXXXX_TB.vhd	✓
	CPU_Mux2_32Bit_XXXXXXX_SchematicXX.pdf	✓
	CPU_Mux2_32Bit_XXXXXXX_TDXX.png	✓
	CPU_Mux2_32Bit_XXXXXXX_Doc.txt (can be any file format)	✓
	Is working	✓

08	DP_ArithmeticLogicUnit_XXXXXXX.vhd	✓
	DP_ArithmeticLogicUnit_XXXXXXX_TB.vhd	✓
	DP_ArithmeticLogicUnit_XXXXXXX_SchematicXX.pdf	✓
	DP_ArithmeticLogicUnit_XXXXXXX_TDXX.png	✓
	DP_ArithmeticLogicUnit_XXXXXXX_Doc.txt (can be any file format)	✓
	Is working	✓

09	DP_Mux3_1Bit_XXXXXXX.vhd	✓
	DP_Mux3_1Bit_XXXXXXX_TB.vhd	✓
	DP_Mux3_1Bit_XXXXXXX_SchematicXX.pdf	✓
	DP_Mux3_1Bit_XXXXXXX_TDXX.png	✓
	DP_Mux3_1Bit_XXXXXXX_Doc.txt (can be any file format)	✓
	Is working	✓

10	DP_ShifterCFlagMux2_1Bit_XXXXXXX.vhd	✓
	DP_ShifterCFlagMux2_1Bit_XXXXXXX_TB.vhd	✓
	DP_ShifterCFlagMux2_1Bit_XXXXXXX_SchematicXX. pdf	✓
	DP_ShifterCFlagMux2_1Bit_XXXXXXX_TDXX.png	✓
	DP_ShifterCFlagMux2_1Bit_XXXXXXX_Doc.txt (can be any file format)	✓
	Is working	✓

11	DP_Shifter_XXXXXXX.vhd	✓
	DP_Shifter_XXXXXXX_TB.vhd	✓
	DP_Shifter_XXXXXXX_SchematicXX. pdf	✓
	DP_Shifter_XXXXXXX_TDXX.png	✓
	DP_Shifter_XXXXXXX_Doc.txt (can be any file format)	✓
	Is working	✓

12	DP_CFlagMux2_1Bit_XXXXXXX.vhd	✓
	DP_CFlagMux2_1Bit_XXXXXXX_TB.vhd	✓
	DP_CFlagMux2_1Bit_XXXXXXX_SchematicXX. pdf	✓
	DP_CFlagMux2_1Bit_XXXXXXX_TDXX.png	✓
	DP_CFlagMux2_1Bit_XXXXXXX_Doc.txt (can be any file format)	✓
	Is working	✓

13	DP_ZeroDetection_XXXXXXX.vhd	✓
	DP_ZeroDetection_XXXXXXX_TB.vhd	✓
	DP_ZeroDetection_XXXXXXX_SchematicXX.pdf	✓
	DP_ZeroDetection_XXXXXXX_TDXX.png	✓
	DP_ZeroDetection_XXXXXXX_Doc.txt (can be any file format)	✓
	Is working	✓

14	DP_FunctionalUnit_XXXXXXX.vhd	✓
	DP_FunctionalUnit_XXXXXXX_TB.vhd	✓
	DP_FunctionalUnit_XXXXXXX_SchematicXX.pdf	✓
	DP_FunctionalUnit_XXXXXXX_TDXX.png	✓
	DP_FunctionalUnit_XXXXXXX_Doc.txt (can be any file format)	
	Is working	

15	Processor_XXXXXXX.srds (including all subdirectories and files)	✓
	Processor_XXXXXXX.xpr	✓

Student Name: MATTHEW POOLE

Student ID: 23373470

01/11/24

Date

Matthew Poole

Signature