## CSU22022 Computer Architecture I

## Prof. Michael Manzke

Assignment: Milestone Datapath

## **Checklist**

30th October 2024

Version 1.0

All files must be submitted to Blackboard as individual files, no zip-files. With one exception: the "Processor\_XXXXXXXXXXxsrcs" must be a zip-file. Submit only entities that are working and are fully tested. You must follow the entity order on the checklist. You cannot skip entities.

The entities must be programmed and tested in the following order. Please confirm by ticking all the boxes, next to the items, that all required files are available on Blackboard and that the entity is fully working. You must print this checklist, tick the boxes, sign it, and submit it with your milestone assignment to Blackboard.

## All files must be submitted in the checklist order:

DP\_Datapath\_XXXXXXXX.vhd
DP\_Datapath\_XXXXXXXX\_TB.vhd
DP\_Datapath\_XXXXXXXX\_SchematicXX.jpg
DP\_Datapath\_XXXXXXXX\_TDXX.jpg
DP\_Datapath\_XXXXXXXX\_Doc.jpg

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My submission complies with the "Instructions for the Blackboard Submission of the Assignment" (CSU22022 Milestone Datapath 2024-2025 Instruction V1.0.pdf)

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DP_Datapath_XXXXXXXX.vhd	
DP_Datapath_XXXXXXXX_TB.vhd	<b>/</b>
DP_Datapath_XXXXXXXX_SchematicXX.pdf	
DP_Datapath_XXXXXXXX_TDXX.png	
DP_Datapath_XXXXXXXX_Doc.pdf (can be any file format)	<b>/</b>
Is working	

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Processor\_XXXXXXXXX.srcs (including all subdirectories and files)

Processor\_XXXXXXXX.xpr

CSU22022 Milestone Register File 2024-2025 Checklist V2.0

CSU22022 Milestone Function Unit 2024-2025 Checklist V2.0

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