

Case	I0	I1	I2	I3	I4	I5	I6	I7	I8	I9	I10	I11	I12	I13	I14	I15	S	Y
1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "0000"	0
1	23373470	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "0000"	23373470
2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "0001"	0
2	0	23373470	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "0001"	23373470
3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "0010"	0
3	0	0	23373470	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "0010"	23373470
4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "0011"	0
4	0	0	0	23373470	0	0	0	0	0	0	0	0	0	0	0	0	0 "0011"	23373470
5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "0100"	0
5	0	0	0	0	23373470	0	0	0	0	0	0	0	0	0	0	0	0 "0100"	23373470
6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "0101"	0
6	0	0	0	0	0	23373470	0	0	0	0	0	0	0	0	0	0	0 "0101"	23373470
7	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "0101"	0
7	0	0	0	0	0	0	23373470	0	0	0	0	0	0	0	0	0	0 "0110"	23373470
8	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "0110"	0
8	0	0	0	0	0	0	0	23373470	0	0	0	0	0	0	0	0	0 "0111"	23373470
9	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "1000"	0
9	0	0	0	0	0	0	0	0	23373470	0	0	0	0	0	0	0	0 "1000"	23373470
10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "1001"	0
10	0	0	0	0	0	0	0	0	0	23373470	0	0	0	0	0	0	0 "1001"	23373470
11	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "1010"	0
11	0	0	0	0	0	0	0	0	0	23373470	0	0	0	0	0	0	0 "1010"	23373470
12	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "1011"	0
12	0	0	0	0	0	0	0	0	0	0	0	23373470	0	0	0	0	0 "1011"	23373470
13	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "1100"	0
13	0	0	0	0	0	0	0	0	0	0	0	0	23373470	0	0	0	0 "1100"	23373470
14	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "1101"	0
14	0	0	0	0	0	0	0	0	0	0	0	0	0	23373470	0	0	0 "1101"	23373470
15	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "1110"	0
15	0	0	0	0	0	0	0	0	0	0	0	0	0	0	23373470	0	0 "1110"	23373470
16	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 "1111"	0
16	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	23373470	0 "1111"	23373470

The propagation delay for this circuit and its 1 bit equivalent is 21 ns.