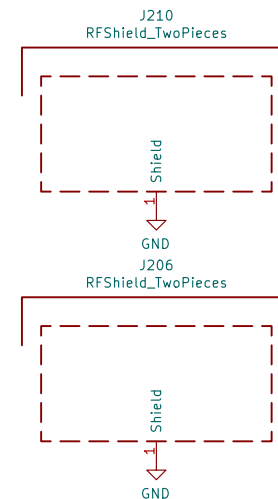
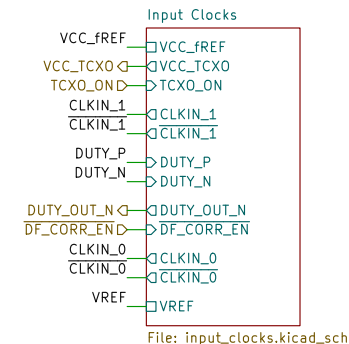
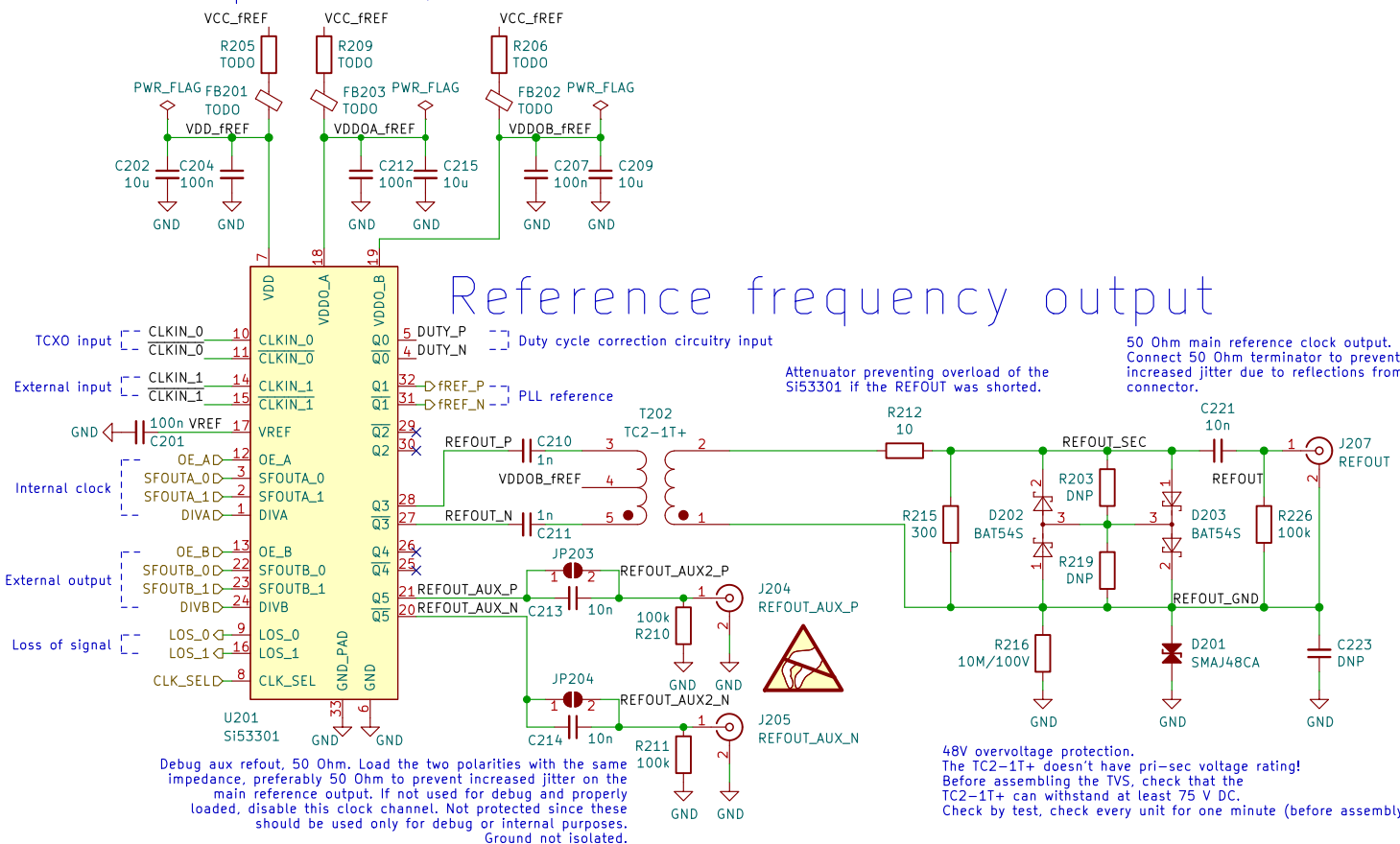
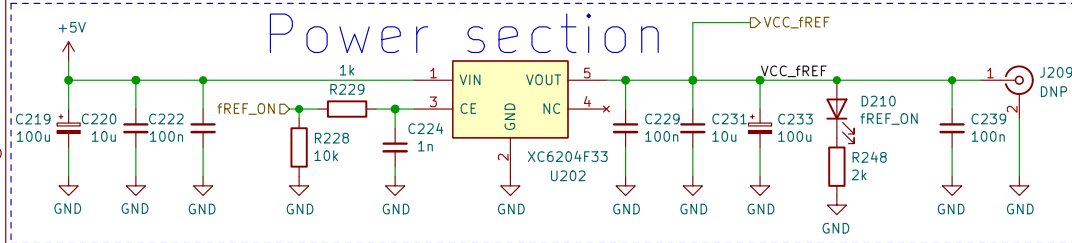


Multiplexer / buffer

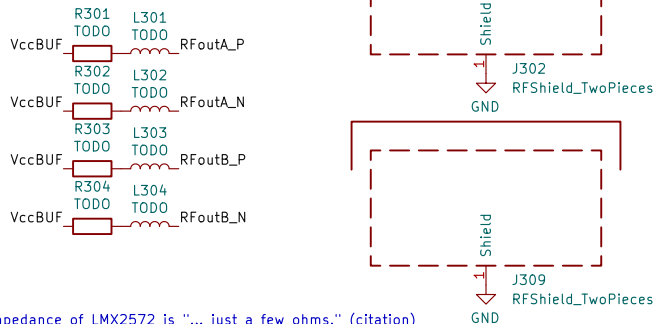
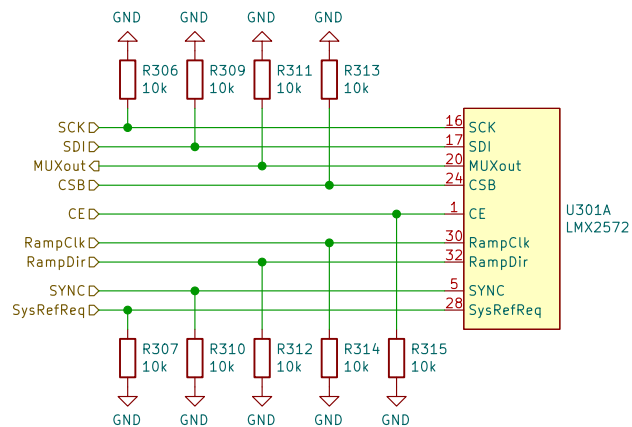


Power section



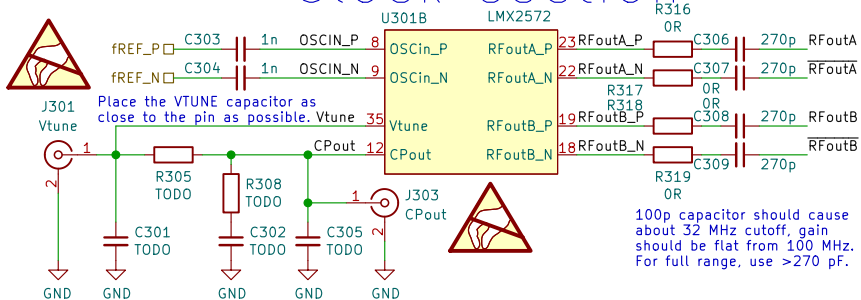
LMX2572 (6.4 GHz) / LMX2592 (9.8GHz) / LMX2594 (15 GHz) / LMX2595 (20 GHz)		
TCXO / external input		
001, 2021-12-30 01:03		
Petr Polasek		
Sheet: /Clock reference/		
File: clock_reference.kicad_sch		
Title: Generator 0.0125 – 6.4 GHz (or up to 20 GHz)		
Size: A4	Date: 2021-12-30	Rev: 211230-001
KiCad E.D.A. kicad 6.0.0-rc2-unknown-160328abc7-144-ubuntu21.10.1 Id: 2/5		

Communication section

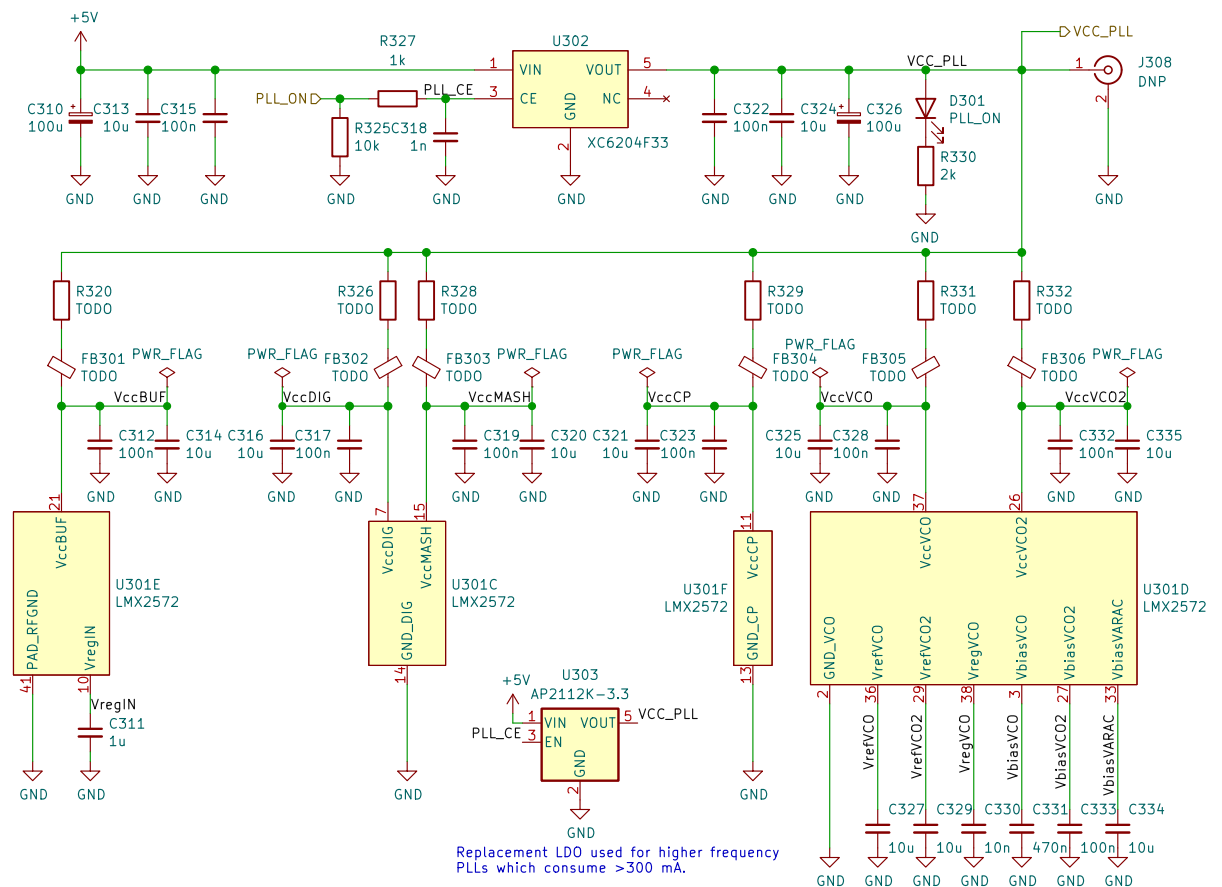


The output impedance of LMX2572 is "... just a few ohms." (citation)
The impedance should be measured and according series resistors should be placed to get 50R output. Place the resistors as close as possible to the PLL.
<https://e2e.ti.com/support/clock-and-timing/f/48/t/833789?LMX2572-LMX2572-RF-output-impedance-For LMX2592, place 0R resistors.>

Clock section



Power section



Be careful as the outputs have no protections and can be sensitive to ESD or devices with DC offset.
Before connecting anything, first be sure to discharge any charge present on the core of the connecting cable and on the connected device!

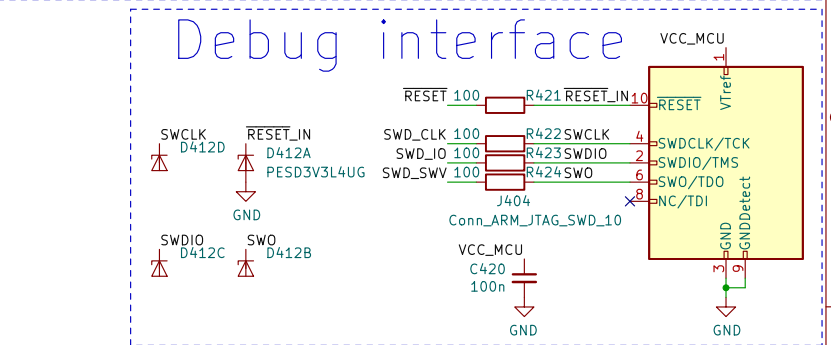
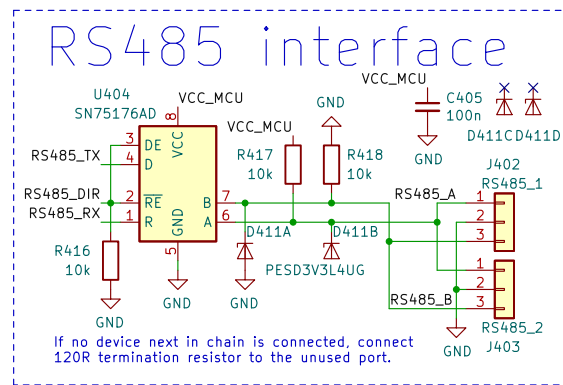
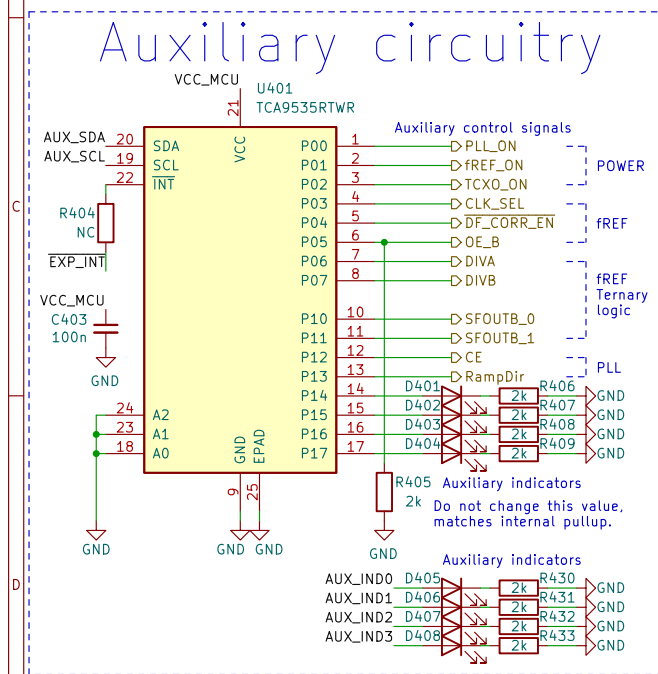
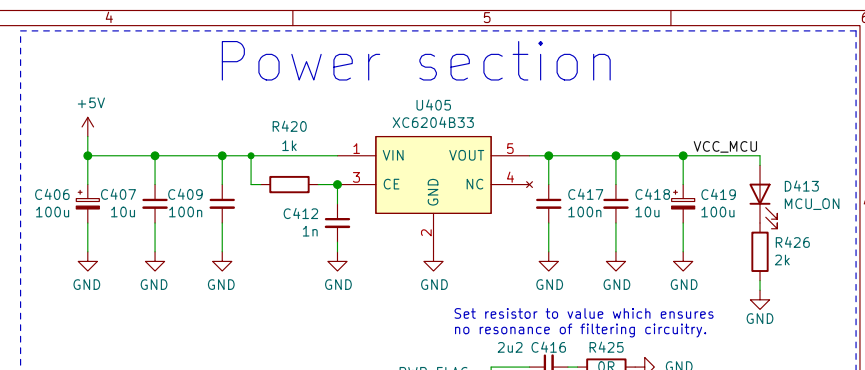
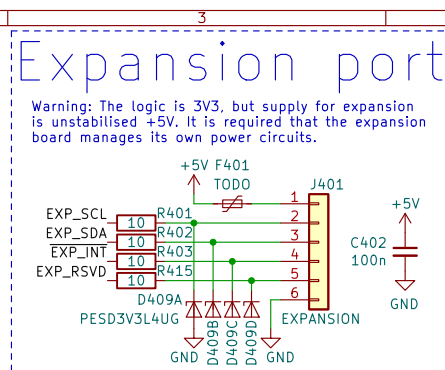
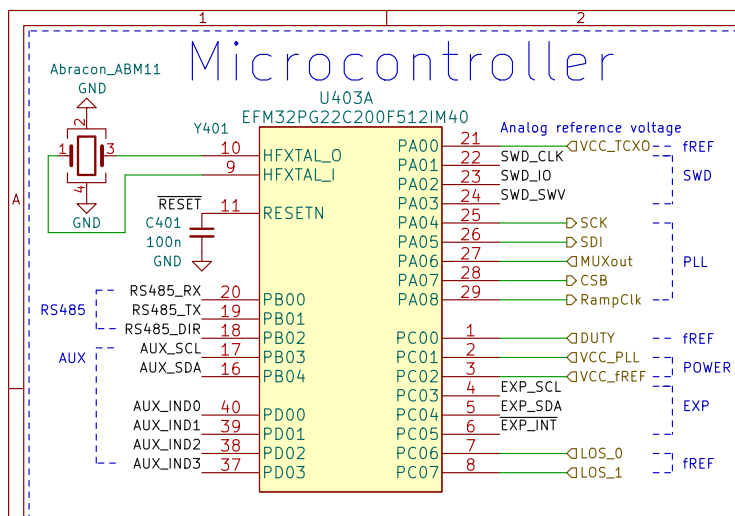
LMX2572 (6.4 GHz) / LMX2592 (9.8GHz) / LMX2594 (15 GHz) / LMX2595 (20 GHz)
TCXO / external input
001, 2021-12-30 01:03

Petr Polasek

Sheet: /PLL/
File: PLL.kicad_sch

Title: Generator 0.0125 - 6.4 GHz (or up to 20 GHz)

Size: A4 Date: 2021-12-30 Rev: 211230-001
KiCad E.D.A. kicad 6.0.0-rc2-unknown-160328abc7-144-ubuntu21.10.1 Id: 3/5



LMX2572 (6.4 GHz) / LMX2592 (9.8GHz) / LMX2594 (15 GHz) / LMX2595 (20 GHz)
TCXO / external input
001, 2021-12-30 01:03

Petr Polasek

Sheet: /MCU/
File: MCU.kicad_sch

Title: Generator 0.0125 – 6.4 GHz (or up to 20 GHz)

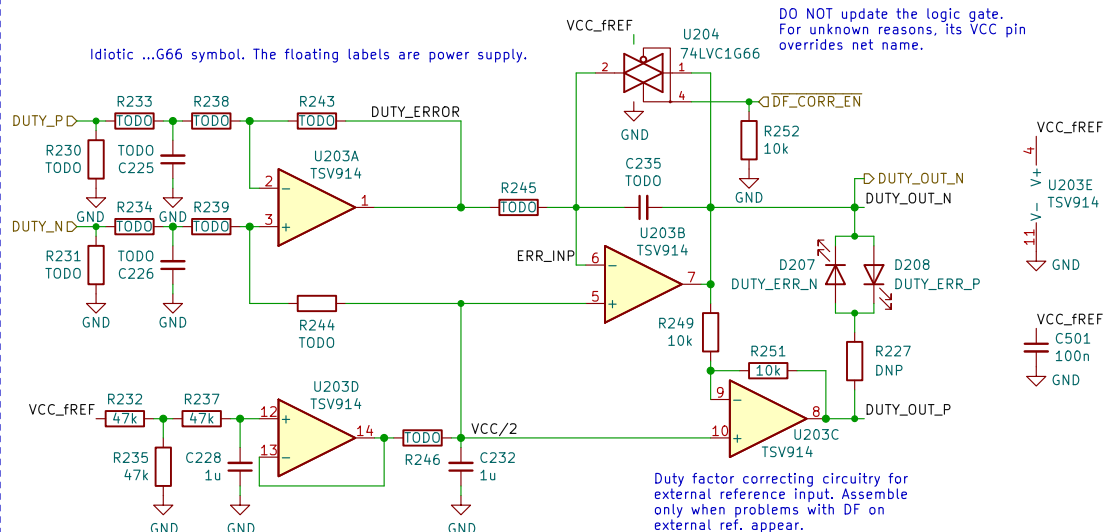
Size: A4	Date: 2021-12-30
----------	------------------

Rev: 211230-001

KiCad E.D.A.	kicad 6.0.0-rc2-unknown-160328abc7~144~ubuntu21.10.1	Id: 4/5
--------------	--	---------

Duty factor / DC corrector

Idiotic ...G66 symbol. The floating labels are power supply.

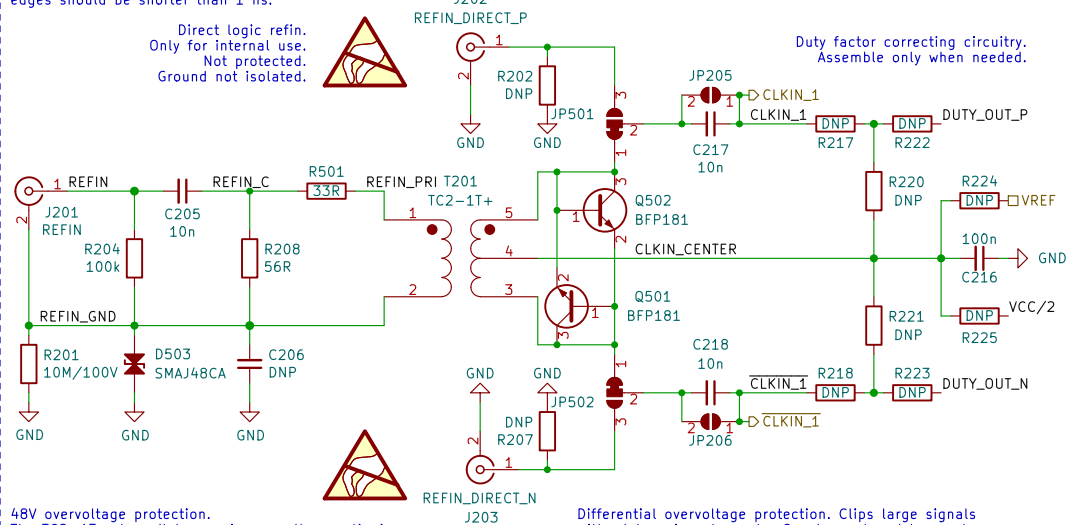


Duty factor correcting circuitry for external reference input. Assemble only when problems with DF on external ref. appear.

Reference frequency input

The Si53301 requires slew rate at least 750 V/us to meet 50 fs additive jitter. This would require 5.5 Vpk-pk @ 10 MHz or 1.45 Vpk-pk at 40 MHz for sine wave. Therefore, clipped sine wave or rectangle is required for best jitter performance. The clipped sine should have slew rate at least 400 V/us and voltage at least 150 mVpp. edges should be shorter than 1 ns.

Direct logic ref.
Only for internal use.
Not protected.
Ground not isolated.



48V overvoltage protection. The TC2-1T+ doesn't have pri-sec voltage rating! Before assembling the TVS, check that the TC2-1T+ can withstand at least 75 V DC. Check by test, check every unit for one minute (before assembly).

Differential overvoltage protection. Clips large signals without lowering slew rate. Can be replaced by most microwave NPN transistors. Do not use diodes as these tend to be too slow or have high capacitance except for special types.

VCTCX0

