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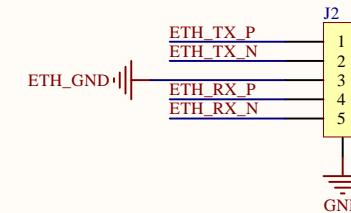
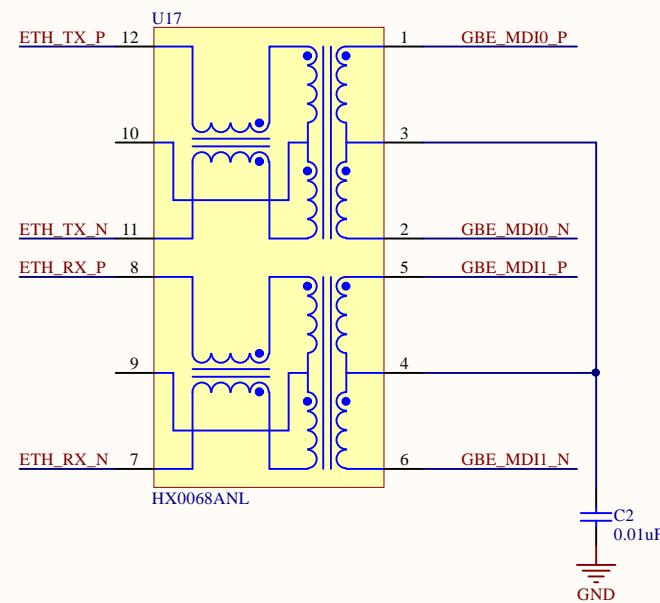
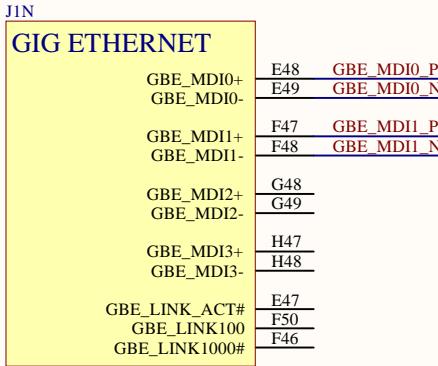
Title		
Size	Number	Revision
A4		
Date:	2017-12-23	Sheet of
File:	C:\Users\.\Debug_connectors.SchDoc	Drawn By:

1

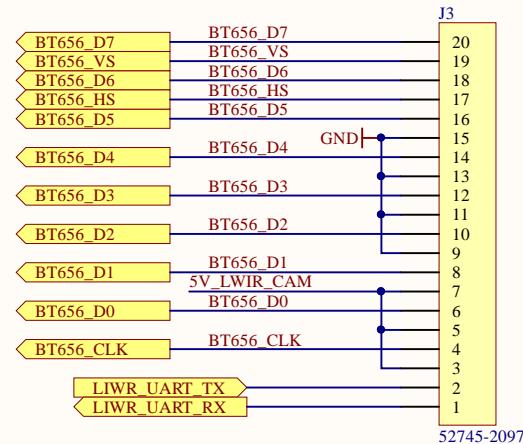
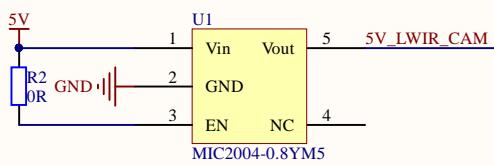
2

3

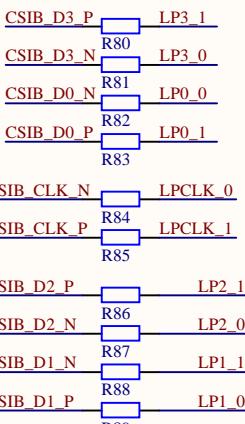
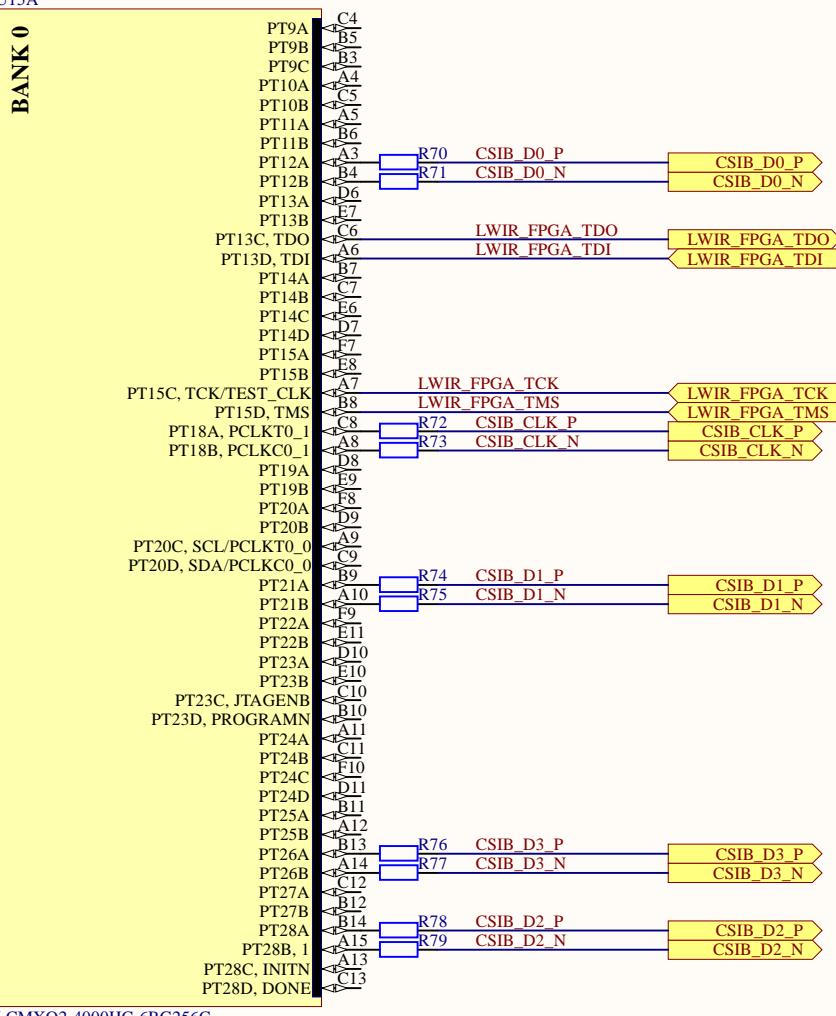
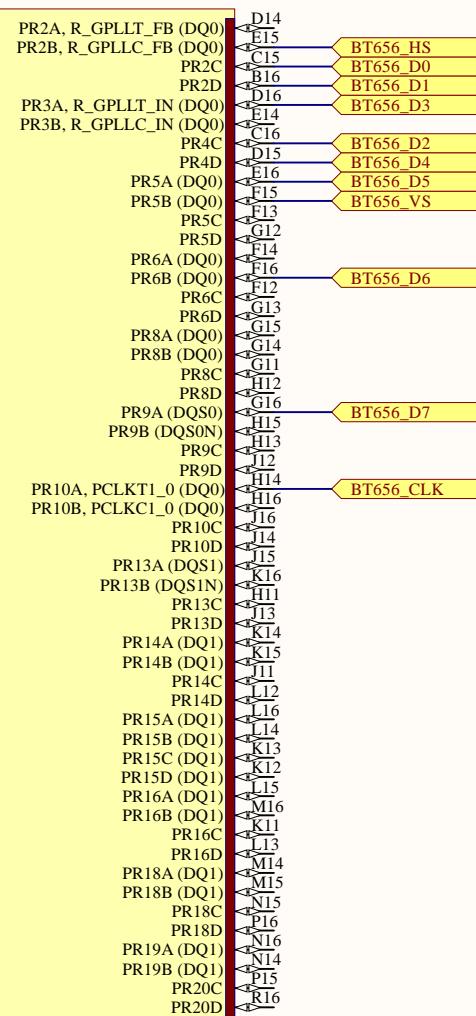
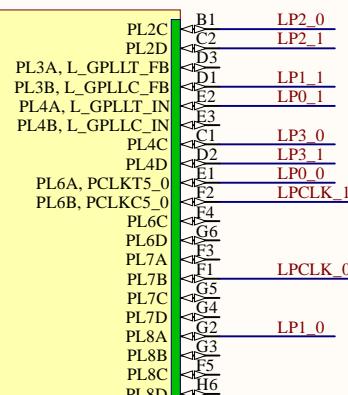
4



Title		
Size	Number	Revision
A4		
Date: 2017-12-23	Sheet of	
File: C:\Users\.\Ethernet.SchDoc		Drawn By:



Title		
Size	Number	Revision
A4		
Date: File:	2017-12-23 C:\Users\.\LWIR_Camera.SchDoc	Sheet of Drawn By:

U15A**U15B****U15F**

Title

Size
A4

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Revision

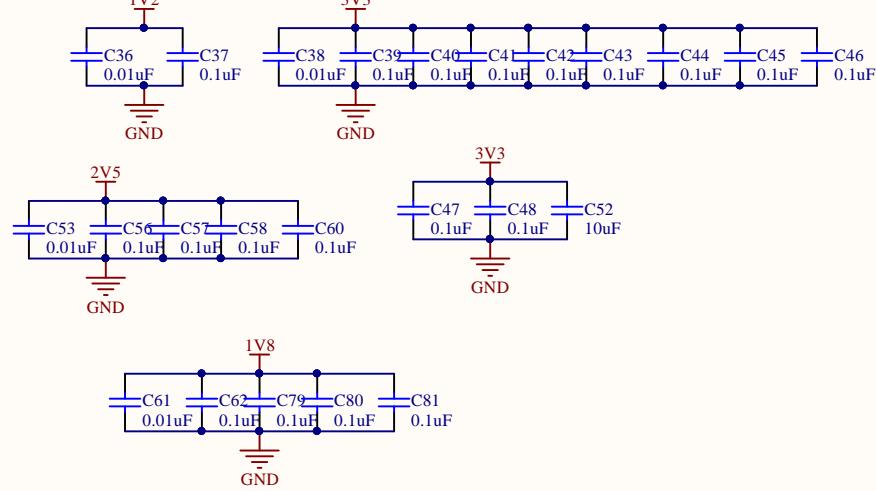
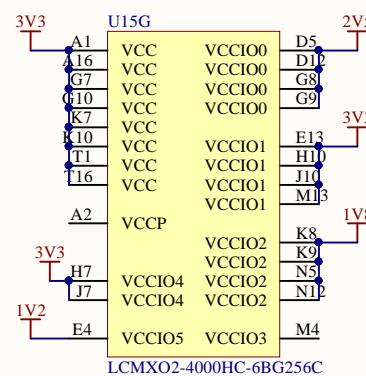
Date: 2017-12-23 Sheet of
File: C:\Users\.\LWIR_FPGA_CSI.SchDoc Drawn By:

1

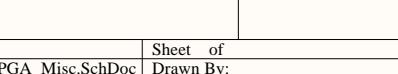
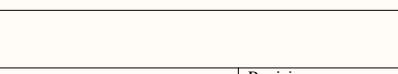
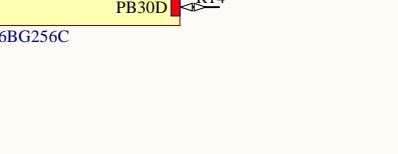
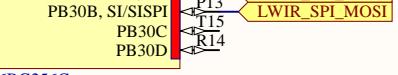
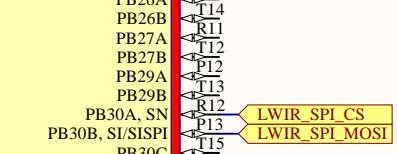
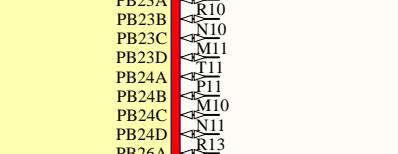
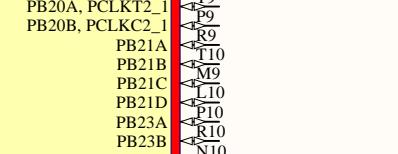
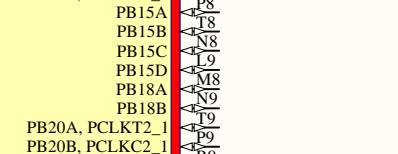
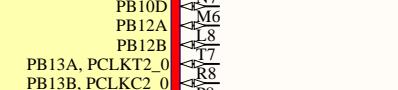
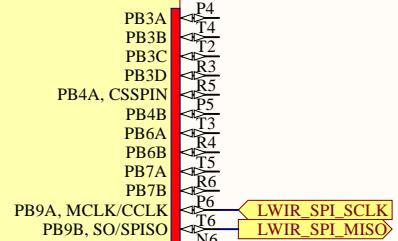
2

3

4



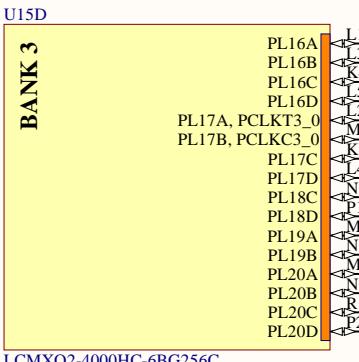
U15C

BANK 2

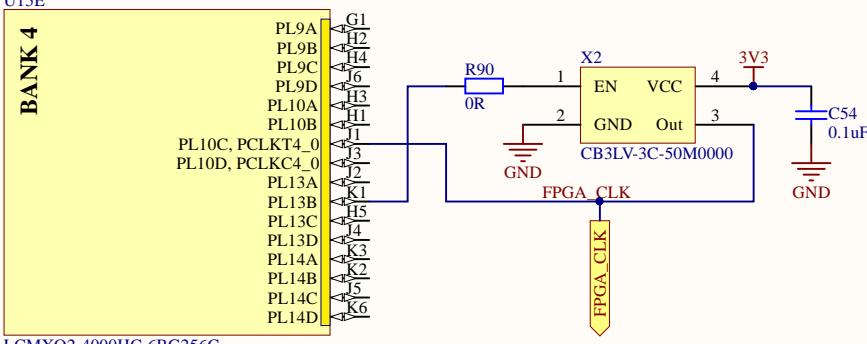
LWIR_SPI_SCLK
LWIR_SPI_MISO

LWIR_SPI_CS
LWIR_SPI_MOSI

C

BANK 3

LCMXO2-4000HC-6BG256C

BANK 4

LCMXO2-4000HC-6BG256C

Title

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File: C:\Users\.\LWIR_FPGA_Misc.SchDoc

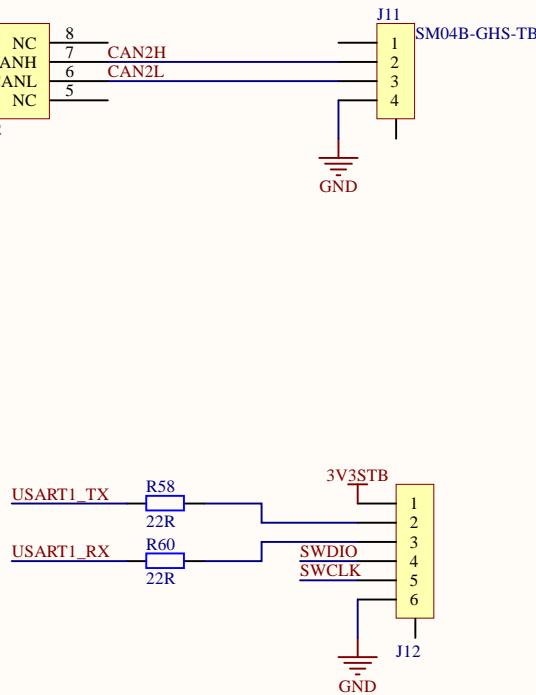
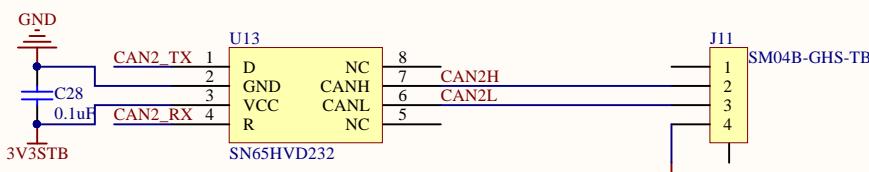
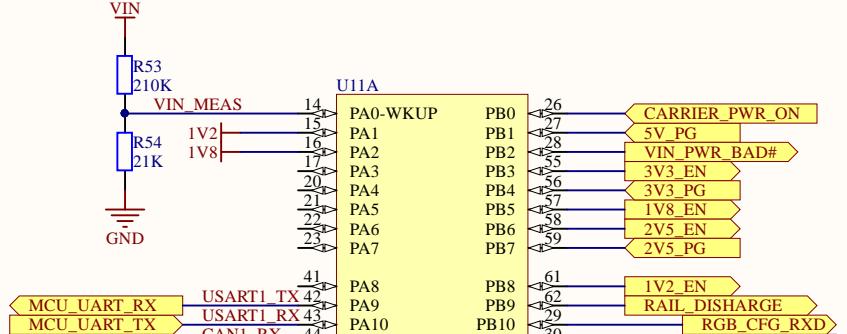
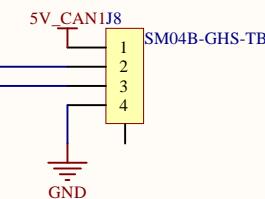
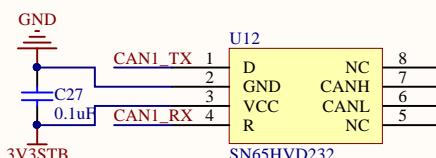
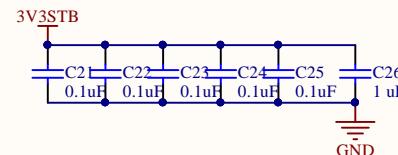
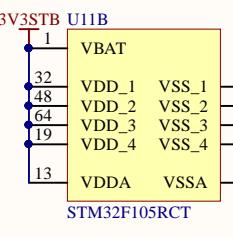
Drawn By:

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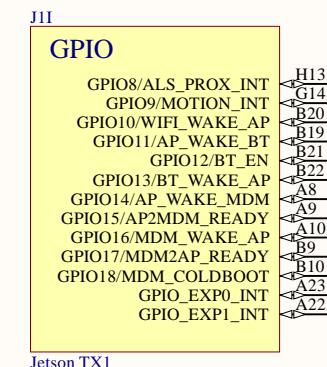
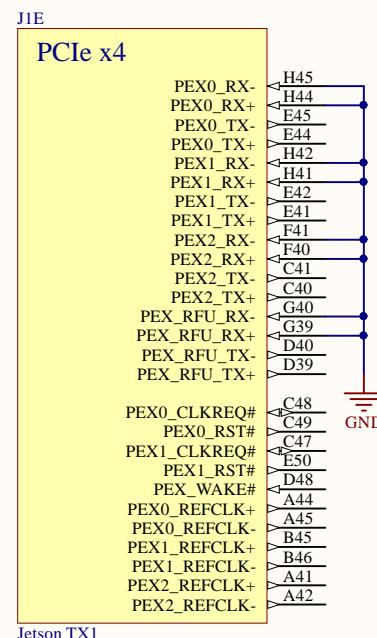
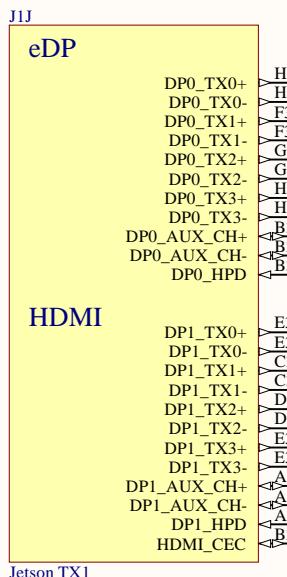
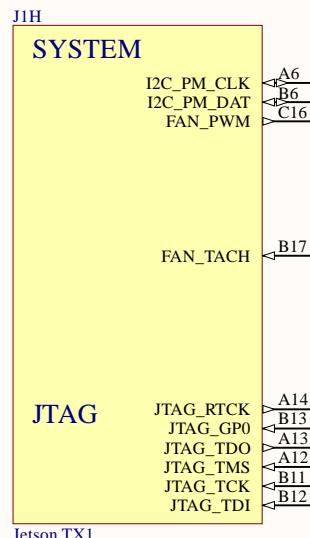
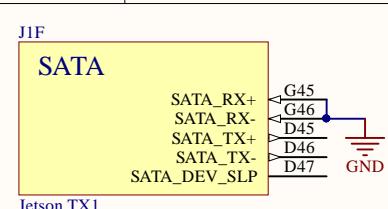
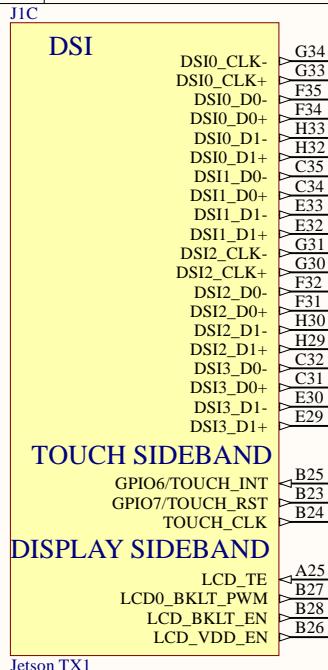
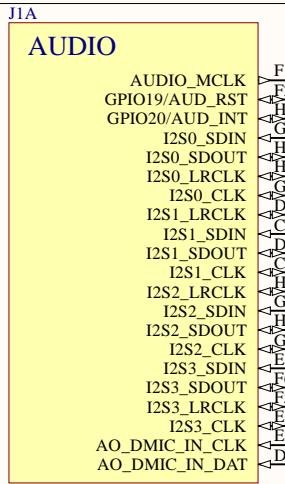
Title		
Size	Number	Revision
A4		
Date: 2017-12-23	Sheet of	
File: C:\Users\.\MCU_CAN.SchDoc		Drawn By:

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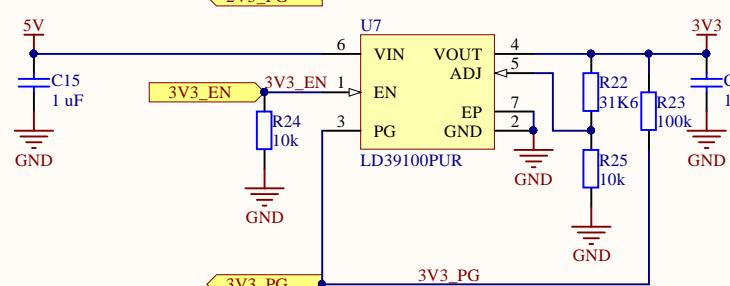
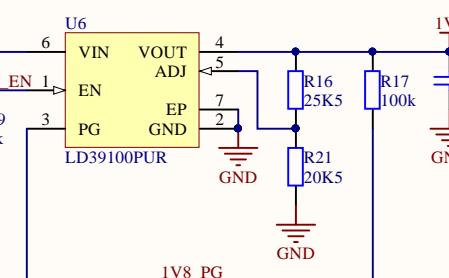
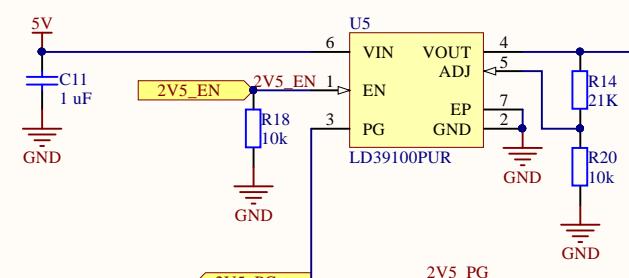
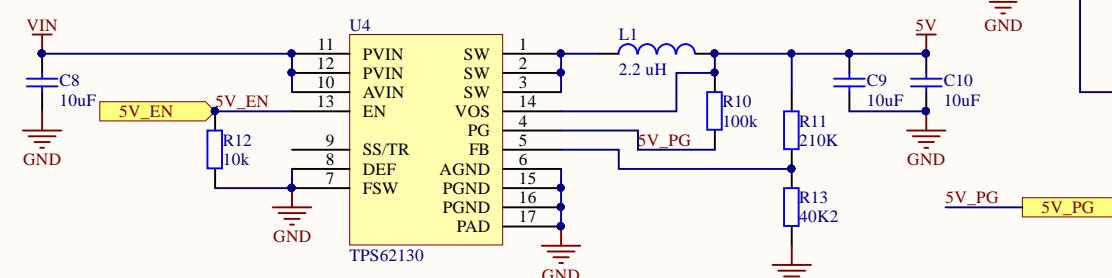
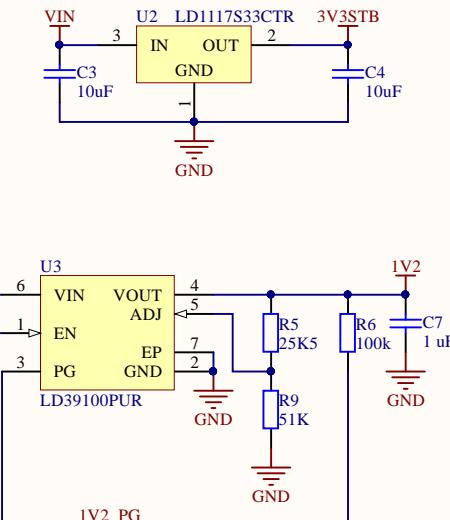
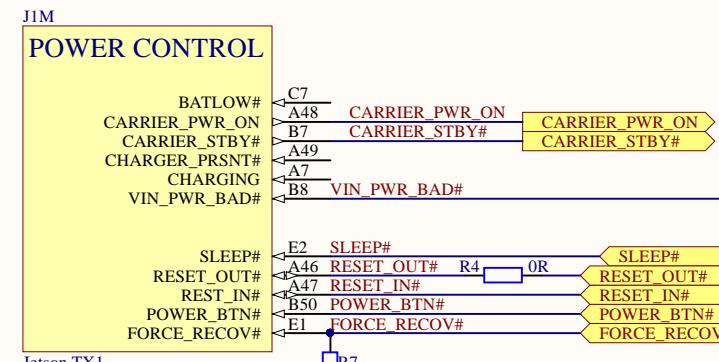
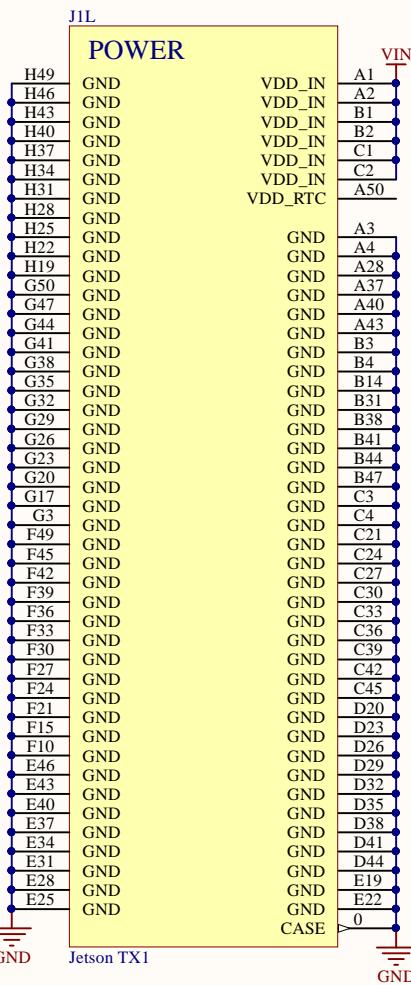
Title		
Size	Number	Revision
A4		
Date: 2017-12-23	Sheet of	
File: C:\Users\.\Not_Used.SchDoc		Drawn By:

1

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Title		
Size A4	Number	Revision
Date:	2017-12-23	Sheet of
File:	C:\Users\...\Power.SchDoc	Drawn By:

A

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B

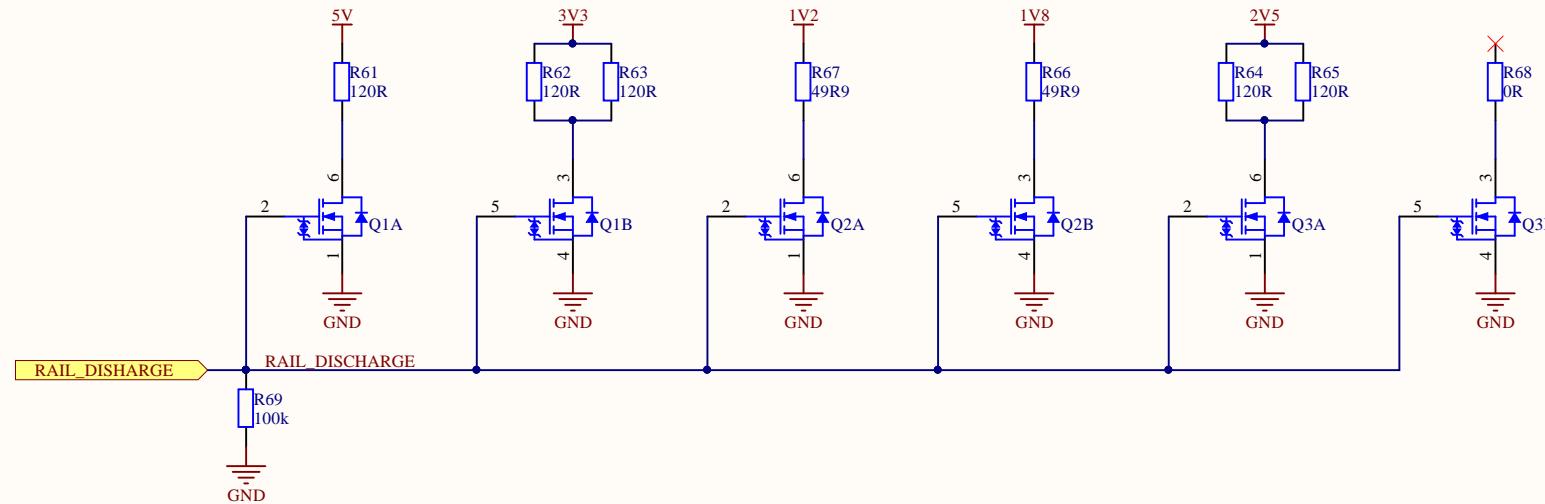
B

C

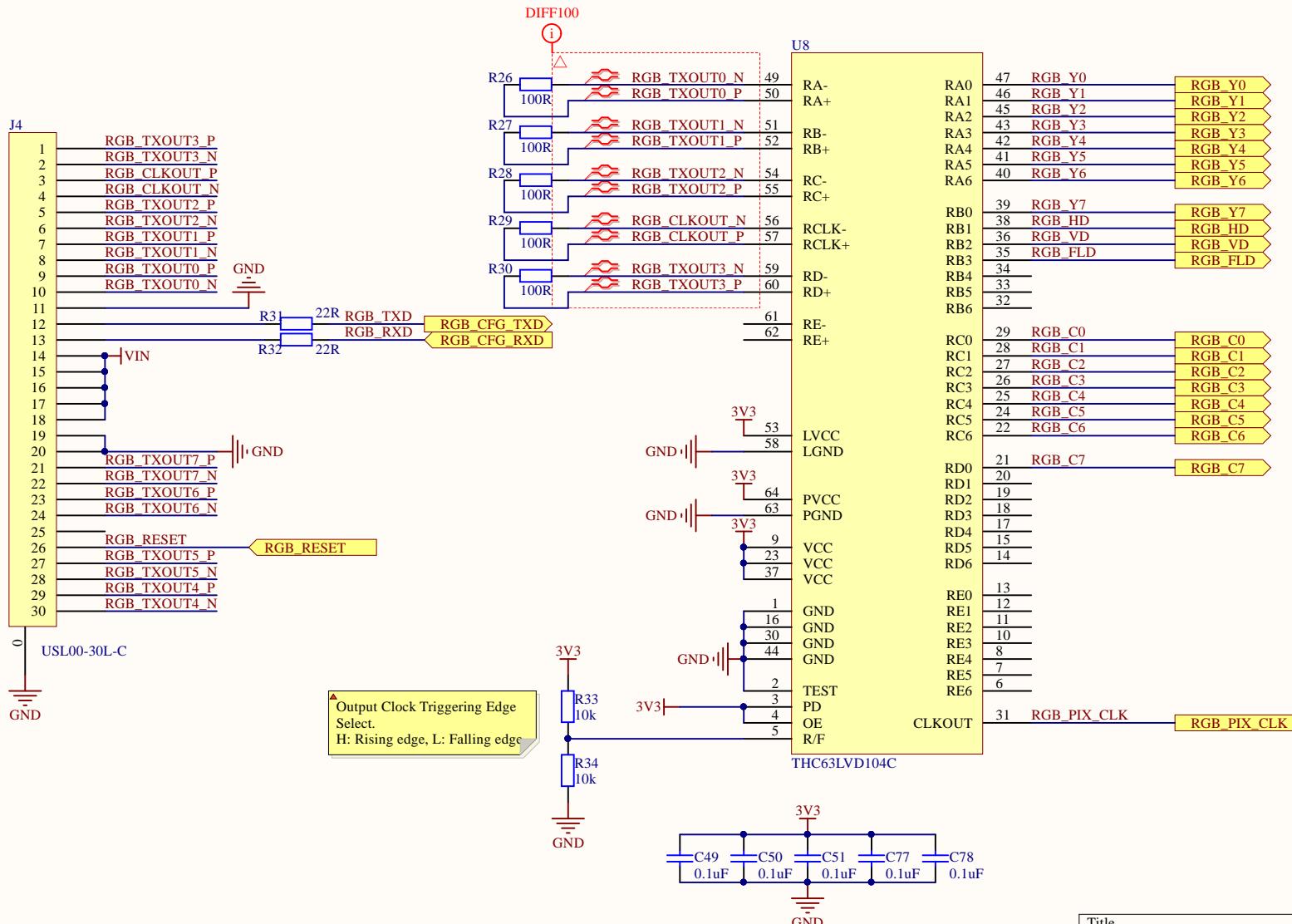
C

D

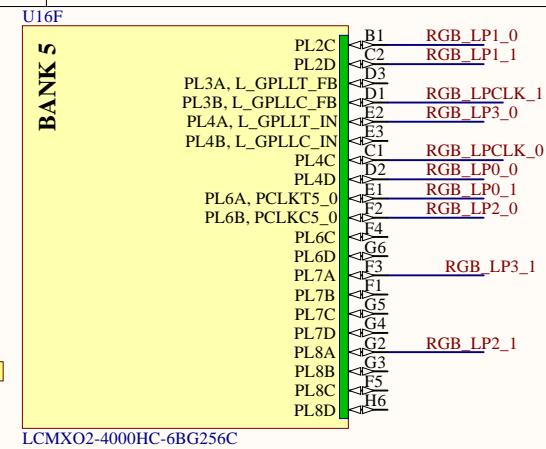
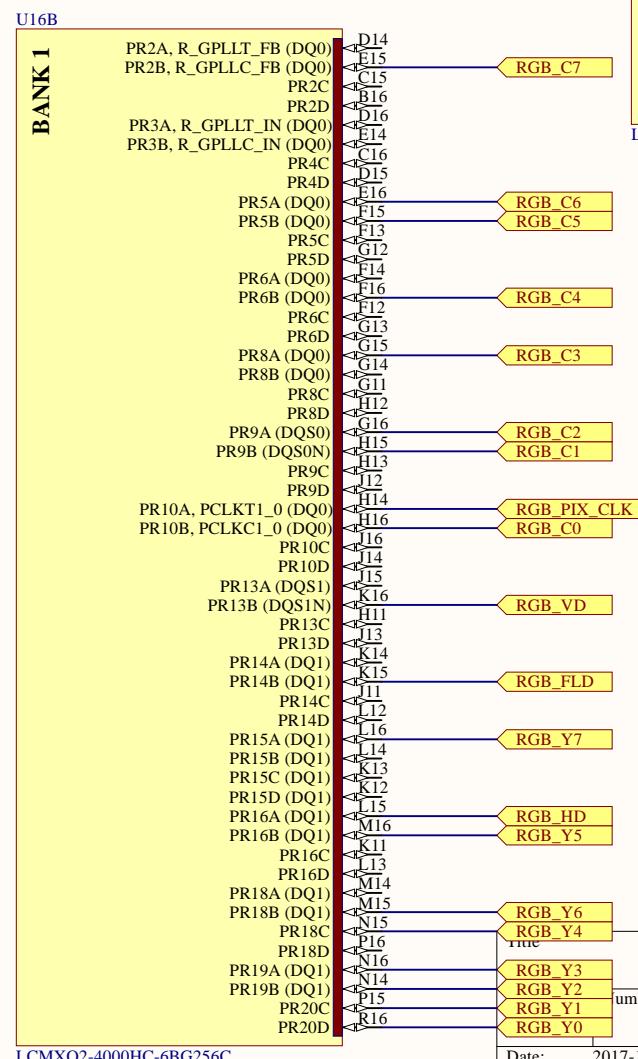
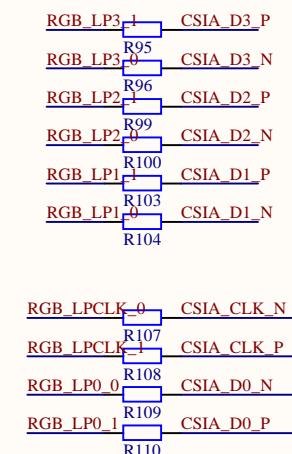
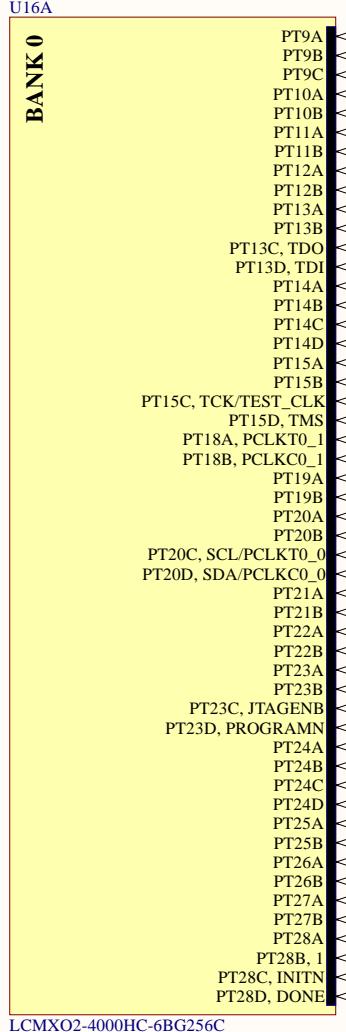
D



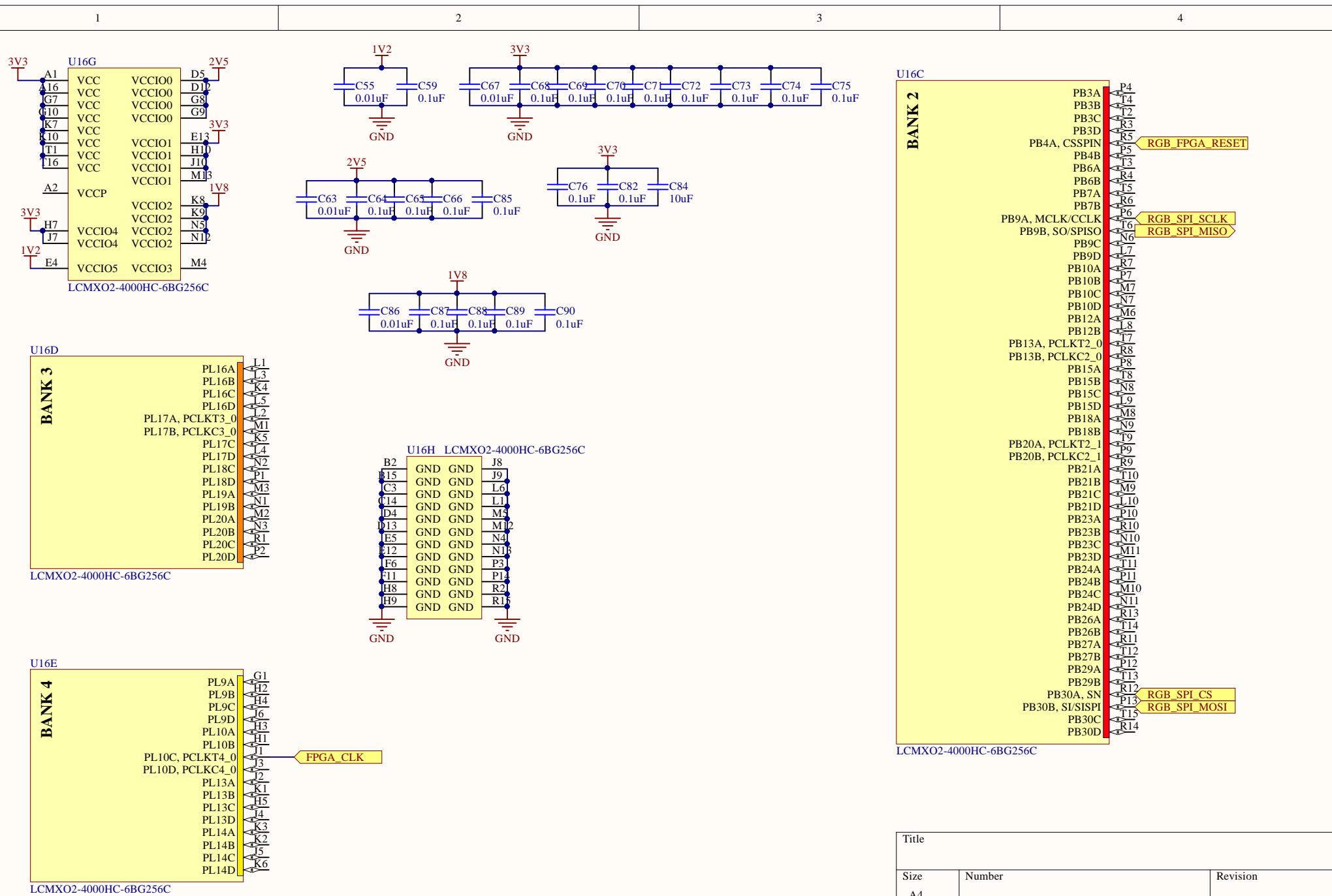
Title		
Size A4	Number	Revision
Date: 2017-12-23		Sheet of
File: C:\Users\.\Rail_discharge.SchDoc		Drawn By:



Title		
Size A4	Number	Revision
Date: 2017-12-23		Sheet of
File: C:\Users\...\RGB_Camera.SchDoc		Drawn By:



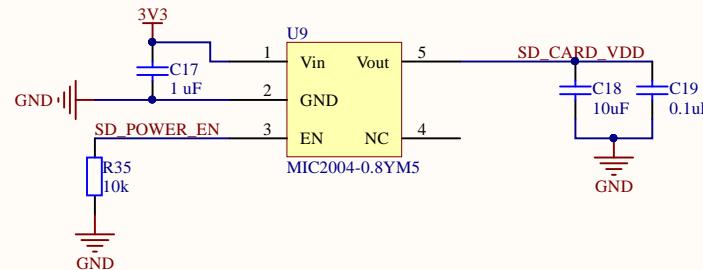
Date: 2017-12-23 Sheet of
File: C:\Users\.\RGB_FPGA_CSI.SchDoc Drawn By:



Title		
Size A4	Number	Revision
Date:	2017-12-23	Sheet of
File:	C:\Users\l\RGB_FPGA_Misc.SchDoc	Drawn By:

A

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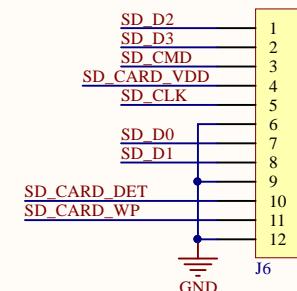
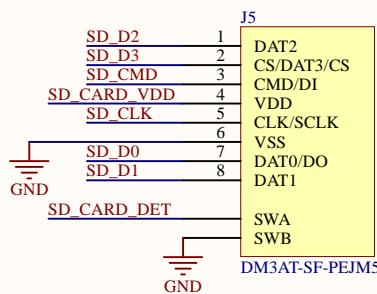
B

J1G

SD CARD

SDCARD_CLK	R36	OR	SD_CLK
SDCARD_CMD	R37	10R	SD_CMD
SDCARD_D0	R38	10R	SD_D0
SDCARD_D1	R39	10R	SD_D1
SDCARD_D2	R40	10R	SD_D2
SDCARD_D3	R41	10R	SD_D3
SDCARD_PWR_EN	H16		SD_POWER_EN
SDCARD_CD#	F17		SD_CARD_DET
SDCARD_WP	F20		SD_CARD_WP
SDIO_CLK	B30		
SDIO_CMD	B29		
SDIO_D0	B32		
SDIO_D1	A32		
SDIO_D2	A31		
SDIO_D3	A30		
SDIO_RST#	A29		

Jetson TX1



Title

Size

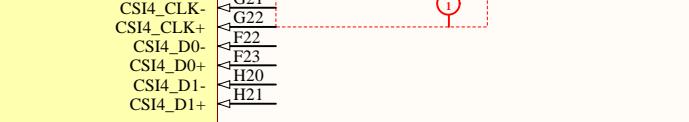
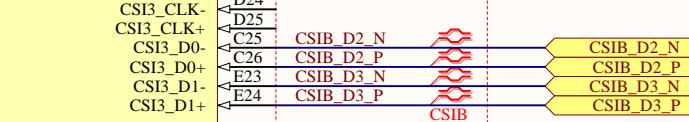
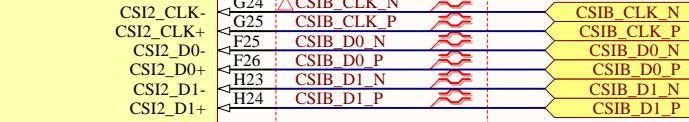
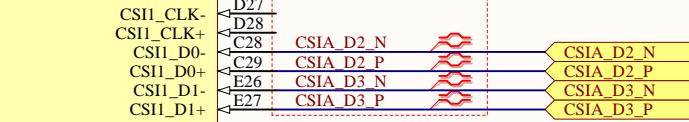
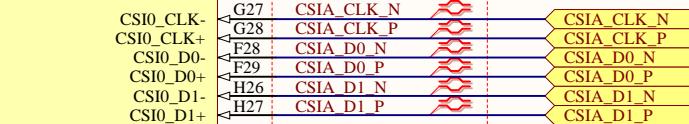
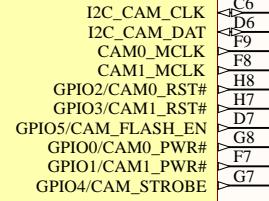
A4

Number

Revision

Date: 2017-12-23
File: C:\Users\.\SD_Card.SchDocSheet of
Drawn By:

J1D

CSI**CAM SIDEBAND**

Jetson TX1

Title

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File: C:\Users\.\Tegra_CSI.SchDoc

Drawn By:

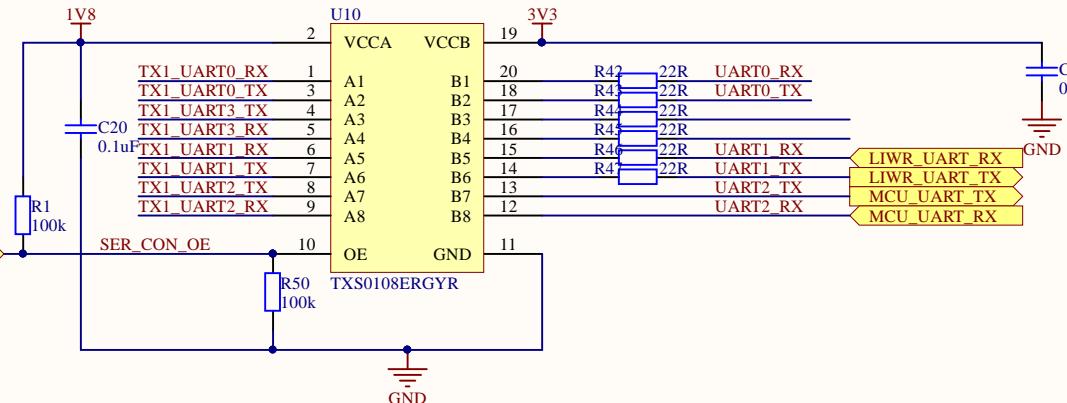
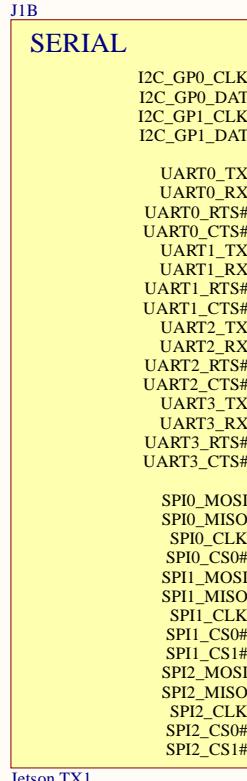
A

B

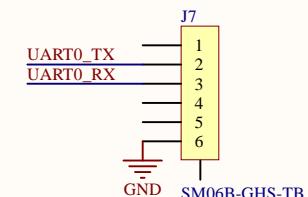
C

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Title		
Size	Number	Revision
A4		
Date: 2017-12-23	Sheet of	
File: C:\Users\.\Tegra_Serial.SchDoc	Drawn By:	

A

A

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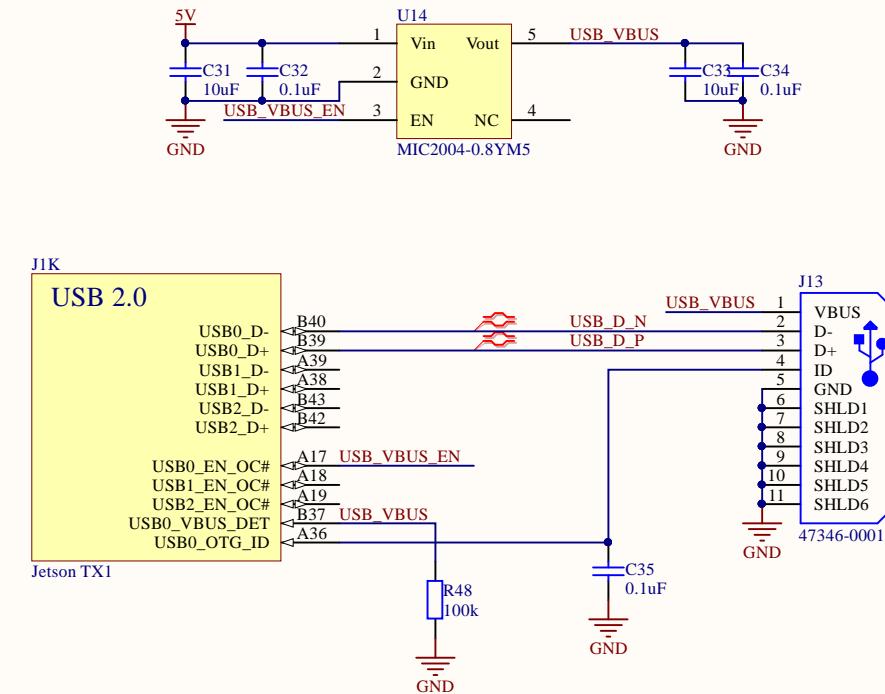
B

C

C

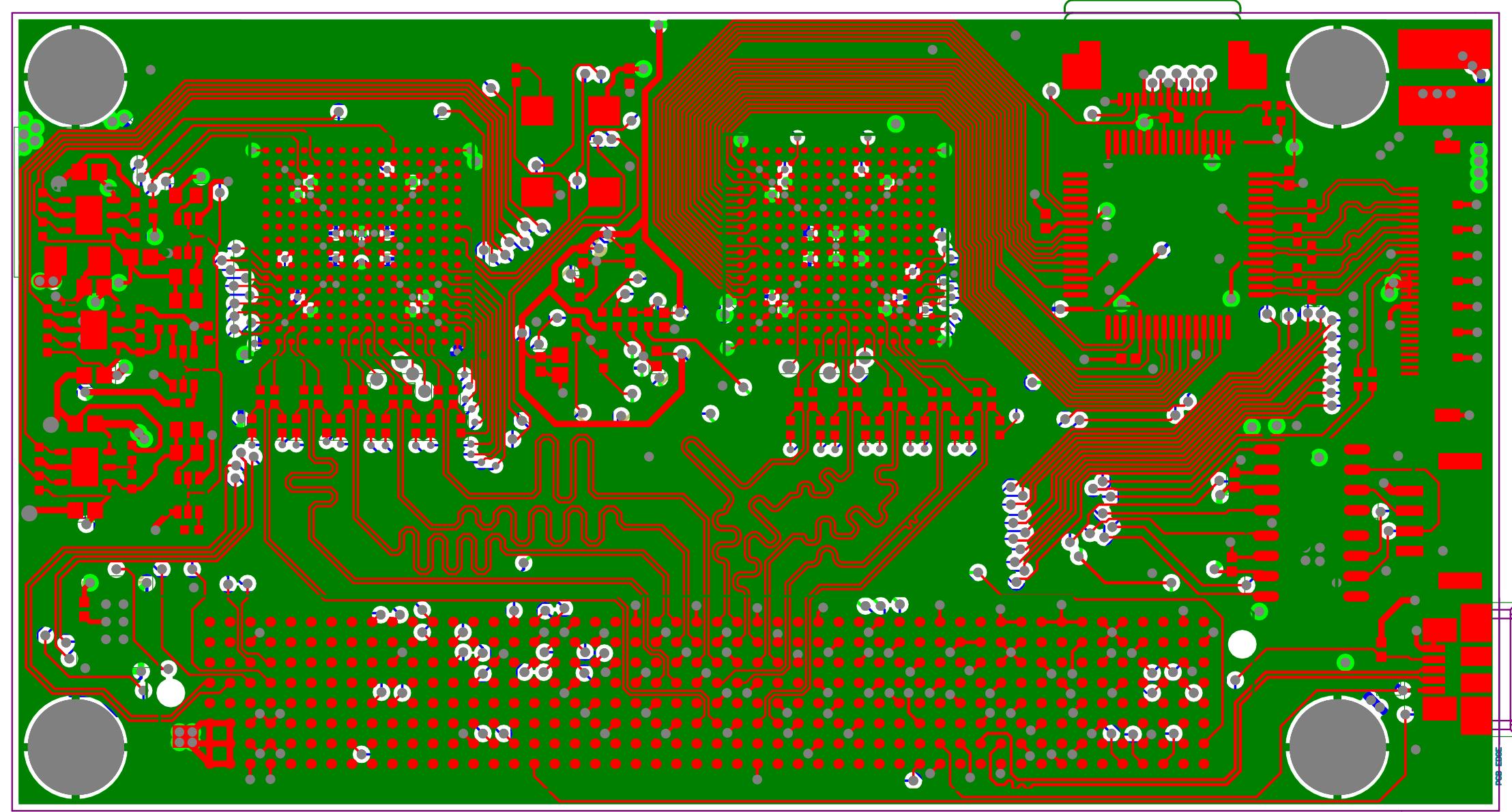
D

D



Title

Size	Number	Revision
A4		
Date:	2017-12-23	Sheet of
File:	C:\Users\...\USB.SchDoc	Drawn By:



Comment	Description	Designator	Footprint	LibRef	Quantity	
	CAP CER 0.1UF 16V XTR	C1, C19, C20, C21, C22, C23, C24, C25, C27, C28, C32, C34, C35, C37, C39, C40, C41, C42, C43, C45, C46, C47, C48, C49, C50, C51, C54, C56, C57, C58, C59, C60, C61, C62, C63, C65, C68, C69, C70, C71, C72, C73, C74, C75, C76, C77, C78, C79, C80, C81, C82, C83	0402C_M	GRM155R71C104KA8BD	57	
	CAP CER 0.1UF 16V XTR	C19, C28, C29, C90	0402C_M	GRM155R71C104KA8BD	1	
	CAP CER 0.1UF 50V XTR 4002	C2, C36, C38, C53, C55, C61, C63, C67, C68	0402C_M	GRM155R71H103KA8BD	9	
	CAP CER 1.0UF 100V 25V XTR 10%	C1, C2, C3, C4, C5, C6, C7, C8, C9, C10, C11, C12, C13, C14, C15, C16, C17, C26	0402C_M	TMK31AB7106KL-TD	2	
	CAP CER 10UF 6.3V XSR	C3, C8	1206_1.6_M	TMK31AB7106KL-TD	1	
	CAP CER 0.1UF 50V XTR 4003	C4, C9, C10, C18, C31, C32, C52, C84	0603C_M	GRM188R60106ME47D	8	
	CAP CER 1UF 10V XSR	C6, C7, C11, C12, C13, C14, C15, C16, C17, C26	0603C_M	GRM188R61A105KA61D	10	
	CAP Ceramic 27pF 50V C0G 5% SMD 0402 125°C	C29, C30	0402C_M	GRM155C1H270JA01D	2	
	400W Peak Power Zener Transient Voltage Suppressor, Unidirectional, 100V, 1A, PnP SMB, Tape and Reel	D1	DN455C-SMB-2-403A- 032_LV	CMP-1041-00394-1	1	
	403SFV350B/32-2	F1	0603R_0.45_M	0603SFV350B/32-2	1	
	Memory computing module	J1	Nvidia Jetson TX1	NVIDIA_Jetson_TX1	1	
	MO5B-GHS-TB(J)JSN	CONN HEADER GH SIDES APO5 1.25MM	J2	SM05B-GHS-TB	1	
	MO5B-GHS-TB(J)JSN	CONN HEADER GH SIDES APO5 1.25MM	J3	SM05B-GHS-TB(J)JSN	1	
	ONFI 2.0 TCF TOP 20PDS 0.50MM R/A	J4	52745-209	0527452097	1	
	USLO-30-C	J4	USLO-30-C	USLO-30-C	1	
	CONN MICRO SD CARD PUSH/PUSH A/J	J5	DM3AT-SF-PEJIM5	DM3AT-SF-PEJIM5	1	
	CONN FFC 10P TOPZOS 1.25MM	J6	52745-1297	0527451297	1	
	CONN HEADER GH SIDES APO5 1.25MM	J7	SM05B-GHS-TB	SM05B-GHS-TB(J)JSN	1	
	CONN HEADER GH SIDES APO5 1.25MM	J8, J11	SM04B-GHS-TB	SM04B-GHS-TB(J)JSN	2	
	CONN HEADER GH SIDES APO5 1.25MM	J9	SM04B-SRSS-SB	SM04B-SRSS-TB(J)JSN	1	
	Micro-USB B Receptacle with Flange, Type B 50 degC, 5-Pin SMD, RoHS, Tape and Reel	J13	MOLX-47346-0001-5_V	CMP-2000-05246-1	1	
7346-0001	Fixed Inductors Power Inductor 2.2 uH 20 % B A	L1	XFL4020_M	XFL4020-222MEC	1	
	DUAL N-CHANNEL 20-V (D-S) 175°C	O1, O2, O3	SOT236_M	SG1922EEH-T1_G3	3	
	RES SMD 100 OHM %5 1/16W 0402	R1, R3, R6, R10, R15, R11, R23, R48, R50, R55, R56, R69	0402R_M	RCW0402100KINED	12	
	Generic resistor	R2, R4, R7, R36, R68, R95	0402R_M	Resistor	6	
	RES SMD 25 K5 OHM 1/16W 0402	R5, R16	0402R_M	RCW040225K5FED	2	
	RES SMD 40K 10% OHM 1/16W 0402	R8, R12, R18, R19, R20, R21, R22, R31, R34, R35,	0402R_M	RCW040210K0FKED	12	
	RES SMD 51 OHM 1% 1/16W 0402	R9	0402R_M	RCW040251K0FKED	1	
	RCW0402210KFYED	RES SMD 210K 10% OHM 1/16W 0402	R11, R53	0402R_M	RCW0402210KFYED	2
	RCW0402402K0FKED	RES SMD 40K 2.0% OHM 1/16W 0402	R13	0402R_M	RCW0402402K0FKED	1
	RCW0402402K0FKED	RES SMD 21K 10% OHM 1/16W 0402	R14, R54	0402R_M	RCW040221K0FKED	2
	RCW040220K5K0FKED	RES SMD 31K 0.1% OHM 1/16W 0402	R21	0402R_M	RCW040220K5K0FKED	1
	RCW040220K5K0FKED	RES SMD 31K 0.1% OHM 1/16W 0402	R22	0402R_M	RCW040231K4FKED	1
	RCW0402100RFKED	RES SMD 100% OHM 1/16W 0402	R24, R27, R28, R29, R30	0402R_M	RCW0402100RFKED	5
	RCW040222R0UNED	R31, R32, R42, R43, R44, R45, R46, R47, R57, R58, R59, R60	0402R_M	RCW040222R0UNED	12	
	RCW040210R0FKED	RES SMD 22 OHM 5% 1/16W 0402	R41, R42, R43, R44	0402R_M	RCW040210R0FKED	5
	RCW040210R0FKED	RES SMD 10% OHM 1/16W 0402	R45, R46, R47, R48	0402R_M	RCW040210R0FKED	5
	RCW040210R0FKED	RES SMD 120% 5% 100mW	R61, R62, R63, R64, R65	0603R_0.45_M	RCW0603120RNED	5
	RES 6003 120% 5% 100mW	R66, R67, R80, R81, R82, R83, R84, R85, R86, R87, R88, R89, R95, R96, R99, R100, R101, R104, R107, R108, R109, R110	0402R_M	RCW0603120RNED	22	
	RES SMD 49 9 OHM 1% 1/16W 0402	R70, R71, R72, R73, R74, R75, R76, R77, R78, R79, R80, R81, R93, R94, R97, R98, R101, R102, R105, R106	0402R_M	RCW0402330RFKED	20	
	IC DISTRIBUTION SW LQFP1023-5	U1, U14	SOT23_55_M	MIC2004-0.8VMS	2	
	IC REGULATOR 3.0 V 3A SOT223	U2	SOT-223_L	LD1117533CTR	1	
	1 A Low Quiescent Current Low Noise Voltage Regulator Adjustable from 0.8V to 8.0V Pin VF DN/PN, Tape and Reel	U3, U5, U6, U7	DN64D_M	CMP-0244-00287-1	4	
D39100PUR	REG, DFN BLOCK ADJ 3A SYN 1ACIN	U4	VQFN16_16_L	TPS2120RGT	1	
PS62120	IC DESERIALIZER SNGL LVDS 64x16P	U8	THC_LOFP64_L	THCA63LV104C	1	
	IC DISTRIBUTOR SNGL U9	U9	SOI23_55_M	MIC2004-0.8VMS	1	
	IC REGULATOR 3.0 V 3A SOT223	U11	VQFN20	TXS1008ERGYR	1	
	TM1212105RCT	U11	LOFP44_LD	STM32F105RCT	1	
	IC 3.3V CAN TXR 8- SOIC	U12, U13	SOIC8_L	SN65HVD23QDRO1	2	
	IC 100MHz HIGH Performance CRWL with 4320 LUTs, Voltage Regulator, 2.5V/3.3V, 256-pin QFN, 1.6mm x 1.6mm, Commercial Grade, Halogen Free (RoHS)	U15, U16	caBGA256	CMP-0111-00181-1	2	
X0068ANL	10/100 Mbit LAN Magentic Module, .40 to 85 degC, 12-pin SMD, RoHS, Tube	U17	PULS-XH0068ANL_V	CMP-2000-0430-1	1	
	Crystals 16MHz 10ppm 9pf -20...+75°C	X1	TSX-3225_H	TSX-3225_6.000MHz092	1	
	BB3V-3C-50M0000D OSC 3.0 50MHz HX	X2	CB-3LV-3C-50M0000	CB-3LV-3C-50M0000	1	