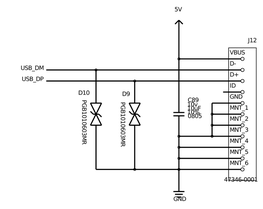
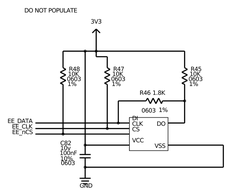
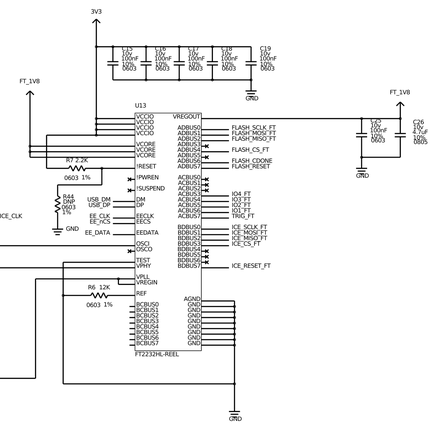
USB CONNECTOR

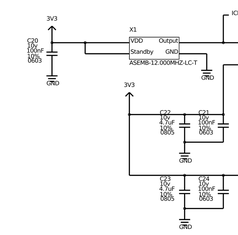


USB to I2C and SPI

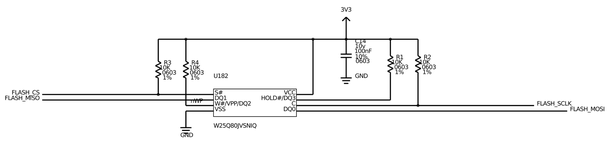
DO NOT POPULATE



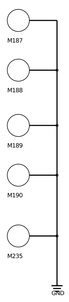




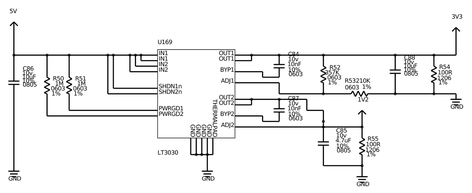
FLASH FPGA PROG



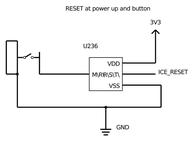
MOUNTING HOLES



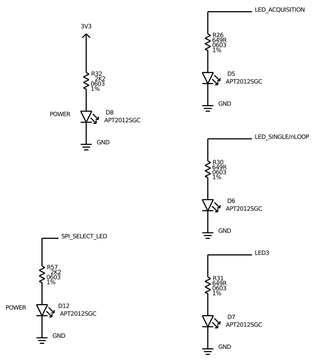
LOCAL POWER SUPPLIES 3V3 and 1V2



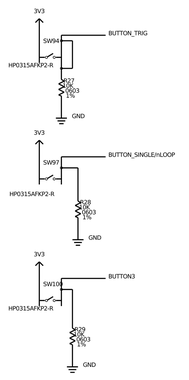
RESET at power up and button



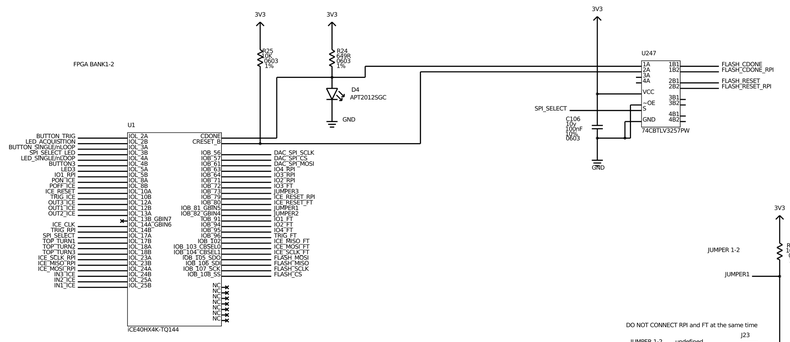
LED STATUS

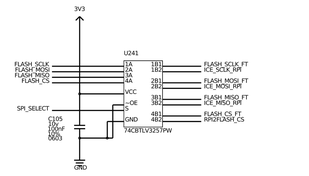


BUTTONS

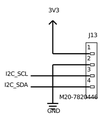


FPGA BANK1-2

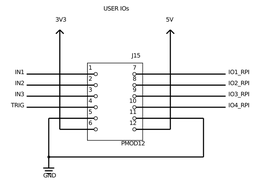




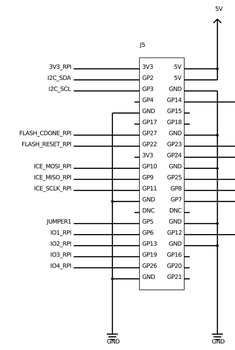
OLED SCREEN

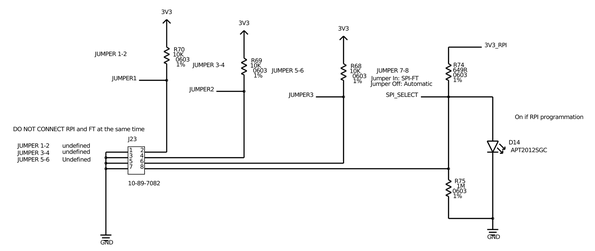


USER IOs

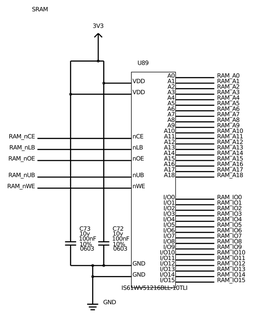


RAPSBERRY Header

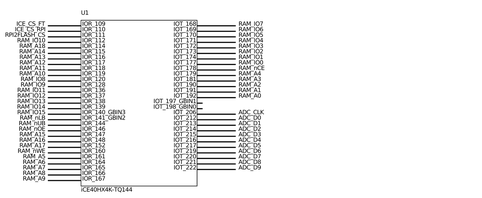




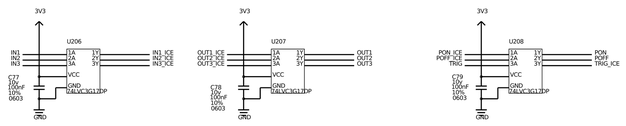
SRAM



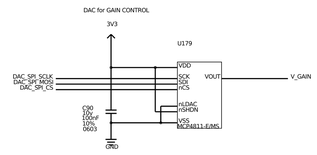
FPGA BANK3-4



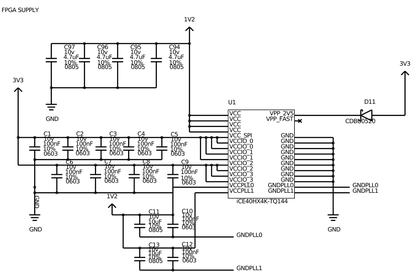
OUTSIDE IOs BUFFERS



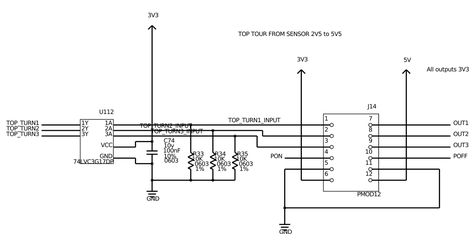
DAC for GAIN CONTROL



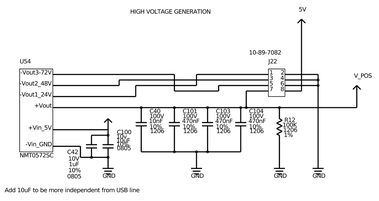
FPGA SUPPLY



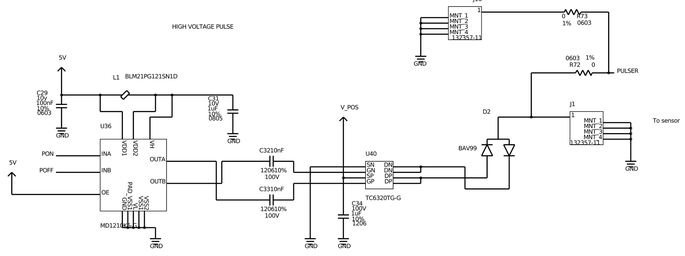
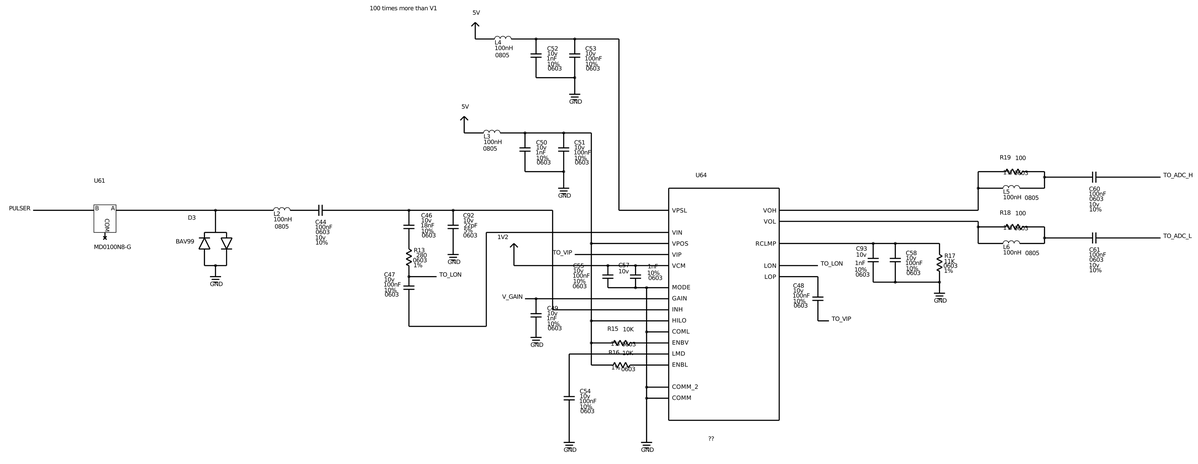
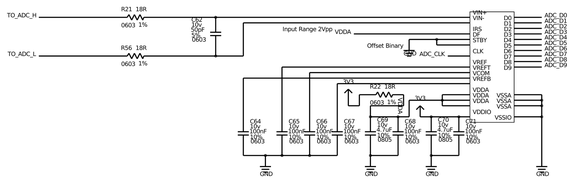
TOP TOUR FROM SENSOR 2V5 to 5V5



HIGH VOLTAGE GENERATION



HIGH VOLTAGE PULSE



100 times more than V1