



# The 3<sup>rd</sup> International Workshop on Memory and Storage Computing

Oct. 13th, 2022

## Call for Papers

Co-located with ESWEEK 2022 (Hybrid, Shanghai)

<https://msc-esweek.github.io/>

### **Aims and Scope:**

Memory and storage have been developed during last decades. However, the data transfer cost between CPU and storage/memory becomes the critical challenge for the advanced systems. Storage and memory computing provides a new opportunity to solve this issue by adding computing function beside storage or memory. In this workshop, we are not only interested in the advanced storage and memory computing technologies, but also pay a special attention to the advanced storage and memory technologies. The workshop will bring together scientists and engineers from industry and academia who are working on storage and memory computing architectures and systems.

### **Topics of Interest:**

The topics of the workshop will include but not limit to the following topics:

- 1) Storage computing architectures
- 2) Memory computing architectures
- 3) Systems for memory and computing
- 4) Systems for storage and computing
- 5) State-of-the-art storage technologies
- 6) State-of-the-art memory technologies
- 7) FPGA enabled in-storage computing
- 8) Intermittent computing/system
- 9) Design-space exploration for memory and storage systems
- 10) Cross-layer design methodologies for memory hierarchy
- 11) Compiler and OS optimization for emerging memory and storage
- 12) Modeling, simulation and analysis for memory and storage
- 13) Storage and memory design issues for emerging application scenarios-
- 14) Unified memory and storage
- 15) Non-volatile computing systems
- 16) In-memory computing near-memory computing their applications
- 17) In-storage computing and its applications

### **Submission Guidelines:**

All papers must be submitted electronically in PDF format. The submitted manuscripts must describe original work not previously published and not concurrently submitted elsewhere. Submissions must be no more than 6 pages in the IEEE conference proceedings format (two-column, single-space). Each paper is peer-reviewed by the program committee members, and the authors are notified of either "accepted for a presentation", or "rejected". A presentation will be given a 30-mins slot (including Q&As). The authors should submit their papers through the submission web page at: <https://easychair.org/conferences/?conf=msc2022>.

**Selected papers, after further revisions, will be considered for publication in special issues of selected journals.** Possible journals include ACM Transactions on Embedded Computing Systems and Journal of System and Architecture -.

**About Registration:**

If you have registered the main conference, you can attend the workshop for free. Otherwise, you can attend the workshop by invitation for free.

**Important Dates:**

Submission Deadline: 20<sup>th</sup> Aug 2022, UTC+9

Acceptance Notification: 20<sup>th</sup> Sept 2022

Final Paper Submission: 7<sup>th</sup> Oct2022

**Organizers:**

*Liang Shi*, Professor, East China Normal University, China

*Weichen Liu*, Nanyang Assistant Professor, Nanyang Technological University, Singapore

*Yuan-Hao Chang*, Research Fellow, Academia Sinica, Taiwan

**Program Chair:**

*Mengying Zhao*, Shandong University, China

**Web Chair:**

*Yujiong Liang*, East China Normal University, China