|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Rows (15 bits) | Higher Column  Bits (8 bits) | Banks (2 bits) | Bank Groups (2 bits) | Lower Column  Bits (3 bits) | Byte Select  (3 bits) |

In DIMM 8GB PC4 25600 clock rate is

MHz

Page size is 2KB

The internal access width is 8, therefore number of columns is 2^11 = 2K.

Number of column bits = 11

There are 8 chips of x8 device (as per the description) as the width of DIMM module is 64 bits.

Number of Banks = 4 therefore 2 address bits for banks.

Number of Bank Groups = 4 therefore 2 address bits for bank groups.