

Session 2.1

# SDLC Life Cycle – V Process Model

# AN INITIATIVE BY

**UNICAL ACADEMY** 

#### Introduction



#### What will be covered in this session?

Use cases and traceability

Part 4: Hands-on Sessions (100 h)

- Test data and scenarios
- We will start Module 2 with a detailed walkthrough of SDLC (V Process) workflow and key documents for each phase otherwise otherwise otherwise.

- After that, a detailed walkthrough of Loan Management System (hands-on user Web Driver, Locators, experience feel)
- Study of the use cases, test data, test scenarios for Loan Management System
- Part 2: CHands-on preparation of Test cases, test data, and manual testing of the
  - Tyapplication, and report generation
  - CoThe dast-section of Module 2 covers various types of Testing from practical
  - Maspedesting Test cases, Data, Scenarios etc.
  - Case-studies and Scenarios

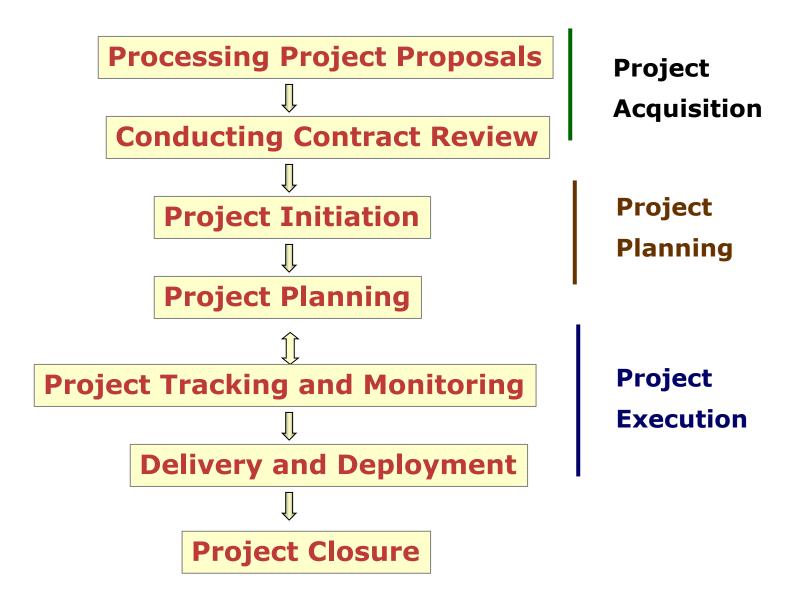
#### Part 1: The Basics (20 hours)

- Organization& its working
- SDLC & STLC Overview
- Basics of OOPS, Database Java essentials for Testing
- Overview on few Testing roles **Job Descriptions**

Let's go!!!

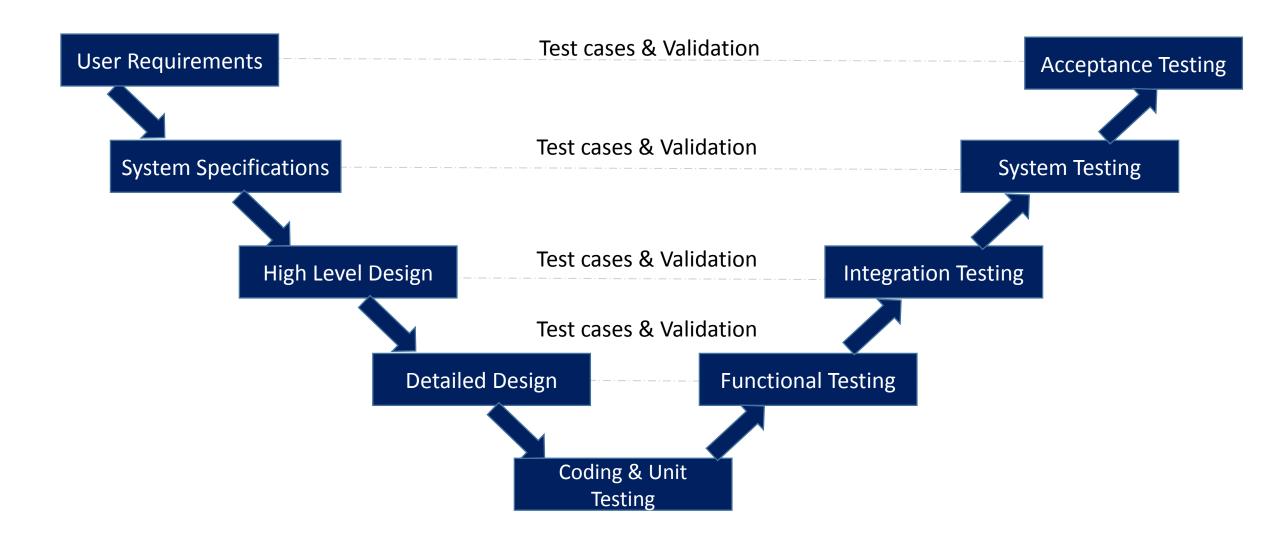
### **High-level workflow**





#### SDLC - 'a V Process' Model





#### **User Requirements Phase**



Entry	Customer's requirements/Voice Of Customer*/Contract
Inputs	Request for Proposal
	Gather customer needs
	Collect customer needs using VOC Collection Plan
	Prepare VOC Collection Plan (Refer VOC Collection Plan template)
	Prioritize customer needs using Affinity diagram ( ReferAffinity Diagram template)
	Document user requirements (URD template)
	Prepare draft Acceptance Test Plan (Refer ATP template) and draft Acceptance Test Cases (Refer ATR template). Reviewed, approved
	and baselined ATP and ATR shall be ready before the Acceptance Testing begins.
	Prepare Traceability Matrix (Refer Traceability Matrix template)
Tasks	Conduct Tollgate Review (Refer Tollgate Review Checklist for Initiate)
	URD
	Traceability Matrix
Verification	VOC Collection Plan
/ Validation	Affinity Diagram
	URD
	ATP and ATR
	Updated Traceability Matrix
	Tollgate Review Reports
	Updated Risk Assessment Sheet
Outputs	
Exit	Review and Approval of URD

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# **System Specifications Phase**

Entry	Baselined URD
	URD
	Project Charter
Inputs	Tollgate Review Report
	Map Customer Needs to CTQ's & prioritize Customer needs using HOQ – (Refer HOQ – 1 template)
	Document unstable requirements (Refer URD template)
	Prepare System Test Plan (Refer STP template) to validate the functionality of the product.
	Document System Test Report (Refer STR Template) with the baselined SRS as the reference.
	Update Traceability Matrix (Refer Traceability Matrix)
Tasks	Conduct Tollgate review (Refer Tollgate Review Checklist for initiate)
	SRS
	STP & STR
Verification / Validation	Traceability Matrix
	SRS
	SRR
	Draft STP/STR
	Updated Traceability Matrix
	Tollgate Review Reports
Outputs	Updated Risk Assessment Sheet
Exit	Approval of SRS and System Specifications

# **High Level Design Phase**



Entry	Approved SRS, Completed / draft STPs and Completed / draft STRs
	Baselined SRS
Inputs	Tollgate Review Report
	Prepare High Level Design Document (ReferHLDD template)
	Develop alternate concepts and select the best concept
	Perform function analysis
	Map CTQ's to high-level functions using HOQ – 2 (ReferHOQ – 2 template)
	Map high level functions to High Level Design elements using HOQ – 3 (ReferHOQ – 3 template)
	Prepare of HLD Scorecards (ReferHLD scorecard template)
	Prepare Integration Test Plans (Refer ITP template)
	Prepare Integration Test Cases (Refer ITR template) against the baselined input documents.
	Update Traceability Matrix
Tasks	Conduct Tollgate review (Refer Tollgate Review Checklist for Design)
	HLDs
Verification /	ITP & ITR
Validation	Traceability Matrix
	HLDs
	ITP & ITR
	Updated Traceability Matrix
Outputs	Tollgate Review Report
Exit	Approval of HLDs

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Entry	Approval of HLDDs, completed/draft ITP and completed/draft ITRs	
	Baselined HLDDs	
Inputs	Tollgate Review Report	
Tasks	Prepare DDDs (ReferDDD template) Prepare Unit Test Plan (ReferFTP template) and Unit Test Cases (ReferFTR template) against the baselined input documents Identify critical design elements Prepare Integrated Design Document (Refer Integrated Design Document template) Map the high-level design elements to Low Level Design elements using HOQ – 4 (ReferHOQ – 4 template) Prepare LLD scorecard (Refer LLD Scorecard template) Prepare of HLD-LLD traceability matrix (ReferHLD-LLD Traceability Matrix form) Update Traceability Matrix (Refer Traceability Matrix template) Conduct Tollgate review (Refer Tollgate Review Checklist for Design)	
Idaka	DDDs, IDDs	
	Traceability Matrix	
Verification /	FTPs and FTRs	
Validation	LLD Scorecard	
	DDD	
	DDD, FTP and FTC	
	Traceability Matrix	
Outputs	Tollgate Review Reports	
Exit	Approval of DDDs	

# **Coding & Unit Testing Phase**



Entry	Approved DDDs
	Baselined DDDs, UTP, and UTRs
	Coding Standards
Inputs	Tollgate Review Reports
	Code according to the coding standards and DDDs
	Version control of the Source Code
	Revisit Unit Test Plan's (Refer Unit Test Plan template)
	Unit test Reports (Refer Unit Test Report template)
	Perform Unit Testing
	Update Traceability Matrix (Refer Traceability Matrix template)
Tasks	Conduct Tollgate Review (Refer Tollgate Review Checklist for Testing)
	Code Review/Inspection against programming standards and DDR
	Review of UTR against DDD
	Traceability Matrix
Verification / Validation	Testing Review
	Source Code Code review report (CRR) (Refer Code Review Report) Unit Test Report (UTR) Test Results Defect Consolidation Log (Refer Issue Log template) Updated Traceability Matrix
Outputs	Tollgate Review Reports
Exit	Unit tested and baselined Source code



# **Functional Testing Phase**

Entry	Unit tested and base lined Source Code
	Unit tested and baselined source code
	Reviewed, approved and baselined FTP, FTRs
	Defect Tracking Form, Defect Consolidation Log
Inputs	Tollgate Review Report
	Perform Integration Testing
	Revisit FTP (Refer Functional Test Plan template), FTR (Refer Functional Test Report template)
	Update Traceability Matrix (Refer Traceability Matrix template)
Tasks	Conduct Tollgate review (Refer Tollgate Review Checklist for Testing
Verification / Validation	Functional Test Reports (FTRs) against LLDDs
	Functional Test Reports (FTRs)
	Test Results
	Defect Consolidation Log (Refer Issue Log template)
	Updated Traceability Matrix
Outputs	Tollgate Review Reports
Exit	Functional Tested and base lined source code



# **Integration Testing Phase**

Entry	Functional tested and base lined Source Code
	Unit tested and baselined source code
	Reviewed, approved and baselined ITP, ITRs
	Defect Tracking Form, Defect Consolidation Log
Inputs	Tollgate Review Report
	Perform Integration Testing
	Revisit ITP (Refer Integration Test Plan template), ITR (Refer Integration Test Report template)
	Update Traceability Matrix (Refer Traceability Matrix template)
Tasks	Conduct Tollgate review (Refer Tollgate Review Checklist for Testing
Verification /	
Validation	Integration Test Reports (ITRs) against HLDDs
	Integration Test Reports (ITRs)
	Test Results
	Defect Consolidation Log (Refer Issue Log template)
	Updated Traceability Matrix
Outputs	Tollgate Review Reports
Exit	Integrated, Tested and base lined source code



# **System Testing Phase**

Entry	Completion of Integration Testing
	Integration tested and baselined source code
	Reviewed and approved STP and STRs
	Defect Tracking Form, Defect Consolidation Log
Inputs	Tollgate Review Report
	Perform System Testing
	Revisit STP (Refer System Test Plan template), STR (Refer System Test Report
	template)
	Update Project Charter (Refer Project Charter template)
	Update Traceability Matrix (Refer Traceability Matrix template)
Tasks	Conduct Tollgate review (Refer Tollgate Review Checklist for Testing)
Verification / Validation	System Test Reports (STRs) against
	System Test Reports (STRs)
	Test Results
	Defect Consolidation Log (Refer Issue Log template)
	Updated Traceability Matrix
	Tollgate Review Report
Outputs	Updated Risk Assessment Sheet
Exit	Tested and Approved System (all functional blocks / modules)



# **Acceptance Testing Phase**

Entry	System Tested Software
	Reviewed, approved and baselined URD, ATP and ATR
Inputs	Tollgate Review Report
	Setup UAT environment
	Update Project Charter
	Conduct Tollgate review
Tasks	Deliver Software
Verification / Validation	Validation of product against URD and acceptance criteria
	Baselined version of product with the user documentation
	Test Results
	Additional outputs for six sigma projects:
	Updated Project Charter
Outputs	Tollgate Review Report
Exit	Acceptance by Customer



# **Session Recap**

