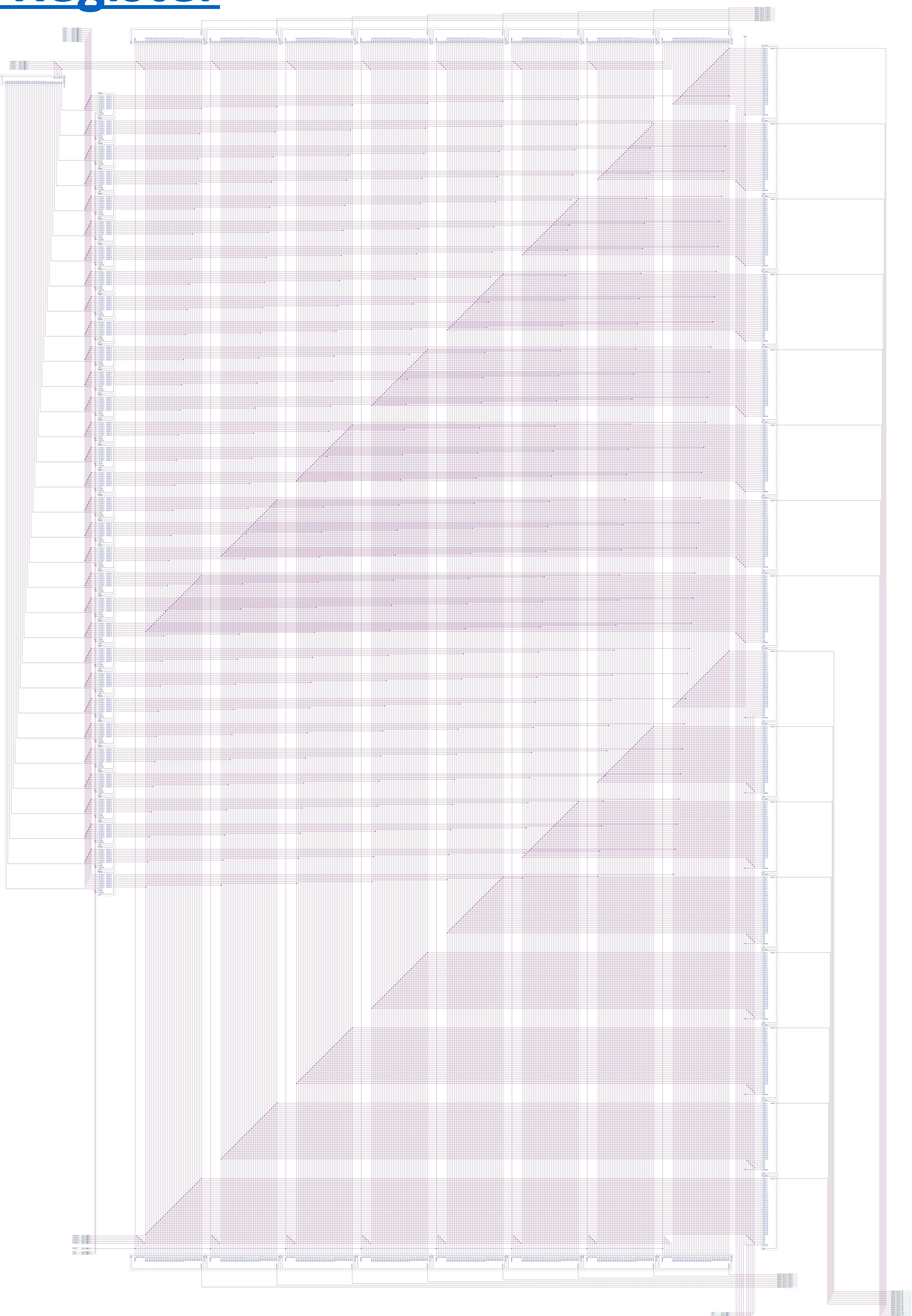


BACK TO AVR
TOP LEVEL

5:32 DECODER

8 Bit Register

32:1 MUX



32:1 MUX

32:1 MUX